Product Manual

Fibre Channel Interface
### Revision status summary sheet

<table>
<thead>
<tr>
<th>Revision</th>
<th>Date</th>
<th>Writer/Engineer</th>
<th>Sheets Affected</th>
</tr>
</thead>
<tbody>
<tr>
<td>A</td>
<td>03/08/06</td>
<td>C. Chalupa/J. Coomes</td>
<td>All</td>
</tr>
</tbody>
</table>
# Contents

1.0  Contents ................................................................. i

2.0  Publication overview .................................................. 1
2.1  Acknowledgements .................................................... 1
2.2  How to use this manual .............................................. 1
2.3  General interface description ..................................... 2

3.0  Introduction to Fibre Channel ..................................... 3
3.1  General information .................................................. 3
3.2  Channels vs. networks .............................................. 4
3.3  The advantages of Fibre Channel ................................. 4

4.0  Fibre Channel standards ............................................ 5
4.1  General information .................................................. 6
   4.1.1  Description of Fibre Channel levels ....................... 6
      4.1.1.1  FC-0 ............................................... 6
      4.1.1.2  FC-1 ............................................... 6
      4.1.1.3  FC-1.5 ........................................... 6
      4.1.1.4  FC-2 ............................................... 6
      4.1.1.5  FC-3 ............................................... 6
      4.1.1.6  FC-4 ............................................... 7
   4.1.2  Relationship between the levels ............................ 7
   4.1.3  Topology standards .......................................... 7
   4.1.4  FC Implementation Guide (FC-IG) ......................... 7
   4.1.5  Applicable Documents ..................................... 7

5.0  Introduction to topologies ......................................... 19
5.1  Nodes .................................................................. 19
5.2  Ports .................................................................. 19
5.3  Links .................................................................. 20
5.4  Arbitrated loop topology ........................................ 21
5.5  Topology and port login .......................................... 22
5.6  Port bypass circuits .............................................. 22

6.0  Data encoding (FC-1) .............................................. 23
6.1  Encoding and decoding ............................................. 23
6.2  Buffer-to-buffer data transfers .................................. 24
6.3  Data hierarchy ....................................................... 25
    6.3.1  Transmission words ....................................... 25
    6.3.1.1  Data characters ................................... 25
    6.3.1.2  Special characters ................................ 25
   6.4  Ordered sets ...................................................... 25
    6.4.1  Primitive signals ........................................ 25
    6.4.1.1  Primitive signals used as frame delimiters ....... 26
    6.4.1.2  Primitive signals custom made for arbitrated loop topologies ........................................... 28
    6.4.2  Primitive sequences ..................................... 30
<table>
<thead>
<tr>
<th>Section</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>7.0</td>
<td>Framing protocol (FC-2)</td>
</tr>
<tr>
<td>7.1 Frames</td>
<td>33</td>
</tr>
<tr>
<td>7.1.1 Frame structure</td>
<td>33</td>
</tr>
<tr>
<td>7.1.1.1 Start-of-frame (SOF) delimiter</td>
<td>34</td>
</tr>
<tr>
<td>7.1.1.2 Frame header</td>
<td>34</td>
</tr>
<tr>
<td>7.1.1.3 Data field (payload)</td>
<td>39</td>
</tr>
<tr>
<td>7.1.1.4 CRC field</td>
<td>39</td>
</tr>
<tr>
<td>7.1.1.5 End-of-frame (EOF) delimiter</td>
<td>40</td>
</tr>
<tr>
<td>7.1.2 Frame Field Order</td>
<td>40</td>
</tr>
<tr>
<td>7.2 Frame sequences</td>
<td>41</td>
</tr>
<tr>
<td>7.3 Exchanges</td>
<td>41</td>
</tr>
<tr>
<td>7.4 Credit</td>
<td>42</td>
</tr>
<tr>
<td>8.0 Classes of service (FC-2)</td>
<td>43</td>
</tr>
<tr>
<td>8.1 Class 1</td>
<td>43</td>
</tr>
<tr>
<td>8.2 Class 2</td>
<td>43</td>
</tr>
<tr>
<td>8.3 Class 3</td>
<td>43</td>
</tr>
<tr>
<td>8.3.1 Class 3 flow control</td>
<td>44</td>
</tr>
<tr>
<td>8.4 Classes 4 and 5</td>
<td>44</td>
</tr>
<tr>
<td>9.0 FC Arbitrated Loop concepts</td>
<td>45</td>
</tr>
<tr>
<td>9.1 Arbitrated Loop physical address (AL_PA)</td>
<td>45</td>
</tr>
<tr>
<td>9.2 Loop initialization</td>
<td>48</td>
</tr>
<tr>
<td>9.2.1 Loop initialization state machine</td>
<td>58</td>
</tr>
<tr>
<td>9.2.2 Loop reinitialization</td>
<td>59</td>
</tr>
<tr>
<td>9.3 Accessing another L_Port</td>
<td>59</td>
</tr>
<tr>
<td>9.3.1 Access fairness</td>
<td>60</td>
</tr>
<tr>
<td>9.3.2 Access unfairness</td>
<td>61</td>
</tr>
<tr>
<td>9.3.3 Clock skew management</td>
<td>61</td>
</tr>
<tr>
<td>9.4 Loop ports</td>
<td>61</td>
</tr>
<tr>
<td>9.4.1 Maximum number of NL_Ports</td>
<td>61</td>
</tr>
<tr>
<td>9.4.2 Blocking switch emulation</td>
<td>62</td>
</tr>
<tr>
<td>9.4.3 Non-meshed environment</td>
<td>62</td>
</tr>
<tr>
<td>9.4.4 Assigned AL_PA values</td>
<td>63</td>
</tr>
<tr>
<td>10.0 Fibre Channel link services</td>
<td>65</td>
</tr>
<tr>
<td>10.1 Basic link services</td>
<td>66</td>
</tr>
<tr>
<td>10.1.1 Abort Sequence (ABTS)</td>
<td>67</td>
</tr>
<tr>
<td>10.1.2 Basic Accept (BA_ACC)</td>
<td>68</td>
</tr>
<tr>
<td>10.1.3 Basic Reject (BA_RJT)</td>
<td>69</td>
</tr>
<tr>
<td>10.2 Extended link services</td>
<td>70</td>
</tr>
<tr>
<td>10.2.1 Port Login (PLOGI) (02x)</td>
<td>72</td>
</tr>
<tr>
<td>10.2.2 Port Logout (PLOGO) (03x)</td>
<td>81</td>
</tr>
<tr>
<td>10.2.3 Fabric Login (FLOGI) (04)</td>
<td>82</td>
</tr>
<tr>
<td>10.2.4 Process Login (PRLI)</td>
<td>87</td>
</tr>
<tr>
<td>10.2.5 Process Logout (PRLO)</td>
<td>91</td>
</tr>
<tr>
<td>10.2.6 Third Party Process Logout (TPRLO)</td>
<td>95</td>
</tr>
<tr>
<td>10.2.7 Read Link Error Status Block (RLS)</td>
<td>99</td>
</tr>
<tr>
<td>10.2.8 Reinstate Recovery Qualifier (RRQ)</td>
<td>102</td>
</tr>
<tr>
<td>10.2.9 Port Discovery (PDISC)</td>
<td>103</td>
</tr>
<tr>
<td>10.2.10 Discover Address (ADISC)</td>
<td>103</td>
</tr>
<tr>
<td>10.2.11 Report Node Capabilities (RNC)</td>
<td>106</td>
</tr>
<tr>
<td>10.2.12 Link Service Reject (LS_RJT)</td>
<td>108</td>
</tr>
<tr>
<td>10.3 FC common transport</td>
<td>109</td>
</tr>
<tr>
<td>10.3.1 Register FC-4 Types Name Service (RFT_ID)</td>
<td>111</td>
</tr>
</tbody>
</table>
### 11.0 Enclosure services interface (ESI) ................................................................. 113
- 11.1 Discovery process .......................................................... 113
- 11.2 8045 mode ................................................................. 115
  - 11.2.1 8045 ESI pinouts ....................................................... 115
- 11.3 8067 mode ................................................................. 116
  - 11.3.1 8067 ESI command .................................................. 116
  - 11.3.2 8067 ESI interface pinouts ....................................... 117
  - 11.3.3 8067 information format ........................................ 117
- 11.4 ESI command transfer .................................................... 118
  - 11.4.1 ESI read transfer .................................................. 118
  - 11.4.2 ESI write transfer ................................................ 118
- 11.5 Enclosure-initiated ESI transfer ........................................ 119
  - 11.5.1 EIE Discovery ...................................................... 119
  - 11.5.2 EIE operations ..................................................... 121
  - 11.5.3 Enclosure requested information ................................ 122
    - 11.5.3.1 Device Standard Inquiry Data page ....................... 124
    - 11.5.3.2 Device Address page ...................................... 125
    - 11.5.3.3 Loop Position Map page .................................. 126
    - 11.5.3.4 Device Identification page .............................. 127
    - 11.5.3.5 Device Temperature page ................................ 128
    - 11.5.3.6 Port Parameters page ..................................... 129
    - 11.5.3.7 Link Status page .......................................... 130
    - 11.5.3.8 Spin-Down Control Action Specific Bits .................. 133
    - 11.5.3.9 ESI data validation ...................................... 134

### 12.0 SCSI operations ............................................................................. 137
- 12.1 SCSI-FCP ................................................................. 137
  - 12.1.1 FC-4 mapping layer ................................................ 137
- 12.2 FCP CMND ................................................................. 138
  - 12.2.1 Command Descriptor Block (CDB) .............................. 144
    - 12.2.1.1 Operation Code .............................................. 144
    - 12.2.1.2 Logical block address ..................................... 148
    - 12.2.1.3 Operation code ............................................. 149
    - 12.2.1.4 Relative address bit ....................................... 149
    - 12.2.1.5 Transfer length .............................................. 149
    - 12.2.1.6 Control byte ................................................ 149
- 12.3 FCP XFER RDY ............................................................ 150
- 12.4 FCP DATA ................................................................. 153
- 12.5 FCP RSP ................................................................. 156
  - 12.5.1 Extended Sense Data format ................................... 162
    - 12.5.1.1 Sense Key Specific Valid (SKSV) and Sense Key Specific ........................... 164
  - 12.5.2 Current and deferred errors ................................... 166
- 12.6 Parameter rounding .................................................................. 167

### 13.0 Drive features .............................................................................. 169
- 13.1 Self-Monitoring Analysis and Reporting Technology .................... 169
- 13.2 Self-test operations ............................................................ 169
  - 13.2.1 Default self-test ..................................................... 169
  - 13.2.2 Short and extended device self-tests ............................ 169
  - 13.2.3 Device self-test modes .......................................... 170
    - 13.2.3.1 Foreground mode ............................................ 170
    - 13.2.3.2 Background mode .......................................... 170
    - 13.2.3.3 Elements common to foreground and background self-test modes .................. 171

### 14.0 Seagate Technology support services ........................................... 173

### Glossary ......................................................................................... 179
List of Tables

Table 1. Fibre Channel port types .......................................................... 20
Table 2. Running disparity examples ....................................................... 24
Table 3. Primitive signals ..................................................................... 26
Table 4. Frame delimiters .................................................................... 27
Table 5. FC-AL primitive signals .......................................................... 28
Table 6. Primitive sequences ................................................................. 30
Table 7. FC-AL primitive sequences ..................................................... 31
Table 8. Frame Header ......................................................................... 34
Table 9. Routing Control values ........................................................... 36
Table 10. Data type codes ..................................................................... 37
Table 11. Frame Control (F_CTL) bit definitions ................................. 38
Table 12. Frame Byte Order .................................................................. 40
Table 13. 8B/10B characters with neutral disparity .............................. 45
Table 14. AL_PA value priorities ............................................................ 47
Table 15. AL_PA addressing ................................................................. 47
Table 16. Loop Initialization Primitive (LIP) sequences ...................... 50
Table 17. Loop initialization sequence AL_PA bit map ....................... 55
Table 18. AL_PA mapped to bit maps .................................................... 63
Table 19. Basic link services header ...................................................... 66
Table 20. BA_ACC Payload ................................................................. 68
Table 21. BA_RJT Payload ................................................................. 69
Table 22. Extended link services header .............................................. 70
Table 23. PLOGI Payload ................................................................. 72
Table 24. Port/Node Name format ......................................................... 73
Table 25. N_Port Common Service Parameters ................................. 74
Table 26. Common Features bits ......................................................... 75
Table 27. Class Service Parameters ..................................................... 76
Table 28. Initiator Control fields .......................................................... 77
Table 29. Recipient Control fields ....................................................... 77
Table 30. Port Login Accept Payload (PLOGI ACC) ............................ 79
Table 31. LOGO Payload ................................................................. 81
Table 32. PLOGO Accept ................................................................. 81
Table 33. FLOGI Payload ................................................................. 82
Table 34. F_Port Common Service Parameters ................................. 83
Table 35. Common Features bits .......................................................... 84
Table 36. Class 3 Service Parameters .................................................. 84
Table 37. Service Option Class 3 fields ............................................... 85
Table 38. Fabric Login Accept Payload (FLOGI ACC) ....................... 85
Table 39. PRLI Payload ................................................................. 87
Table 40. PRLI Accept Payload ......................................................... 89
Table 41. PRLO Payload ................................................................. 91
Table 42. PRLO Accept Payload ......................................................... 93
Table 43. TPRLO Payload ............................................................... 95
Table 44. TPRLO Accept Payload ...................................................... 97
Table 45. RLS Payload ................................................................. 99

Fibre Channel Interface Manual, Rev. A
Table 93. Sense Key Specific reference tables ................................................................. 164
Table 94. Field Pointer bytes ........................................................................................... 164
Table 95. Actual Retry Count bytes .................................................................................. 164
Table 96. Format Indication bytes .................................................................................... 165
Table 97. Applicable disc drive sense keys ........................................................................ 165
Table 98. Device Self-test mode summary .......................................................................... 172
| Figure 1. | Fibre Channel Standards | 5 |
| Figure 2. | Arbitrated loop topology (dual port private loop) | 21 |
| Figure 3. | Port bypass circuit physical interconnect | 22 |
| Figure 4. | Decimal value translation | 23 |
| Figure 5. | Serialization process | 24 |
| Figure 6. | FC data hierarchy | 25 |
| Figure 7. | Relationship between frames, sequences, and exchanges | 33 |
| Figure 8. | Frame structure | 33 |
| Figure 9. | FC–SCSI exchanges, command and response transfers | 42 |
| Figure 10. | Loop initialization sequences | 49 |
| Figure 11. | Loop initialization state machine | 58 |
| Figure 12. | Loop state machine (simplified) | 59 |
| Figure 13. | Discovery process flow diagram | 114 |
| Figure 14. | ESI transfer phases | 116 |
| Figure 15. | ESI command transfers | 118 |
| Figure 16. | ESI reads | 118 |
| Figure 17. | ESI writes | 119 |
| Figure 18. | Enclosure Initiated ESI Request | 120 |
| Figure 19. | Prepare for Removal | 121 |
| Figure 20. | EIE Operation Phases | 122 |
1.0 Publication overview

This publication provides some general information about Fibre Channel as well as detailed information about how Seagate disc drives implement Fibre Channel Arbitrated Loop technology.

This publication will continue to be revised as Fibre Channel technology advances and as Seagate Fibre Channel drives change to meet data storage needs.

You will observe that many references are made to SCSI throughout this publication. This is because Fibre Channel transports the SCSI command set. This concept is discussed in more detail throughout this publication beginning in Chapter 2. For details about each SCSI command, refer to the Seagate SCSI Command Reference Manual, part number 100293068.

1.1 Acknowledgements

The information contained in this publication was gathered from many sources. Portions of the text used to explain general Fibre Channel concepts were adapted in various forms, with permission, from Ancot Corporation's Fibre Channel, Volume 1: The Basics written by Gary R. Stephens and Jan V. Dedek. Additional information was contributed by Canadian Valley Vocational-Technical instructor Chuck Chalupa.

1.2 How to use this manual

This publication provides a universal detailed description of the Fibre Channel interface for Seagate disc drives. You may read it from front-to-back, or turn directly to the sections that interest you the most. A glossary is provided in the back (see Appendix A) which you may find useful as you read this manual.

Note. Each Seagate drive family has a product manual which have tables that specify which SCSI features are implemented in each specific drive model, what the default parameters are for the various features they implement, and which parameters are changeable and which are not.

No method exists at present to inform an initiator if a target supports SCSI-3 features as opposed to only SCSI-2 features. A few SCSI-3 features are supported by Seagate drives, but no attempt has been made herein to differentiate between SCSI-2 and SCSI-3 features. Therefore, when an Inquiry command reports what the ANSI-approved version of the drive is, it reports SCSI-2, where SCSI-2 means SCSI-2 features plus some SCSI-3 features.

No attempt is made in this universal specification to specify which descriptions or tables apply to SCSI-2 or SCSI-3. The combination of this general specification with the details in the individual drive’s product manual, provides a description of the individual drive implementation of the SCSI interface.

This interface manual is not intended to be a stand-alone publication about Fibre Channel’s features. You should reference the individual drive’s product manual to determine the specific features supported by each drive model and the Seagate SCSI Command Reference Manual, part number 100293068 for command-related information.
1.3 General interface description

This manual describes the Seagate Technology LLC Fibre Channel/SCSI (Small Computer Systems Interface) as implemented on Seagate Fibre Channel (FC) disc drives.

The disc drives covered by this manual are classified as intelligent peripherals.

The interface supports multiple initiators, self-configuring host software, automatic features that relieve the host from the necessity of knowing the physical architecture of the target (logical block addressing is used), and some other miscellaneous features.

The physical interface uses differential drivers and receivers for the Fibre Channel serial connections. The single channel transfer rate is 106 MB/second (commonly called 1 gigabit/sec) or 212 MB/second (commonly called 2 gigabit/sec). See the drive’s product manual for a definition of the electrical characteristics of the interface.
2.0 Introduction to Fibre Channel

Fibre Channel is an American National Standards Institute (ANSI) interface that acts as a general transport vehicle to simultaneously deliver the command sets of several existing interface protocols including SCSI-3, IPI-3, HIPPI-FP, IP, and ATM/AAL5. Proprietary and other command sets may also use and share the Fibre Channel, but these are not yet defined as part of the Fibre Channel standard.

Fibre Channel Arbitrated Loop (FC-AL) is one topology used to connect two or more devices within the guidelines set by the ANSI standards. This topology is discussed in detail throughout this manual. Other topologies do exist and are discussed briefly in this manual to give you some idea of how these topologies can coexist and interact.

2.1 General information

Fibre Channel supports both large and small data transfers. This makes it effective in transferring a wide variety of data and can be used in systems ranging from supercomputers to individual workstations. Fibre Channel peripherals can include devices such as, but not limited to, disc drives, tape units, high-bandwidth graphics terminals, and laser printers.

To accommodate all of these device types with various command sets, Fibre Channel separates the physical I/O interface from the I/O operations. This makes it possible to use the multiple command sets simultaneously. This also allows new speeds and new functions to be added without making all previous investment in existing components obsolete.

Another benefit of Fibre Channel is that it supports both channel and network peripheral protocols for device communication. This means that channel and network protocols can share the same physical medium.

Fibre Channel does not have its own native I/O command set protocol. It simply lets other protocols superimpose their command sets onto itself and then transports this information. Fibre Channel has a command set that it uses to manage the links between the various participating devices using Fibre Channel. Fibre Channel calls these link level functions “link services.”

Since multiple command sets may use Fibre Channel, it identifies the information by command set type. This allows the receiving port to distinguish among the protocols and make processing decisions. Each Fibre Channel frame has a field in the frame header to identify the protocol associated with that frame. Additional information about frames is available in Section 6.0 beginning on page 33.
2.2 Channels vs. networks

As mentioned above, Fibre Channel supports both channel and network communications.

Channels

Traditional disc drive communications occur in a channel environment where the host controls the devices attached to it. The primary requirement for channel environments is to provide error-free delivery, with transfer delays being a secondary consideration.

Networks

Networks allow many devices to communicate with each other at will. This is usually accompanied by software support to route transactions to the correct provider and to verify access permission. Networks are used for transferring data with “error-free delivery” and voice and video where “delivery on time” is the primary requirement with error-free delivery being a secondary consideration. For example, when transferring video, it is more important to provide on-time delivery of data to prevent loss of video frames than to lose one or two pixels in a video frame.

2.3 The advantages of Fibre Channel

In addition to the channel/network support, Fibre Channel:

- Supports multiple physical interface types.
- Provides a means to interconnect physical interface types.
- Provides high-speed data transfer rates much faster than parallel SCSI.
- Separates the logical protocol being transported from the physical interface—this allows multiple protocols to be transported over a common physical interface.
- Allows increased cable lengths. You can have 30 meters between each device when using twisted pair copper media. Note that this 30 meters is between each device, not the total length (multiply the number of devices by 30m to obtain total allowable length). Fiber optic media extended this even further by allowing distances of 10km between each device.
- Increases the potential number of devices connected. Just one private arbitrated loop can have up to 125 devices attached. Even more can be attached in non-participating mode. Also, multiple loops can be attached to fabrics to significantly increase the number of devices attached.
- Uses asynchronous transmission to fully utilize the available bandwidth.
- Allows flexibility in transfer rates, distances, media types, and protocols.
3.0 Fibre Channel standards

Figure 1 shows the various documents involved in the ANSI set of standards relating to Fibre Channel. This model is not static—it is growing as others areas of interest are developed. Fibre Channel standards

Figure 1. Fibre Channel Standards

The interface is compatible with a subset of the ANSI standards listed below:
- SCSI-2 Standard and the Common Command Set (CCS) document, Revision 4.B
- SCSI Parallel Interface-3 (SPI-3)
- SCSI Enclosure Services (SES) Command Set, X3T10 NCITS, 305-199x
- Fibre Channel Physical and Signaling, Revision 4.3 (FC-PH)
- Fibre Channel Physical and Signaling, Revision 7.4 (FC-PH-2)
- Fibre Channel Physical and Signaling, Revision 9.4 (FC-PH-3)
- SCSI Fibre Channel Protocol, Revision 12 (SCSI-FCP)
- Fibre Channel Arbitrated Loop, Revision 4.5 (FC-AL)
- Fibre Channel Arbitrated Loop Direct Attach SCSI Technical Report
- Fibre Channel Fabric Loop Attach Technical Report
3.1 General information

The FC-PH standard is the foundation upon which all others are based. Each topology, command set, and protocol has its own standard. These are all separate to allow future growth and to allow designers to more easily use only those parts that affect their products. It is important for system designers to consider the requirements of the set of protocols to be supported because different protocols require different subsets of the functions permitted in a Fibre Channel port.

3.1.1 Description of Fibre Channel levels

The Fibre Channel levels are listed below:

- Physical (FC-0)
- 8B/10B encoding/decoding (FC-1)
- FC-AL (FC-1.5)
- Framing protocol (FC-2)
- Common services (FC-3)
- Interface mapping (FC-4)

3.1.1.1 FC-0

FC-0 defines the physical portions of the Fibre Channel. This includes the fibre, connectors, and optical and electrical parameters for a variety of data rates and physical media. Coax, twinax, and twisted pair versions are defined for limited distance applications. FC-0 also provides the point-to-point physical portion of the Fibre Channel.

3.1.1.2 FC-1

FC-1 defines the transmission protocol which includes the serial encoding, decoding, and error control.

3.1.1.3 FC-1.5

FC-1.5 defines the topology involved with Fibre Channel Arbitrated Loop (FC-AL) configurations.

3.1.1.4 FC-2

FC-2 defines the signaling protocol which includes the frame structure and byte sequences.

3.1.1.5 FC-3

FC-3 defines a set of services which are common across multiple ports of a node.
3.1.1.6 FC-4

FC-4 defines the interface mapping between the lower levels of the Fibre Channel and the various command sets. These various command sets are known as upper layer protocols (ULPs). Examples of upper layer protocols include SCSI, IPI, HIPPI, and IP.

3.1.2 Relationship between the levels

FC-0, FC-1, and FC-2 are integrated into the FC-PH document. The other documents are separate so that each implementation may use the technology best suited to the environment in which it will be used.

3.1.3 Topology standards

Each topology has its own standard. This is done so that designers can concentrate on documents that apply to the technology suited to their specific area of interest.

The following topology standards are available:

- FC-FG (Fibre Channel Fabric Generic)
- FC-SW (Fibre Channel Cross-point switch)
- FC-AL (Fibre Channel Arbitrated Loop)

The FC-FG and FC-AL documents are of the most interest for Seagate disc drive interconnection.

3.1.4 FC Implementation Guide (FC-IG)

FC-IG provides some implementation guidance for all Fibre Channel systems.

3.1.5 Applicable Documents

The following ANSI standards should be referenced for more details about Fibre Channel and SCSI.

- Fibre Channel Protocol for SCSI, Third Version (FCP-3), T10/1560-D
- Fibre Channel Link Services (FC-LSr1.2), T11/1620-D/Rev1.2
- Fibre Channel Framing and Signaling - 2 (FC-FS-2), T11/1619-D
- SFF-8067 Specification for 40-pin SCA-2 Connector w/Bidirectional ESI Rev 3.4
- SCSI Block Commands - 3 (SBC-3), T10/1215-D
- SCSI Primary Commands - 4 (SPC-4), T10/1731-D
- Enclosure Services - 2 (SES-2), T10/1559-D
4.0 Introduction to topologies

Topologies include all the elements necessary to successfully connect two or more nodes (also known as devices). See Section 4.1. There are several topologies available with Fibre Channel, but all of them have certain common components: nodes, ports, and links. These components are discussed in this section.

The ANSI Fibre Channel standard defines three topologies:
1. Arbitrated loop (Fibre Channel Arbitrated Loop, FC-AL)
2. Fabric
3. Point-to-point

Seagate supports arbitrated loop and fabric as the primary topologies for disc drive connections.

Note. Some brief discussions about items not directly associated with arbitrated loop and fabric topologies are included to make you aware that other topologies exist within the constructs of the ANSI Fibre Channel standard.

The fabric topology permits dynamic interconnections between nodes through ports connected to a fabric. This fabric is similar to a switch or router and is often compared to a telephone system because of its redundant rerouting capabilities. The fabric topology also allows multiple connections simultaneously, unlike FC-AL which results in a single circuit being established between only two ports at any one particular time. Fabric and arbitrated loop topologies may be combined in one system to provide a wide variety of services and performance levels to the nodes.

Point-to-point topologies are used only to connect two ports without any routing capabilities.

4.1 Nodes

Fibre Channel devices are called nodes. This is a generic term describing any device (workstation, printer, disc drive, scanner, etc.) connected to a Fibre Channel topology. Each node has at least one port, called an N_Port to provide access to other nodes. The “N” in N_Port stands for node. As you will see later, ports used in a Fibre Channel Arbitrated Loop topology are called NL_Ports where the “NL” stands for node loop.

The components that connect two or more node ports together are what are collectively called a topology. Nodes work within the provided topology to communicate with all other nodes.

4.2 Ports

Ports are the link to the outside world for a Fibre Channel node. See Figure 2. As stated above, each node has at least one port to provide access to other nodes. Each Seagate Fibre Channel drive has two ports.

Each port uses a pair of fibers—one to carry information into the port and one to carry information out of the port. This pair of fibers (actually copper wire) is called a “link” and is part of each topology. The Fibre Channel ANSI specification also supports fibers made of optical strands as a medium for data transfer.
As stated above, ports used in a FC-AL topology are called node loop ports (NL_Ports). Other port types exist as documented in the following table.

**Table 1: Fibre Channel port types**

<table>
<thead>
<tr>
<th>Port type</th>
<th>Location</th>
<th>Associated topology</th>
</tr>
</thead>
<tbody>
<tr>
<td>N_Port</td>
<td>Node</td>
<td>Point-to-point or Fabric</td>
</tr>
<tr>
<td>NL_Port</td>
<td>Node</td>
<td>in N_Port mode—Point-to-point or Fabric</td>
</tr>
<tr>
<td></td>
<td></td>
<td>in NL_Port mode—Arbitrated Loop</td>
</tr>
<tr>
<td>F_PORT</td>
<td>Fabric</td>
<td>Fabric</td>
</tr>
<tr>
<td>FL_Port</td>
<td>Fabric</td>
<td>in F_Port mode—Fabric</td>
</tr>
<tr>
<td></td>
<td></td>
<td>in FL_Port mode—Arbitrated Loop</td>
</tr>
<tr>
<td>E_Port</td>
<td>Fabric</td>
<td>Internal Fabric Expansion</td>
</tr>
<tr>
<td>G_Port</td>
<td>Fabric</td>
<td>in F_Port mode—Fabric</td>
</tr>
<tr>
<td></td>
<td></td>
<td>in E_Port mode—Internal fabric expansion</td>
</tr>
<tr>
<td>GL_Port</td>
<td>Fabric</td>
<td>in F_Port mode—Fabric</td>
</tr>
<tr>
<td></td>
<td></td>
<td>in FL_Port mode—Arbitrated Loop</td>
</tr>
<tr>
<td></td>
<td></td>
<td>in E_Port mode—Internal fabric expansion</td>
</tr>
</tbody>
</table>

NL_Ports and FL_Ports discover their mode of operation dynamically during the initialization procedure.

**Note.** You may hear the term “L_Port” when discussing Fibre Channel. This term is often used as a “catch-all” term meaning NL_Port or FL_Port when it is not important to specifically distinguish between the two. Therefore, when you read the term L_Port, you should think of NL_Port when the discussion is about Arbitrated Loop or Point-to-Point topologies, and you should think of FL_Port when the discussion is about the Fabric topology.

An L_Port discovers its environment and works properly, without outside intervention, with an F_Port, N_Port, or with other L_Ports.

Arbitrated loops can have up to 126 active NL_Ports, but only one active FL_Port attached to the same arbitrated loop. The NL_Ports discover whether there is a fabric present by discovering whether there is an FL_Port present. If there isn’t an FL_Port present, the loop is called a private loop. If there is an FL_Port present, the loop is called a public loop and the FL_Port is considered the manager of the loop.

The most commonly used ports are the NL_Port and FL_Port. With these two types, you can easily migrate nodes from one topology to another.

Each NL_Port is attached to one link. Information flows in one direction only.

### 4.3 Links

Each port is comprised of two fibers, one carries information into the port and is called a receiver. The other carries information out of the port and is appropriately called a transmitter. Fibre Channel supports two types of fibers—electrical wires (most commonly copper) and optical strands. This pair of wires is called a link. See Figure 2.

Links carry the data frames between nodes. Each link can handle multiple frame types; therefore, frame multiplexing is possible. For example, a frame containing SCSI information may be followed by a frame containing TCP/IP followed by a frame containing yet another protocol’s information.
4.4 Arbitrated loop topology

Fibre Channel Arbitrated Loops (FC-AL) attach multiple nodes in a loop without hubs and switches. The node ports use arbitration to establish a point-to-point circuit. FC-AL is a distributed topology where each L_Port includes the minimum necessary function to establish the circuit.

The arbitrated loop topology is used to connect from two to 126 node ports. See Figure 2.

![Arbitrated Loop Topology Diagram](image)

**Figure 2. Arbitrated loop topology (dual port private loop)**

The ports in an arbitrated loop topology are called NL_Ports (Node Loop Ports). Each of the NL_Ports has an input and output connection. Seagate Fibre Channel drives support dual ports (specified with a “1” in byte 6, bit 4 of the disc drive inquiry data). The actual ports are located on the host’s backpanel, not on the disc drive. Dual ports are provided for redundancy so that if one loop fails, the other one can fulfill the loop duties. Each drive has one FC SCA (single connector attachment) connector.

The arbitrated loop protocol is a token acquisition protocol. This means each port on the same arbitrated loop sees all messages, but passes all messages that are addressed to other ports.
4.5 Topology and port login

Each NL_Port must sign in with the other ports on the loop. Each port first attempts to locate an FL_Port. If it finds an FL_Port, it knows it is a part of a public loop connected to a fabric. If the port does not locate an FL_Port, it knows it is a part of a private loop with other NL_Ports only.

There can be up to 126 active NL_Ports and up to one active FL_Port attached to the same arbitrated loop. If an NL_Port does not attempt to discover an FL_Port and there is an FL_Port present, the NL_Port is only allowed to access other NL_Ports on the same loop.

4.6 Port bypass circuits

So far, we've discussed links, ports, nodes, and topology logins. All's fine with this model as long as the loop is intact. What happens, then, if a device is disconnected from the loop. Doesn't this break the loop? The answer is no. Fibre Channel provides port bypass circuitry which bypasses the drive if it is removed, incapable of providing loop services, unable to obtain a valid address, or otherwise incapable of providing loop services. These port bypass circuits (PBCs) are located external to the drive or other FC-AL device. Figure 3 shows the relationship between the PBC and drive.

![Figure 3. Port bypass circuit physical interconnect](image-url)
5.0 Data encoding (FC-1)

Fibre Channel devices don’t transmit 8-bit bytes. If this were to occur, the receiving node would not understand the transmitter’s intentions. To fix this situation, the data is encoded prior to transmission. Encoding allows the creation of special transmission code characters with unique bit patterns for data management and word alignment so the receiving node will know what to do with the bytes. Encoding also improves the transmission characteristics across a fibre and increases the likelihood of detecting errors.

5.1 Encoding and decoding

An unencoded data byte is represented in FC-1 as HGFEDCBA where H is the most significant bit. The hex to FC-1 decimal value translation is accomplished as shown below:

![Decimal value translation diagram]

A decimal value is assigned to each bit combination with the range of 0 to 31 for xx and 0 to 7 for y. This means the range of valid data characters using the FC-1 naming convention is D00.0 through D31.7.

FC serial transmission delivers 10-bit characters which represent encoded data. Of the 1,024 characters possible with the 10-bit space, 256 8-bit byte data characters are mapped, along with 1 control character. This mapping process is called 8B/10B encoding. This encoding method involves selecting encoded 10-bit characters to maintain a run-length-limited serial stream of bits. To prevent too many ones or zeros on the serial interface from causing a DC electrical shift of the serial media, the encoder monitors the number of ones in the encoded character and selects the option of the 10-bit encode character that will shift to balance the total number of zeros and ones. This balancing is called running disparity.

A 10-bit character is actually made up of 6- and 4-bit sub-blocks. The 6-bit sub-block shifts out first followed by the 4-bit sub-block. Running disparity is set positive at the end of the sub-block as follows:
If the number of ones in a sub-block is greater than the number of zeros, the 6-bit sub-block is 000111b, or the 4-bit sub-block is 0011b. Running disparity is set positive at the end of the sub-block.

If the number of zeros in a sub-block is greater than the number of ones, the 6-bit sub-block is 111000b, or the 4-bit sub-block is 1100b. Running disparity is set negative at the end of the sub-block.

If the number of ones and zeros in a sub-block are equal, running disparity is neutral and the value of running disparity at the end of the sub-block remains the same as the preceding character even if it is separated by neutral characters.

Note. The rules of running disparity prohibit consecutive positive or consecutive negative characters even if they are separated by neutral disparity characters. In other words, the negative and positive disparity characters must alternate, even if separated by a neutral disparity character. See Table 2.

### Table 2: Running disparity examples

<table>
<thead>
<tr>
<th>Sub-blocks</th>
<th>Valid?</th>
</tr>
</thead>
<tbody>
<tr>
<td>+ N – + –</td>
<td>Yes</td>
</tr>
<tr>
<td>+ N +</td>
<td>No</td>
</tr>
<tr>
<td>+ +</td>
<td>No</td>
</tr>
<tr>
<td>– –</td>
<td>No</td>
</tr>
</tbody>
</table>

5.2 Buffer-to-buffer data transfers

Fibre Channel devices transfer information from an output buffer in the transmitting node to an input buffer of the receiving node. This is called a buffer-to-buffer transfer. Each node may have from 1 to n buffers. The number of buffers in each node does not have to be equal. Each buffer is the size a frame may transfer in its payload. The Fibre Channel standard does not define the actual length of the buffer or the method used to store the bytes in the buffer. Figure 5 shows how data is sent from the transmit buffer and received by the receive buffer.

![Serialization process](image)

**Figure 5. Serialization process**

The bytes being transmitted are sent in increasing displacement.1

The basic unit of transfer for the contents of a buffer-to-buffer data transfer is the frame.

---

1. This means that if byte 0 is sent first, bytes 1, 2, 3, and 4 are then transmitted in that order. If byte 100 is sent first, bytes 101, 102, 103, and 104 are then transmitted in that order.
5.3 **Data hierarchy**

A hierarchy of data types is presented in Figure 6.

**Figure 6. FC data hierarchy**

<table>
<thead>
<tr>
<th>Transmission word (40 bits — 4 10-bit encoded bytes)</th>
</tr>
</thead>
<tbody>
<tr>
<td>8B/10B encoded byte 1 (10 bits)</td>
</tr>
<tr>
<td>Byte (8 bits)</td>
</tr>
<tr>
<td>Bit Bit Bit Bit Bit Bit Bit Bit Bit Bit Bit Bit Bit Bit</td>
</tr>
<tr>
<td>...</td>
</tr>
<tr>
<td>8B/10B encoded byte 4 (10 bits)</td>
</tr>
<tr>
<td>Byte (8 bits)</td>
</tr>
<tr>
<td>Bit Bit Bit Bit Bit Bit Bit Bit Bit Bit Bit Bit Bit Bit</td>
</tr>
</tbody>
</table>

**5.3.1 Transmission words**

Transmission words are the lowest level of control on Fibre Channel other than the control character used to provide character synchronization.

A transmission word is defined as four contiguous 8B/10B encoded transmission characters (10 bits each) treated as a unit. This unit is 40 bits in length (4 characters x 10 bits each) and is the smallest unit of transmission in Fibre Channel.

**5.3.1.1 Data characters**

A data character is a 8B/10B encoded transmission character with a data byte equated by the transmission code as one of the 256 possible data characters.

**5.3.1.2 Special characters**

The 8B/10B encoding scheme allows for all 256 data byte values, plus several others that can be used for special signaling. These other values are called special characters. Special characters include any transmission character considered valid by the transmission code but not equated to a valid data byte.

**5.4 Ordered sets**

An ordered set is a four-character combination of data and special transmissions characters. There are three primary categories of ordered sets:

- Primitive signals
- Primitive sequences
- Frame delimiters

The K28.5 special character is the first character of all ordered sets.

**5.4.1 Primitive signals**

Primitive signals are ordered sets that perform a control function. Primitive signals are recognized when one ordered set is detected. Table 3 lists the defined primitive signals. There must be a minimum of six primitive signals (Idles and R_RDYs) at the N_Port transmitter between frames to properly maintain clock skew.
Table 3: Primitive signals

<table>
<thead>
<tr>
<th>Primitive signal</th>
<th>Signal</th>
<th>Beginning running disparity</th>
<th>Ordered set (FC-1)</th>
<th>Ordered set (hex)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Idle</td>
<td>IDLE</td>
<td>Negative</td>
<td>K28.5  D21.4  D21.5  D21.5</td>
<td>BC  95  B5  B5</td>
</tr>
<tr>
<td>Receiver_Ready</td>
<td>R_RDY</td>
<td>Negative</td>
<td>K28.5  D21.4  D10.2  D10.2</td>
<td>BC  95  4A  4A</td>
</tr>
</tbody>
</table>

**Idle (IDLE)**

An Idle is transmitted on the loop to indicate the node is operational and ready for frame transmission and reception. Idles are transmitted when frames, R_RDY, or primitive sequences are not being transmitted. This maintains word synchronization and minimum spacing between frames.

**Receiver ready (R_RDY)**

R_RDY indicates that a frame was received and that the interface buffer that received the frame is ready for another frame. R_RDY is preceded and followed by a minimum of two Idles. R_RDY establishes buffer-to-buffer credit during data transmissions between an initiator and target.

### 5.4.1.1 Primitive signals used as frame delimiters

Primitive signals can also be frame delimiters. A frame is an indivisible information unit that may contain data to record on disc or control information such as a SCSI command.

**Note.** All ordered sets (except for the End-of-frame delimiter, EOF) require the running disparity from the previous word to be negative. The second character of an ordered set (except EOF) will be positive and the third and fourth characters are neutral.

Frame delimiters mark the beginning and end of frames. There are several frame delimiters available in Fibre Channel. There are nine Start-of-frame (SOF) delimiters and six End-of-frame (EOF) delimiters as listed in Table 4.

**Start-of-frame (SOF) delimiters:**
- mark the beginning of a frame,
- indicate whether this is the first frame of the sequence, and
- indicate the class of service for the frame.

**End-of-frame (EOF) delimiters:**
- mark the end of a frame, and
- indicate whether this frame is the last frame of the sequence.

The second character of EOF delimiters differentiates between normal and invalid frames. The EOF delimiter also ensures that negative running disparity results after processing the set by assigning the appropriate second character.

The third and fourth characters of the delimiter functions (SOF and EOF) are repeated to ensure that an error affecting a single character will not result in the recognition of an ordered set other than the one transmitted. See Table 4 below.
Seagate disc drives use only those listed in **bold type** (Seagate Fibre Channel disc drives are Class 3 devices and use only Class 3 delimiters).

**Table 4: Frame delimiters**

<table>
<thead>
<tr>
<th>Delimiter function</th>
<th>Delimiter</th>
<th>Beginning running disparity</th>
<th>Ordered set (FC-1)</th>
<th>Ordered set (hex)</th>
</tr>
</thead>
<tbody>
<tr>
<td>SOF Connect Class 1</td>
<td>SOFc1</td>
<td>Negative</td>
<td>K28.5 D21.5 D23.0 D23.0</td>
<td>BC B5 17 17</td>
</tr>
<tr>
<td>SOF Initiate Class 1</td>
<td>SOFi1</td>
<td>Negative</td>
<td>K28.5 D21.5 D23.2 D23.2</td>
<td>BC B5 57 57</td>
</tr>
<tr>
<td>SOF Normal Class 1</td>
<td>SOFn1</td>
<td>Negative</td>
<td>K28.5 D21.5 D23.1 D23.1</td>
<td>BC B5 37 37</td>
</tr>
<tr>
<td>SOF Initiate Class 2</td>
<td>SOFi2</td>
<td>Negative</td>
<td>K28.5 D21.5 D21.2 D21.2</td>
<td>BC B5 55 55</td>
</tr>
<tr>
<td>SOF Normal Class 2</td>
<td>SOFn2</td>
<td>Negative</td>
<td>K28.5 D21.5 D21.1 D21.1</td>
<td>BC B5 35 35</td>
</tr>
<tr>
<td>SOF Initiate Class 3</td>
<td>SOFi3</td>
<td>Negative</td>
<td>K28.5 D21.5 D22.2 D22.2</td>
<td>BC B5 56 56</td>
</tr>
<tr>
<td>SOF Normal Class 3</td>
<td>SOFn3</td>
<td>Negative</td>
<td>K28.5 D21.5 D22.1 D22.1</td>
<td>BC B5 36 36</td>
</tr>
<tr>
<td>SOF Initialize Loop</td>
<td>SOFil</td>
<td>Negative</td>
<td>K28.5 D21.5 D22.2 D22.2</td>
<td>BC B5 56 56</td>
</tr>
<tr>
<td>SOF Activate Class 4</td>
<td>SOFc4</td>
<td>Negative</td>
<td>K28.5 D21.5 D25.0 D25.0</td>
<td>BC B5 19 19</td>
</tr>
<tr>
<td>SOF Initiate Class 4</td>
<td>SOFi4</td>
<td>Negative</td>
<td>K28.5 D21.5 D25.2 D25.2</td>
<td>BC B5 59 59</td>
</tr>
<tr>
<td>SOF Normal Class 4</td>
<td>SOFn4</td>
<td>Negative</td>
<td>K28.5 D21.5 D25.1 D25.1</td>
<td>BC B5 39 39</td>
</tr>
<tr>
<td>SOF Fabric</td>
<td>SOFf</td>
<td>Negative</td>
<td>K28.5 D21.5 D22.2 D22.2</td>
<td>BC B5 58 58</td>
</tr>
<tr>
<td>EOF Terminate</td>
<td>EOFt</td>
<td>Negative Positive</td>
<td>K28.5 D21.4 D21.3 D21.3</td>
<td>BC 95 75 75</td>
</tr>
<tr>
<td>EOF Disconnect-Terminate</td>
<td>EOFdt</td>
<td>Negative Positive</td>
<td>K28.5 D21.4 D21.4 D21.4</td>
<td>BC 95 95 95</td>
</tr>
<tr>
<td>EOF Abort</td>
<td>EOFa</td>
<td>Negative Positive</td>
<td>K28.5 D21.4 D21.7 D21.7</td>
<td>BC 95 F5 F5</td>
</tr>
<tr>
<td>EOF Normal</td>
<td>EOFn</td>
<td>Negative Positive</td>
<td>K28.5 D21.4 D21.6 D21.6</td>
<td>BC 95 D5 D5</td>
</tr>
<tr>
<td>EOF Disconnect-Terminate-Invalid</td>
<td>EOFdtti</td>
<td>Negative Positive</td>
<td>K28.5 D10.4 D21.4 D21.4</td>
<td>BC 8A 95 95</td>
</tr>
<tr>
<td>EOF Normal-Invalid</td>
<td>EOFni</td>
<td>Negative Positive</td>
<td>K28.5 D10.5 D21.6 D21.6</td>
<td>BC 8A D5 D5</td>
</tr>
</tbody>
</table>

**Notes.**

1. EOF primitives come in two forms—one is used when the beginning running disparity is positive and the other is used if the beginning running disparity is negative. Regardless of which form is used, each EOF delimiter is defined so that negative current running disparity results after processing the final (rightmost) character of the ordered set.

2. Ordered sets associated with SOF delimiters, primitive signals, and primitive sequences are always transmitted with negative beginning running disparity. As a result, primitive signals, primitive sequences, and SOF delimiters are only defined for the negative beginning running disparity case.

3. Frames that end with any other EOF type are discarded by the drive.
Start-of-frame Initiate Class 3 (SOFi3)

SOFi3 indicates the beginning of the first frame of a sequence of frames (an exchange). (This includes all single frame sequences, commands, link services, transfer readys, and response frames.) SOFi3 is also used in the first data frame of a sequence.

Start-of-frame Normal Class 3 (SOFn3)

SOFn3 indicates the beginning of any frame other than the first frame of an exchange (see SOFi3 above).

Start-of-frame Initialize Loop (SOFil)

SOFil is the same as SOFi3, but is renamed for use in Loop Initialization to remove the class of service distinction from the initialization process.

End-of-frame Normal (EOFn)

EOFn indicates the end of any frame other than the last frame of an exchange or sequence.

End-of-frame Terminate (EOFt)

EOFt marks the end of the last frame of all sequences. It can also indicate the end of the last frame of an exchange. For example, the single frame sequences, commands, link services, transfer readys, and response frames.

5.4.1.2 Primitive signals custom made for arbitrated loop topologies

There are eight primitive signals used exclusively within arbitrated loop (FC-AL) topologies. These primitives are listed in Table 5 and defined in text following the table.

Table 5: FC-AL primitive signals

<table>
<thead>
<tr>
<th>FC-AL primitive signal</th>
<th>Signal</th>
<th>Beginning running disparity</th>
<th>Ordered set (FC-1)</th>
<th>Ordered set (hex)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Arbitrate</td>
<td>ARBx</td>
<td>Negative</td>
<td>K28.5 D20.4 AL_PA AL_PA</td>
<td>BC 94 AL_PA AL_PA</td>
</tr>
<tr>
<td>Arbitrate (F0)</td>
<td>ARB(F0)</td>
<td>Negative</td>
<td>K28.5 D20.4 D16.7 D16.7</td>
<td>BC 94 F0 F0</td>
</tr>
<tr>
<td>Open full-duplex</td>
<td>OPNyx</td>
<td>Negative</td>
<td>K28.5 D17.4 AL_PD AL_PS</td>
<td>BC 91 AL_PD AL_PS</td>
</tr>
<tr>
<td>Open half-duplex</td>
<td>OPNyy</td>
<td>Negative</td>
<td>K28.5 D17.4 AL_PD AL_PD</td>
<td>BC 91 AL_PD AL_PD</td>
</tr>
<tr>
<td>Open broadcast replicate</td>
<td>OPNfr</td>
<td>Negative</td>
<td>K28.5 D17.4 D31.7 D31.7</td>
<td>BC 91 FF FF</td>
</tr>
<tr>
<td>Open selective replicate</td>
<td>OPNyr</td>
<td>Negative</td>
<td>K28.5 D17.4 AL_PD D31.7</td>
<td>BC 91 AL_PD FF</td>
</tr>
<tr>
<td>Close</td>
<td>CLS</td>
<td>Negative</td>
<td>K28.5 D5.4 D21.5 D21.5</td>
<td>BC 85 B5 B5</td>
</tr>
<tr>
<td>Dynamic Half Duplex</td>
<td>DHD</td>
<td>Negative</td>
<td>K28.5 D10.4 D21.5 D21.5</td>
<td>BC 8A B5 B5</td>
</tr>
<tr>
<td>Mark</td>
<td>MRKtx</td>
<td>Negative</td>
<td>K28.5 D31.2 MK_TP AL_PS</td>
<td>BC 5F MK_TP AL_PS</td>
</tr>
</tbody>
</table>

Arbitrate (ARBx)

ARBx is transmitted to request access to the loop. Each ARBx contains the Physical (port) Address (AL_PA) of the requestor.

Arbitrate (ARB(F0))

ARB(F0) is transmitted to manage access fairness (see Section 8.3.1). It is also used to assign a loop master during initialization.
Open full-duplex (OPNx)

After successful arbitration, the transmitting port (x) opens the receiving port (y) for control and data frame transmission and reception. Any FC port can transmit or receive an OPN.

Open half-duplex (OPNy)

After successful arbitration, the initiator opens the target (y) for control and data frame transmission and reception of control frames. Data frame transmission from the target is not allowed.

Open broadcast replicate (OPNfr)—Not supported by Seagate drives

OPNfr is used by the open port to communicate with all ports in the loop. When received by the targets (the opened ports), they are not allowed to generate any transmission words (except fill words). They must replicate and retransmit all received words until a CLS is detected. Both ‘ f ’ and ‘ r ’ are FFh (D31.7 in FC-1 transmission code).

Open selective replicate (OPNyr)—Not supported by Seagate drives

Similar to OPNfr (see definition above), with the exception that the initiator can communicate with a subset of the targets on the loop. The target at AL_PA ‘ y ’ is a member of the subset.

Close (CLS)

Close indicates that the CLS originator is prepared to or has ended the current loop circuit.

Dynamic Half Duplex (DHD)

DHD indicates the open L_Port is relinquishing control of when the current loop circuit is closed. Support of DHD is discovered during Port Login.

Mark (MRKtx)—Not supported by Seagate drives

Mark may be used for synchronization within the loop (for example, spindle sync in a RAID application). The third character of the ordered set ‘ t ’ (MK_TP) is a vendor-unique sync code. Seagate drives pass through this primitive signal without responding to it.
5.4.2 Primitive sequences

Primitive sequences are ordered sets that perform control functions. These primitive sequences are listed in Table 6. Primitive sequences differ from primitive signals in the requirement for detection. Primitive sequences must have at least three consecutive ordered sets to be detected. Primitive sequences are transmitted repeatedly.

All of these primitive sequences are defined in Fibre Channel, but are not recognized by the drive. The Loop Initialization Primitive (LIP) sequence is used in arbitrated loop configurations to reset all attached ports to a known state. Refer to Section 8.2 on page 48 for additional information about LIPs.

Table 6: Primitive sequences

<table>
<thead>
<tr>
<th>Primitive sequences</th>
<th>Sequence</th>
<th>Beginning running disparity</th>
<th>Ordered set (FC-1)</th>
<th>Ordered set (hex)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Offline</td>
<td>OLS</td>
<td>Negative</td>
<td>K28.5 D21.1 D10.4 D21.2</td>
<td>BC 35 8A 55</td>
</tr>
<tr>
<td>Not_Operational</td>
<td>NOS</td>
<td>Negative</td>
<td>K28.5 D21.2 D31.5 D5.2</td>
<td>BC 55 BF 45</td>
</tr>
<tr>
<td>Link_Reset</td>
<td>LR</td>
<td>Negative</td>
<td>K28.5 D9.2 D31.5 D9.2</td>
<td>BC 49 BF 49</td>
</tr>
<tr>
<td>Link_Reset_Response</td>
<td>LRR</td>
<td>Negative</td>
<td>K28.5 D21.1 D31.5 D9.2</td>
<td>BC 35 BF 49</td>
</tr>
</tbody>
</table>

**Offline (OLS)**

Offline is transmitted to indicate that the port is initiating the initialization protocol prior to going offline. The sequence will be transmitted for a minimum of 5 msec before further action is taken.

**Not_Operational (NOS)**

Not_Operational is transmitted to indicate that the port has detected a loop failure or is offline.

**Link_Reset (LR)**

Link_Reset is transmitted by a port to initiate the Link Reset protocol following a Link Timeout. This brings the port back to a stable synchronized state.

**Link_Reset_Response (LRR)**

Link_Reset_Response is transmitted to indicate that the port is receiving and recognizes the Link_Reset (LR) primitive sequence.
5.4.2.1 Primitive sequences custom made for Arbitrated Loop topologies

There are eight primitive sequences used exclusively within arbitrated loop (FC-AL) topologies. These primitives are listed in Table 7 and defined in text following the table.

Table 7: FC-AL primitive sequences

<table>
<thead>
<tr>
<th>FC-AL primitive sequence</th>
<th>Sequence</th>
<th>Beginning running disparity</th>
<th>Ordered set (FC-1)</th>
<th>Ordered set (hex)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Loop initialization, no valid AL_PA</td>
<td>LIP</td>
<td>Negative</td>
<td>K28.5 D21.0 D23.7 D23.7</td>
<td>BC 15 F7 F7</td>
</tr>
<tr>
<td>Loop initialization, loop failure, no valid AL_PA</td>
<td>LIP</td>
<td>Negative</td>
<td>K28.5 D21.0 D24.7 D23.7</td>
<td>BC 15 F8 F7</td>
</tr>
<tr>
<td>Loop initialization, valid AL_PA</td>
<td>LIP</td>
<td>Negative</td>
<td>K28.5 D21.0 D23.7 AL_PS</td>
<td>BC 15 F7 AL_PS</td>
</tr>
<tr>
<td>Loop initialization, loop failure, valid AL_PA</td>
<td>LIP</td>
<td>Negative</td>
<td>K28.5 D21.0 D24.7 AL_PS</td>
<td>BC 15 F8 AL_PS</td>
</tr>
<tr>
<td>Loop initialization reset</td>
<td>LIP</td>
<td>Negative</td>
<td>K28.5 D21.0.0 AL_PD AL_PS</td>
<td>BC 15 AL_PD AL_PS</td>
</tr>
<tr>
<td>Loop initialization reset all</td>
<td>LIP</td>
<td>Negative</td>
<td>K28.5 D21.0 D31.7 AL_PS</td>
<td>BC 15 FF AL_PS</td>
</tr>
<tr>
<td>Loop port enable</td>
<td>LPEyx</td>
<td>Negative</td>
<td>K28.5 D5.0 AL_PD AL_PS</td>
<td>BC 05 AL_PD AL_PS</td>
</tr>
<tr>
<td>Loop port enable all</td>
<td>LPEfx</td>
<td>Negative</td>
<td>K28.5 D5.0 D31.7 AL_PS</td>
<td>BC 05 FF AL_PS</td>
</tr>
<tr>
<td>Loop port bypass</td>
<td>LPByx</td>
<td>Negative</td>
<td>K28.5 D9.0 AL_PD AL_PS</td>
<td>BC 09 AL_PD AL_PS</td>
</tr>
<tr>
<td>Loop port bypass all</td>
<td>LPBfx</td>
<td>Negative</td>
<td>K28.5 D9.0 D31.7 AL_PS</td>
<td>BC 09 FF AL_PS</td>
</tr>
</tbody>
</table>

Note. There are six sequences that invoke loop initialization. There is no operational difference between them except that bytes 3 and 4 identify the reason for the loop initialization.

**Loop initialization, no valid AL_PA (LIP)**

The L_Port is attempting to acquire an AL_PA.

**Loop initialization, loop failure, no valid AL_PA (LIP)**

The transmitting L_Port detects a loop failure at its receiver. Since it has not completed initialization, it uses ‘F7’ (D23.7) rather than a valid AL_PA.

**Loop initialization, valid AL_PA (LIP)**

The L_Port is attempting to reinitialize the loop to a known state after recognizing a problem (performance degradation).

**Loop initialization, loop failure, valid AL_PA (LIP)**

The transmitting L_Port detects a loop failure at its receiver.

**Loop initialization reset (LIP)**

If the rightmost two bytes consist of a valid destination and source address, the source is requesting the target to perform a device reset.

**Loop initialization reset all (LIP)**

If byte 3 is FFh and byte 4 is a valid source address, the source is requesting all receiving targets to perform a device reset.
Loop port enable (LPEyx)
LPEyx resets the bypass circuit and enables a previously-bypassed L_Port.

Loop port enable all (LPEfx)
LPEfx resets the bypass circuits on all L_Ports on the loop.

Loop port bypass (LPByx)
LPByx activates the port bypass circuit and prevents the L_Port from actively participating on the loop.

Loop port bypass all (LPBfx)
LPBfx activates the port bypass circuit for all L_Ports on the loop with bypass circuits except for the source of the LPBfx.
6.0 Framing protocol (FC-2)

The entire responsibility of moving frames between N_Ports is assigned to the Fibre Channel layer called the framing protocol (FC-2). This protocol is primarily concerned with constructing and managing frames, sequences, and exchanges.

6.1 Frames

Frames transfer all information between nodes. The frames are normally constructed by the transmitting node’s N_Port. A frame is the smallest unit of information transfer across a link. A sequence is one or more frames. An exchange is one or more sequences. See Figure 7 below.

It is possible, but not common, for a sequence to have only one frame and for an exchange to have only one sequence. Again, this isn’t common, but possible. Most sequences have more than one frame, and most exchanges have more than one sequence.

![Figure 7. Relationship between frames, sequences, and exchanges](image)

6.1.1 Frame structure

A frame is a string of transmission words containing data bytes. Every frame is prefixed by a start-of-field (SOF) delimiter and suffixed by an end-of-field (EOF) delimiter. There are never any primitive signals or primitive sequences in a frame.

All frames also have a header and a Cyclic Redundancy Check (CRC) field. The payload data field is optional (but normally present) with the size and contents determined by the type of frame.

![Figure 8. Frame structure](image)
6.1.1.1 Start-of-frame (SOF) delimiter

Start-of-frame (SOF) delimiters signal the beginning of a frame. See “Frame delimiters” on page 27. This referenced page contains a list of the various types of SOF delimiters.

6.1.1.2 Frame header

The frame header is 24 bytes long and is present in all frames. It is used to control link operation, control device protocol transfers, and to detect missing frames or frames that are out of order.

Table 8: Frame Header

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>R_CTL</td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>D_ID</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reserved</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>S_ID</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>TYPE</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>F_CTL</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>SEQ_ID</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>DF_CTL</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>SEQ_CNT</td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>OX_ID</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>RX_ID</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Parameter</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
R_CTL (Routing Control)
This one-byte field provides routing bits and information bits to categorize the frame function.

The high order bits (bits 31-28) indicate the frame type as indicated below:

0000  =  FC-4 Device_Data frame
0010  =  Extended Link_Data frame
0011  =  FC-4 Link_Data frame
0100  =  Video_Data frame
1000  =  Basic Link_Data frame
1100  =  Link_Control frame
Others  =  Reserved
The low order bits (bits 27-24) contain the Information field values. This is dependent on the value of the high order bits (31-28). If the high order bit value = 1000, the Information field contains a basic link service. For all other high order bit values, the Information field specifies the Common Information Categories specified in the table below.

Table 9: Routing Control values

<table>
<thead>
<tr>
<th>High order bits</th>
<th>Low order bits</th>
<th>Use</th>
</tr>
</thead>
<tbody>
<tr>
<td>Type 08h, SCSI FCP, Device Data frames</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0000</td>
<td>0000</td>
<td>Uncategorized</td>
</tr>
<tr>
<td></td>
<td>0001</td>
<td>Solicited Data</td>
</tr>
<tr>
<td></td>
<td>0010</td>
<td>Unsolicited Control</td>
</tr>
<tr>
<td></td>
<td>0011</td>
<td>Solicited control</td>
</tr>
<tr>
<td></td>
<td>0100</td>
<td>Unsolicited data</td>
</tr>
<tr>
<td></td>
<td>0101</td>
<td>Data descriptor</td>
</tr>
<tr>
<td></td>
<td>0110</td>
<td>Unsolicited command</td>
</tr>
<tr>
<td></td>
<td>0111</td>
<td>Command status</td>
</tr>
<tr>
<td>Type 01h, Extended link service</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0010</td>
<td>0000</td>
<td>Uncategorized</td>
</tr>
<tr>
<td></td>
<td>0001</td>
<td>Solicited data</td>
</tr>
<tr>
<td></td>
<td>0010</td>
<td>Unsolicited control</td>
</tr>
<tr>
<td></td>
<td>0011</td>
<td>Solicited control</td>
</tr>
<tr>
<td></td>
<td>0100</td>
<td>Unsolicited data</td>
</tr>
<tr>
<td></td>
<td>0101</td>
<td>Data descriptor</td>
</tr>
<tr>
<td></td>
<td>0110</td>
<td>Unsolicited command</td>
</tr>
<tr>
<td></td>
<td>0111</td>
<td>Command status</td>
</tr>
<tr>
<td>Type 00h, Basic link service</td>
<td></td>
<td></td>
</tr>
<tr>
<td>1000</td>
<td>0000</td>
<td>No operation</td>
</tr>
<tr>
<td></td>
<td>0001</td>
<td>Abort sequence (ABTS)</td>
</tr>
<tr>
<td></td>
<td>0010</td>
<td>Remove connection</td>
</tr>
<tr>
<td></td>
<td>0011</td>
<td>Reserved</td>
</tr>
<tr>
<td></td>
<td>0100</td>
<td>Basic_Accept (BA_ACC)</td>
</tr>
<tr>
<td></td>
<td>0101</td>
<td>Basic_Reject (BA_RJT)</td>
</tr>
<tr>
<td></td>
<td>0110-0111</td>
<td>Reserved</td>
</tr>
</tbody>
</table>

**D_ID (Destination Identifier)**
The 3-byte N_Port address to which the frame is being sent.

**S_ID (Source Identifier)**
The 3-byte address of the N_Port or F_Port originating the frame.
Type (Data Structure Type)
This 1-byte field identifies the protocol of the frame contents as described below:

Table 10: Data type codes

<table>
<thead>
<tr>
<th>R_CTL (4 highest order bits)</th>
<th>Type code</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1000</td>
<td>00</td>
<td>Basic Link Service</td>
</tr>
<tr>
<td>0010</td>
<td>01</td>
<td>Extended Link Service</td>
</tr>
<tr>
<td>0000</td>
<td>08</td>
<td>SCSI FCP</td>
</tr>
</tbody>
</table>
F_CTL (Frame Control)

This 3-byte (24-bit) field contains control information relating to the frame content as defined below.

**Table 11: Frame Control (F_CTL) bit definitions**

<table>
<thead>
<tr>
<th>Bit</th>
<th>Definition</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>23</td>
<td>Exchange context</td>
<td>0 = Frame is from the exchange originator. 1 = Frame is from the exchange responder.</td>
</tr>
<tr>
<td>22</td>
<td>Sequence context</td>
<td>0 = Initiator 1 = Recipient</td>
</tr>
<tr>
<td>21</td>
<td>First sequence</td>
<td>0 = Not the first sequence of the exchange. 1 = First sequence of the exchange.</td>
</tr>
<tr>
<td>20</td>
<td>Last sequence</td>
<td>0 = Not the last sequence of the exchange. 1 = Last sequence of the exchange.</td>
</tr>
<tr>
<td>19</td>
<td>End of sequence</td>
<td>0 = Not the last frame of the sequence. 1 = Last frame of the sequence.</td>
</tr>
<tr>
<td>18</td>
<td>End connection</td>
<td>Not supported.</td>
</tr>
<tr>
<td>17</td>
<td>Chained sequence</td>
<td>Not supported.</td>
</tr>
<tr>
<td>16</td>
<td>Sequence initiative</td>
<td>0 = Hold sequence initiative. 1 = Transfer sequence initiative.</td>
</tr>
<tr>
<td>15</td>
<td>X_ID reassigned</td>
<td>Not supported.</td>
</tr>
<tr>
<td>14</td>
<td>Invalid X_ID</td>
<td>Not supported.</td>
</tr>
<tr>
<td>13</td>
<td>Reserved</td>
<td></td>
</tr>
<tr>
<td>12</td>
<td></td>
<td></td>
</tr>
<tr>
<td>11</td>
<td></td>
<td></td>
</tr>
<tr>
<td>10</td>
<td></td>
<td></td>
</tr>
<tr>
<td>9</td>
<td>Retransmitted</td>
<td>Not supported.</td>
</tr>
<tr>
<td>8</td>
<td>Unidirectional</td>
<td>Not supported.</td>
</tr>
<tr>
<td>7</td>
<td>Continue sequence</td>
<td>Not supported.</td>
</tr>
<tr>
<td>6</td>
<td>condition</td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>Abort sequence</td>
<td>Not supported.</td>
</tr>
<tr>
<td>4</td>
<td>condition</td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>Relative offset</td>
<td>0 = Parameter field not meaningful. 1 = Parameter field equals relative offset.</td>
</tr>
<tr>
<td>2</td>
<td>Reserved</td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>Fill data bytes</td>
<td>End of data field fill bytes</td>
</tr>
<tr>
<td>0</td>
<td></td>
<td>00 = 0 bytes of fill. 01 = 1 byte of fill (last byte of data field). 02 = 2 bytes of fill (last 2 bytes of data field). 03 = 3 bytes of fill (last 3 bytes of data field).</td>
</tr>
</tbody>
</table>

SEQ_ID (Sequence Identifier)

This field uniquely identifies frames in a non-streamed sequence or when only one sequence is open.
DF_CTL (Data Field Control)
Specifies the presence of optional headers in the payload of the frame. The drive does not support optional headers.
00h The drive sets this field to 00h for all frames it originates (sends), and should also be 00h for all frames sent to the drive.

SEQ_CNT (Sequence Count)
This 2-byte field identifies the sequential order of frames within a sequence or multiple sequences of the same exchange. The SEQ_CNT value for each frame of a sequence has to be unique. The field range limits the number of frames per sequence to a maximum of 65,536.
For SCSI FCP write data transfers that require more than one sequence to transfer the data, the first frame of each sequence starts with a SEQ_CNT of 0000h. The transfer of sequence initiative between write data sequences and the XFR_RDY sent by the drive give the sequence originator (the SCSI Initiator) verification that the previous sequence is closed.
For SCSI FCP read data transfers that require more than one sequence to transfer the data, the SEQ_CNT does not reset to 0000h for each new sequence. The SEQ_CNT increments sequentially across the sequence boundaries. The SEQ_CNT is still allowed to increment through all 65,536 possible values for each sequence. This is required as there is no verification in Class 3 that all the frames of previous sequences have been delivered.

OX_ID (Originator Exchange Identifier)
This 2-byte field is assigned by the originator of an exchange. For SCSI FCP frames, this value is analogous to the Queue Tag used in Parallel SCSI and must be unique for an initiator/drive pair.

RX_ID (Responder Exchange Identifier)
This 2-byte identifier is a unique identifier generated by the responder for an exchange established by an originator and identified by an OX_ID.
With Class 3 devices such as this drive, the responder of the exchange assigns a unique value for RX_ID other than FFFFh if RX_ID is being used in an ACK to a data frame in the first sequence transmitted as a sequence initiator.
The drive returns FFFFh in this field to indicate the RX_ID is not being used.

Parameter
This 4-byte field is used in data frames to define the relative offset (displacement) of the first byte of the payload from the base address of the command. When the relative offset is present, bit 3 of F_CTL is set to indicate that relative offset is valid. The drive uses the SEQ_CNT value to verify that frames are being received in order. The drive does not use relative offset in frames it receives. The drive sends relative offset information in data frames it originates.

6.1.1.3 Data field (payload)
The data field, also known as the payload, is aligned on word boundaries. The payload length must be an integer multiple of four bytes and is limited to 2,112 bytes. If the data field is not an integer multiple of four bytes, valid fill bytes are inserted to meet the requirement. F_CTL bits 0 and 1 indicate how many fill bytes are used. Fill bytes are only permitted in the last frame of a sequence. These fill bytes can be any valid byte value.
The contents of the frame payload is specified by the type of frame. Refer to Section 9.0 for link service information.

6.1.1.4 CRC field
The Cyclic Redundancy Check (CRC) is a 4-byte field that follows the payload field. The CRC is used to verify the integrity of the frame header and payload fields. This helps detect errors in a frame. The SOF and EOF frame delimiters are not included in the CRC calculation.
The algorithm used to calculate the CRC field value is the same as that used in the Fiber Distributed Data Interface (FDDI) standard. The polynomial for the CRC is:

\[ x^{32} + x^{26} + x^{23} + x^{22} + x^{16} + x^{12} + x^{11} + x^{10} + x^8 + x^7 + x^5 + x^4 + x^2 + x + 1 \]
6.1.1.5 End-of-frame (EOF) delimiter

End-of-frame (EOF) delimiters signal the end of a frame. See “Frame delimiters” on page 27. This page contains a list of the various types of EOF delimiters.

6.1.2 Frame Field Order

The frame content shall be transmitted sequentially following the SOF delimiter as an ordered byte stream within the definition of the frame as specified in figure 11, table 12, and figure 16 until the EOF delimiter is transmitted.

Table 12 relates the frame format to the ordered byte stream transferred from the Upper Level Protocol (or FC-4) and transmitted onto a link.

A frame shall be transmitted in the following byte order:

A0, A1, A2, A3, B0, B1, B2, B3, B4... B32, B33, B34, B35, C0, C1, C2, C3.

Table 12: Frame Byte Order

<table>
<thead>
<tr>
<th>Bits Word</th>
<th>31 .. 24</th>
<th>23 .. 16</th>
<th>15 .. 08</th>
<th>07 .. 00</th>
</tr>
</thead>
<tbody>
<tr>
<td>SOF</td>
<td>K28.5</td>
<td>Dxx.x</td>
<td>Dxx.x</td>
<td>Dxx.x</td>
</tr>
<tr>
<td></td>
<td>A0</td>
<td>A1</td>
<td>A2</td>
<td>A3</td>
</tr>
<tr>
<td>0</td>
<td>R_CTL</td>
<td>B0</td>
<td>B1</td>
<td>B2</td>
</tr>
<tr>
<td>1</td>
<td>CS_CTL/</td>
<td>B4</td>
<td>B5</td>
<td>B6</td>
</tr>
<tr>
<td>Priority</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>TYPE</td>
<td>B8</td>
<td>B9</td>
<td>B10</td>
</tr>
<tr>
<td>3</td>
<td>SEQ_ID</td>
<td>B12</td>
<td>B13</td>
<td>B13</td>
</tr>
<tr>
<td></td>
<td>DF_CTL</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>SEQ_CNT</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>OX_ID</td>
<td>B13</td>
<td>B13</td>
<td>B13</td>
</tr>
<tr>
<td></td>
<td>RX_ID</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>Parameter</td>
<td>B13</td>
<td>B13</td>
<td>B13</td>
</tr>
<tr>
<td>6</td>
<td>Data Field</td>
<td>B13</td>
<td>B13</td>
<td>B13</td>
</tr>
<tr>
<td>7</td>
<td>Data Field</td>
<td>B13</td>
<td>B13</td>
<td>B13</td>
</tr>
<tr>
<td>n-1</td>
<td>CRC</td>
<td>B13</td>
<td>B13</td>
<td>B13</td>
</tr>
<tr>
<td>EOF</td>
<td>K28.5</td>
<td>Dxx.x</td>
<td>Dxx.x</td>
<td>Dxx.x</td>
</tr>
<tr>
<td></td>
<td>C0</td>
<td>C1</td>
<td>C2</td>
<td>C3</td>
</tr>
</tbody>
</table>

If there is one byte of fill and no ESP_Trailer (see 11.2) in the Data Field of this frame, the fill byte is B31. With no optional header present, the relative offset (Parameter Field) shall be specified as follows:
a) relative offset + 0 specifies B24;
b) relative offset + 3 specifies B27; and
c) relative offset + 4 specifies B28.

For a relative offset of decimal 1024 (00 00 04 00h) the Parameter Field shall be specified as: B20, B21, B22, B23 = 00 00 04 00h

6.2 Frame sequences

Since the data field has a maximum length of 2,112 bytes (528 transmission words), larger amounts of data must be split into several frames.

Frame sequences always contain at least one frame. The frame header subfields in the F_CTL field are used to identify the beginning, middle, and end of a frame sequence. The SEQ_ID and SEQ_CNT fields are used to identify the order of the frames for reassembly in the event they arrive out of order at the destination when participating in a fabric topology.

The OX_ID and RX_ID fields identify the larger context (the exchange) of which this frame sequence is a part.

The frame sequence itself may be part of an exchange and is identified by the SEQ_ID field in the frame header. The R_CTL field of the frame header identifies the category of information in all the frames of the sequence. All of the frames within the same sequence are required to have the same category.

6.3 Exchanges

Exchanges are an additional layer that controls operations across Fibre Channel. An exchange provides a control environment for the transfer of information between two N_Ports. You can think of exchanges as an operating system that controls communications between nodes when two or more sequences are required to transmit data or commands. Exchanges even keep track of operations that occur in opposite directions (sender to receiver and receiver to sender). Here’s a short explanation of how this occurs:

One of the N_Ports establishes an exchange by sending a sequence to the other N_Port with at least one frame in it. The originator names its resources using the OX_ID field in the frame header of each frame of the exchange.

The responder of the exchange also allocates resources after it receives the first frame of the first sequence. It names its resources using the RX_ID field.
These two independent fields (OX_ID and RX_ID) allow each N_Port to identify the resources needed to manage a frame or sequence as it arrives. Each N_Port involved with the exchange can use a link service request to view the contents of the control information in the other port in the exchange; however, no other N_Ports are allowed to request information for the exchange since they are not involved with that particular exchange.

Figure 9. FC–SCSI exchanges, command and response transfers

6.4 Credit

The framing protocol must be concerned about how many frames one source N_Port can send to another without overflowing the buffers in the receiving N_Port. To address this problem, there are two types of credit:

1. Buffer-to-buffer credit (BB_Credit). This type of credit is associated only with the immediate fiber exiting the transmitter to the next receiver (F_Port or N_Port). This credit is managed by the R_RDY primitive signal on a link.

2. End-to-end credit (EE_Credit). This type of credit is negotiated between a source N_Port and a destination N_Port. This credit is managed using an acknowledgment (ACK) frame sent from the destination back to the source N_Port. This type of credit is not used in Class 3 therefore it is not applicable to Seagate drives.
7.0 Classes of service (FC-2)

There are five classes of service currently available or being defined. Classes of service are simply different communication methods used between nodes. Seagate drives use only Class 3; however, brief explanations of the other classes are provided as well.

7.1 Class 1

Class 1 is like a direct face-to-face meeting with no interruptions or delays. It is a dedicated full-bandwidth connection between two nodes.

Other Class 1 attributes
- Guaranteed delivery
- Frames are received in the order they are transmitted
- Usually uses the least overall bandwidth
- Very little software interaction

7.2 Class 2

Class 2 is like an electronic mail transaction where each message has an acknowledgment message sent from the receiver to signal that the message was received successfully. This class of service allows one N_Port to transmit consecutive frames to multiple destinations without establishing a dedicated connection with any specific N_Port and also allows one N_Port to receive consecutive frames from one or more N_Ports without having established dedicated connections with any of them.

Other Class 2 attributes
- Confirmed delivery (the receiver sends an acknowledgment on receipt)
- Frames are not always guaranteed to be received in the order they are transmitted
- Can potentially use more bandwidth than Class 1 and latency may increase by waiting for acknowledgments

7.3 Class 3

Class 3 service multiplexes frames at frame boundaries to or from one or more N_Ports without acknowledgment of receipt.

Seagate drives use Class 3 exclusively. Class 3 reduces the complexity of the ports and provides better performance for disc applications.

Other Class 3 attributes
- Full duplex transfers may be used between two ports using Class 3; however, Class 3 operation does not require half duplex operation.
- Acknowledge (ACK) buffer not required (no waiting for ACKs)
- EE_Credit not required
• Busy and Reject not needed due to alternate credit model
• Errors are recovered at the exchange level

7.3.1 Class 3 flow control

Class 3 flow control is a simple model which does not require EE_Credits. With Class 3, there is only buffer-to-buffer flow control in one direction and there are no link level responses to Class 3 frames.

7.4 Classes 4 and 5

Classes 4 and 5 are being defined by Fibre Channel committees to aid audio and video applications.

Class 4 is similar to Class 1 in that a dedicated connection is established; however, with Class 4, the full bandwidth is not available. Connections for other nodes connected may be assigned portions of the bandwidth.

Class 5 provides isochronous service through a fabric. A Class 5 node is guaranteed access through a fabric at established time intervals.
8.0 FC Arbitrated Loop concepts

This section describes some basic Fibre Channel Arbitrated Loop (FC-AL) concepts. Seagate disc drives support FC-AL as the topology for connectivity in Fibre Channel environments.

Fibre Channel is a serial data channel that provides logical point-to-point service to two communicating devices. With FC-AL, you can have a maximum of one point-to-point circuit at any one time. When this circuit is active, only two L_Ports are communicating, but up to 127 devices may be attached to and participating on the same loop. All of the L_Ports that are on the loop but that are not one of the two communicating L_Ports “see” all data transferred across the loop and retransmit this information so that the data reaches its intended destination.

8.1 Arbitrated Loop physical address (AL_PA)

Each device communicating on an arbitrated loop must have an Arbitrated Loop Physical Address (AL_PA). The AL_PA is an 8-bit (1-byte) 8B/10B encoded value that is a valid data character. This 8-bit character, when encoded to 10 bits, must have an equal number of 1’s and 0’s in the address to maintain neutral running disparity. Neutral running disparity is required so that the AL_PA data character does not change the current running disparity of the current transmission word. There are 134 characters that result in neutral disparity. See Table 13. Seven of the 134 neutral disparity characters are reserved (see Table 14 on page 47). This leaves 127 valid addresses.

Table 13: 8B/10B characters with neutral disparity

<table>
<thead>
<tr>
<th>D xx.y</th>
<th>0</th>
<th>1</th>
<th>2</th>
<th>3</th>
<th>4</th>
<th>5</th>
<th>6</th>
<th>7</th>
<th>Hex value</th>
</tr>
</thead>
<tbody>
<tr>
<td>00</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td></td>
<td></td>
<td></td>
<td>00, 80, E0</td>
</tr>
<tr>
<td>01</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td></td>
<td></td>
<td></td>
<td>01, 81, E1</td>
</tr>
<tr>
<td>02</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td></td>
<td></td>
<td></td>
<td>02, 82, E2</td>
</tr>
<tr>
<td>03</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td></td>
<td></td>
<td></td>
<td>23, 43, 63, A3, C3</td>
</tr>
<tr>
<td>04</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td></td>
<td></td>
<td></td>
<td>04, 84, E4</td>
</tr>
<tr>
<td>05</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td></td>
<td></td>
<td></td>
<td>25, 45, 65, A5, C5</td>
</tr>
<tr>
<td>06</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td></td>
<td></td>
<td></td>
<td>26, 46, 66, A6, C6</td>
</tr>
<tr>
<td>07</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td></td>
<td></td>
<td></td>
<td>27, 47, 67, A7, C7</td>
</tr>
<tr>
<td>08</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td></td>
<td></td>
<td></td>
<td>08, 88, E8</td>
</tr>
<tr>
<td>09</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td></td>
<td></td>
<td></td>
<td>29, 49, 69, A9, C9</td>
</tr>
<tr>
<td>10</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td></td>
<td></td>
<td></td>
<td>2A, 4A, 6A, AA, CA</td>
</tr>
<tr>
<td>11</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td></td>
<td></td>
<td></td>
<td>2B, 4B, 6B, AB, CB</td>
</tr>
<tr>
<td>12</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td>*</td>
<td></td>
<td></td>
<td></td>
<td>2C, 4C, 6C, AC, CC</td>
</tr>
</tbody>
</table>
### Table 13: 8B/10B characters with neutral disparity

<table>
<thead>
<tr>
<th>D xx.y</th>
<th>y</th>
<th>Hex value</th>
</tr>
</thead>
<tbody>
<tr>
<td>13</td>
<td>* * * *</td>
<td>2E, 4E, 6E, AE, CE</td>
</tr>
<tr>
<td>14</td>
<td>* * * *</td>
<td>2E, 4E, 6E, AE, CE</td>
</tr>
<tr>
<td>15</td>
<td>*</td>
<td>0F, 8F, EF</td>
</tr>
<tr>
<td>16</td>
<td>*</td>
<td>10, 90, F0(^R)</td>
</tr>
<tr>
<td>17</td>
<td>* * * *</td>
<td>31, 51, 71, B1, D1</td>
</tr>
<tr>
<td>18</td>
<td>* * * *</td>
<td>32, 52, 72, B2, D2</td>
</tr>
<tr>
<td>19</td>
<td>* * * *</td>
<td>33, 53, 73, B3, D3</td>
</tr>
<tr>
<td>20</td>
<td>* * * *</td>
<td>34, 54, 74, B4, D4</td>
</tr>
<tr>
<td>21</td>
<td>* * * *</td>
<td>35, 55, 75, B5, D5</td>
</tr>
<tr>
<td>22</td>
<td>* * * *</td>
<td>36, 56, 76, B6, D6</td>
</tr>
<tr>
<td>23</td>
<td>* * * *</td>
<td>17, 97, F7(^R)</td>
</tr>
<tr>
<td>24</td>
<td>* * * *</td>
<td>18, 98, F8(^R)</td>
</tr>
<tr>
<td>25</td>
<td>* * * *</td>
<td>39, 59, 79, B9, D9</td>
</tr>
<tr>
<td>26</td>
<td>* * * *</td>
<td>3A, 5A, 7A, BA, DA</td>
</tr>
<tr>
<td>27</td>
<td>* * * *</td>
<td>1B, 9B, FB(^R)</td>
</tr>
<tr>
<td>28</td>
<td>* * * *</td>
<td>3C, 5C, 7C, BC, DC</td>
</tr>
<tr>
<td>29</td>
<td>* * * *</td>
<td>1D, 9D, FD(^R)</td>
</tr>
<tr>
<td>30</td>
<td>* * * *</td>
<td>1E, 9E, FE(^R)</td>
</tr>
<tr>
<td>31</td>
<td>* * * *</td>
<td>1F, 9F, FF(^R)</td>
</tr>
<tr>
<td>Total</td>
<td>13 19 19 19 13 19 19 13</td>
<td>13</td>
</tr>
</tbody>
</table>

* character with neutral disparity.

\(^R\) Reserved (see Table 14 on page 47).

Of the 134 neutral disparity characters, seven are reserved. This leaves 127 valid non-reserved addresses. 126 of these addresses may be used by NL_Ports and one (00) may be used by a FL_Port (if present).
In the parallel SCSI world, the higher the bus address, the higher the priority the device has. The opposite is true with FC-AL. See Table 15.

During loop initialization, each NL_Port interested in participating on the loop is assigned one of the 126 AL_PA values. If an NL_Port is offline because its node is powered off, the port is considered a non-participating NL_Port.

One more reason exists for an L_Port being in non-participating mode: you can place more than 127 L_Ports on a loop, and, if this occurs, loop initialization permits only the first 126 NL_Ports and the first FL_Port to acquire a valid AL_PA. The remaining L_Ports must wait for a position to open up to obtain a valid AL_PA.

An L_Port can get a valid AL_PA in four ways:
1. Assigned by the fabric (if present)
2. Use the previously-assigned address
3. Hard assigned by the backpanel
4. Soft assigned and acquired through the loop initialization process

The most common method that disc drives use is to have the backpanel provide a hard assigned address. This is similar to setting a SCSI ID on a drive, but with Fibre Channel, the ID is set on the backpanel rather than the drive. If, for some reason, a duplicate address is encountered when hard-assigned addresses are processed, the drive will revert to acquiring a soft-assigned AL_PA during initialization. This insures that every node gets a unique AL_PA.

A more detailed explanation of loop initialization is provided in the following section.

---

**Table 14: AL_PA addressing**

<table>
<thead>
<tr>
<th>Values (hex)</th>
<th>Use</th>
</tr>
</thead>
<tbody>
<tr>
<td>00</td>
<td>Reserved for the FL_Port (if present)</td>
</tr>
<tr>
<td>01-EF</td>
<td>Contains 127 valid addresses</td>
</tr>
<tr>
<td>F0</td>
<td>Reserved for fairness</td>
</tr>
<tr>
<td>F1-F6</td>
<td>These values do not have neutral running disparity</td>
</tr>
<tr>
<td>F7-F8</td>
<td>Reserved for loop initialization</td>
</tr>
<tr>
<td>FB, FD, and FE</td>
<td>Reserved for future use</td>
</tr>
<tr>
<td>FF</td>
<td>Reserved to address all ports in broadcast mode</td>
</tr>
</tbody>
</table>

**Table 15: AL_PA value priorities**

<table>
<thead>
<tr>
<th>AL_PA value</th>
<th>Priority</th>
</tr>
</thead>
<tbody>
<tr>
<td>00</td>
<td>Highest priority (assigned to the FL_Port if present)</td>
</tr>
<tr>
<td>01</td>
<td>Highest priority NL_Port (on a public loop)</td>
</tr>
<tr>
<td>EF</td>
<td>Lowest priority NL_Port address</td>
</tr>
<tr>
<td>F0</td>
<td>Used only by the fairness algorithm and has no priority</td>
</tr>
</tbody>
</table>
8.2 Loop initialization

Loop initialization is the process used to obtain or verify AL_PAs. Loop initialization occurs at power-up, when a new node is inserted in the loop, or for error recovery.

During loop initialization, each NL_Port discovers that it is connected in an arbitrated loop topology and that there may be multiple NL_Ports on the arbitrated loop. The procedure used for this discovery is called the loop port state machine (LPSM). The FC-2 protocol is also used.

A buffer on the drive receives each of the following loop initialization frames: LISM, LIFA, LIPA, LIHA, LISA, LIRP, and LILP. All other frames may be discarded if the L_Port’s buffer is full.

If the NL_Ports finds that there are not any more NL_Ports attached, but that there is only an F_Port or N_Port attached, the NL_Port configures itself to operate as an N_Port by going into N_Port mode.
An L_Port begins the loop initialization procedure in the initializing state at the request of the node. In most cases, the backpanel will be set to provide a hard assigned physical address which will not be changed by the initialization process, unless duplicate hard assigned physical addresses are found. The AL_PA of the one FL_Port (if present) is always 00h. All other AL_PA values will range from 01h - EFh.

**Figure 10. Loop initialization sequences**
The loop initialization process begins when any NL_Port forces the loop into the INITIALIZING state. See Figure 11. This initializing port begins transmitting loop initialization primitive sequences (LIPs). Any NL_Port can cause this by sending any of the LIPS listed in Table 16. All LIPs cause the 4-step initialization process to occur. A loss of signal will also force the loop to initialize.

There are five Loop Initialization Primitive (LIP) sequences:

Table 16: Loop Initialization Primitive (LIP) sequences

<table>
<thead>
<tr>
<th>LIP Reason Code</th>
<th>Use</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>F7, F7</td>
<td>Initializing LIP</td>
<td>Used when an L_Port wants to acquire an AL_PA.</td>
</tr>
<tr>
<td>F8, AL_PS</td>
<td>Loop failure</td>
<td>The originating L_Port has detected an input failure. The AL_PS is the AL_PA of the L_Port.</td>
</tr>
<tr>
<td>F8, F7</td>
<td>Loop failure</td>
<td>The originating L_Port has detected an input failure but the L_Port does not have an AL_PA.</td>
</tr>
<tr>
<td>F7, AL_PS</td>
<td>Performance degradation</td>
<td>The originating L_Port has detected poor loop performance. Example: The L_Port is unable to win arbitration.</td>
</tr>
<tr>
<td>AL_PD, AL_PS</td>
<td>Selective reset</td>
<td>The source L_Port (AL_PS) wants to reset the destination port (AL_PD). All L_Ports complete loop initialization. The selected L_Port performs a reset after loop initialization.</td>
</tr>
<tr>
<td>FF, AL_PS</td>
<td>Reset all</td>
<td>The source L_Port (AL_PS) wants to reset all other L_Ports supporting LIP reset on the loop. All L_Ports complete initialization. The L_Ports supporting LIP reset all (generally only the targets in a storage implementation) perform a reset after loop initialization.</td>
</tr>
</tbody>
</table>

The next L_Port in the loop receives the LIPs and transitions to the OPEN_INIT state and transmits LIPs to the next L_Port on the loop. This cycle continues until the port that started the initialization process receives the LIPs. It then also transitions to the OPEN_INIT state. This means all L_Ports on the loop are in the OPEN_INIT state at this point.

Each port transmits a minimum of 12 LIPs and then transmits Idles for the AL_TIME (15 msecs). The L_Port then transmit Loop Initialization Select Master (LISM) frames to select a loop master who will then control the initialization process.

Loop initialization steps

There are four primary steps involved in initializing the loop.

1. Select a loop master based on the 8-byte port name (Loop Initialization Select Master—LISM).
2. Assign each port an arbitrated loop physical address (AL_PA). There are four ways a port can acquire an AL_PA. These are listed below.
   - Fabric assigned (Loop Initialization Fabric Assigned—LIFA)
   - Previously acquired (Loop Initialization Previously Acquired—LIPA)
   - Hard assigned (Loop Initialization Hardware Assigned—LIHA)
   - Soft assigned (Loop Initialization Soft Assigned—LISA)
4. Transmit completed AL_PA position map around the loop (Loop Initialization Loop Position—LILP).
Loop Initialization Select Master (LISM)

12-byte payload

| 11010000 | 8-byte port name |

The loop master is determined as follows:
- Each NL_Port selects an initial AL_PA of ‘EF’. The FL_Port (if present) selects an AL_PA of ‘0’.
- Each port transmits LISM with the D_ID and S_ID fields of the header set to its AL_PA. The payload is set to the port name which includes the world wide name (WWN).
- Each port examines the payload in the inbound LISM. If it is greater than its own, the port transmits a new LISM with its own port name in the payload. If it is less than its own, the port retransmits the received LISM. If it is the same as its own, it becomes the loop master.
- The loop master sends ARB(F0) to purge the loop and to inform all other ports that a master has been selected. When the master receives ARB(F0) on the inbound fibre, initialization proceeds to LIFA.

Loop Initialization Fabric Assigned (LIFA)

20-byte payload

| 11020000 | 16-byte bit map of AL_PAs |

The loop master primes the bit map with zeroes (0) then sends the bit map around the loop in the LIFA payload.

<table>
<thead>
<tr>
<th>Example</th>
<th>The loop master places zeroes in the bit map corresponding to each bit position.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Bit position</td>
<td>0</td>
</tr>
<tr>
<td>AL_PA positions</td>
<td>00</td>
</tr>
<tr>
<td>Switch address</td>
<td>7E</td>
</tr>
<tr>
<td>Bit map content</td>
<td>L</td>
</tr>
</tbody>
</table>

Any port which has an AL_PA assigned by the fabric will set a ‘1’ in the bit map at the position corresponding to its AL_PA. If a ‘1’ already exists, no map entry is made.

Loop Initialization Previously Assigned (LIPA)

20-byte payload

| 11030000 | 16-byte bit map of AL_PAs |

The loop master transmits the bit map resulting from the LIFA.
The L_Port checks to see if the bit that corresponds to its previously acquired AL_PA is set. If not, the L_Port sets it to ‘1’. If the L_Port’s bit has already been set by another port, it will attempt to assume a soft-assigned AL_PA (LISA).

**Example**
The L_Port had a previously assigned AL_PA of 4 which was not already set by another port, so it set the bit at position 4 to ‘1’.

<table>
<thead>
<tr>
<th>Bit position</th>
<th>0</th>
<th>1</th>
<th>2</th>
<th>3</th>
<th>4</th>
<th>5</th>
<th>6</th>
<th>7</th>
<th>8</th>
<th>...</th>
<th>128</th>
</tr>
</thead>
<tbody>
<tr>
<td>AL_PA positions</td>
<td>--</td>
<td>00</td>
<td>01</td>
<td>02</td>
<td>04</td>
<td>08</td>
<td>0F</td>
<td>10</td>
<td>17</td>
<td>...</td>
<td>EF</td>
</tr>
<tr>
<td>Switch address</td>
<td>--</td>
<td>7E</td>
<td>7D</td>
<td>7C</td>
<td>7B</td>
<td>7A</td>
<td>79</td>
<td>78</td>
<td>77</td>
<td>...</td>
<td>00</td>
</tr>
<tr>
<td>Bit map content</td>
<td>L</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>...</td>
<td>0</td>
</tr>
</tbody>
</table>

The L_Port at AL_PA 4 claims its’ previously assigned address.

The L_Port then retransmits the LIPA frame.

**Loop Initialization Hard Assigned (LIHA)**

**20-byte payload**

```
11040000  16-byte bit map of AL_PAs
```

The loop master transmits the bit map resulting from the LIPA.

The L_Port checks to see if the bit that corresponds to its hard address is set (usually using switches or jumpers). If not, the L_Port sets it to ‘1’. If the L_Port’s bit has already been set by another port, it will attempt to assume a soft-assigned AL_PA (LISA).

**Example**
The L_Port had a hard address set by switch or jumper at AL_PA 8 which was not already set by another port, so it set the bit at position 8 to ‘1’.

<table>
<thead>
<tr>
<th>Bit position</th>
<th>0</th>
<th>1</th>
<th>2</th>
<th>3</th>
<th>4</th>
<th>5</th>
<th>6</th>
<th>7</th>
<th>8</th>
<th>...</th>
<th>128</th>
</tr>
</thead>
<tbody>
<tr>
<td>AL_PA positions</td>
<td>--</td>
<td>00</td>
<td>01</td>
<td>02</td>
<td>04</td>
<td>08</td>
<td>0F</td>
<td>10</td>
<td>17</td>
<td>...</td>
<td>EF</td>
</tr>
<tr>
<td>Switch address</td>
<td>--</td>
<td>7E</td>
<td>7D</td>
<td>7C</td>
<td>7B</td>
<td>7A</td>
<td>79</td>
<td>78</td>
<td>77</td>
<td>...</td>
<td>00</td>
</tr>
<tr>
<td>Bit map content</td>
<td>L</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>...</td>
<td>0</td>
</tr>
</tbody>
</table>

The L_Port at AL_PA 8 claims its’ preferred (hard-assigned) address.

The L_Port then retransmits the LIHA frame.

**Loop Initialization Soft Assigned (LISA)**

**20-byte payload**

```
11050100  16-byte bit map of AL_PAs
```

The loop master transmits the bit map resulting from the LIHA.
The L_Port checks to see if there are any free addresses left in the bit map by checking for the first available ‘0’. The L_Port sets it to ‘1’. If there are no zeroes in the bit map, all 126 NL_Port addresses have been taken and the port is not allowed to actively participate in the loop.

The L_Port then retransmits the LISA frame.

**Loop Initialization Report Position (LIRP)**

**132-byte payload**

<table>
<thead>
<tr>
<th>11060000</th>
<th>128-byte map of AL_PA physical positions</th>
</tr>
</thead>
</table>

The LIRP and LILP sequences build a mapping between AL_PA values and participating L_Port positions. This allows all L_Ports to know where all of the other AL_PA addresses are physically located on the loop.

To build this address map, the loop master primes the 128-byte position map with ‘FF’ in each position. The loop master then sets byte 0 (the “counter” byte) of the position map to ‘01’ and places its own AL_PA in byte 1 and then sends the position map around the loop.

Each port increments the position counter (byte 0) by one and places its AL_PA at the map position indicated by the counter. For example, the second device on the loop (with the loop master counted as the first device) increments byte 0 by one to make byte 0 have a value of ‘2’ and then places its AL_PA in position 2 of the position map.

Each port retransmits the LIPA frame until the map completes its journey to each of the devices on the loop and gets back to the loop master. At this point, the LIRP process results in a position map containing the physical location and AL_PA of every device on the loop.

**Example**

This example position map shows that there are 6 ports on the loop. The loop master has AL_PA ‘E8’. The first port after the master on the outbound fibre has AL_PA ‘D6’ and the last port on the loop before getting back to the master has AL_PA ‘08’.

<table>
<thead>
<tr>
<th>Byte map content</th>
<th>06</th>
<th>E8</th>
<th>D6</th>
<th>E2</th>
<th>6A</th>
<th>5F</th>
<th>08</th>
<th>FF</th>
<th>...</th>
<th>FF</th>
</tr>
</thead>
</table>

First available ‘0’ changed to ‘1’.

Example: The L_Port saw that the first ‘0’ occurred in bit position 1, so it changed that bit to a ‘1’ to effectively select ‘1’ as it’s AL_PA.
Loop Initialization Loop Position (LILP)

132-byte payload

| 11070000 | 128-byte map of AL_PA physical positions |

When the loop master gets the LIRP frame back from the loop, it contains the AL_PAs and physical location of each participating port on the loop. The loop master retransmits the completed position map (now called the LILP) to the next port on the loop. This informs all ports of the loop’s physical make-up and also makes it possible for each port to make a copy of the map before retransmitting it to the next port.

When the loop master gets the LILP back from the loop, it sends a CLS followed by Idles. All of the other ports retransmit the CLS and transition to the Monitoring state. When the loop master gets the CLS back from the loop, it removes it and then transitions to the Monitoring state. This signals the end of the initialization process.

Example

This example LILP map shows that there are 6 ports on the loop. The hex values of the AL_PAs are in the appropriate positions indicating the physical order of the ports on the loop.

| Byte map content | 06 | E8 | D6 | E2 | 6A | 5F | 08 | FF | ... | FF |

Detailed loop initialization explanation

1. Select initial AL_PA
   - Each FL_Port selects an AL_PA hex value of ‘00’.
   - Each NL_Port selects an AL_PA hex value of ‘EF’.

2. Select the loop master
   - Each L_Port continuously transmits a LISM loop initialization sequence with the D_ID and S_ID fields set to hex ‘0000xx’ (where ‘xx’ is its initial AL_PA) and its Port_Name in the payload.
   - Each L_Port monitors its receiver and proceeds as follows:
     a. If the L_Port receives a LISM loop initialization sequence that is the same as the one it transmits, it becomes the loop master and continues at step 3.
     b. If the L_Port received a LISM loop initialization sequence that is not the same as the one it transmits, the L_Ports checks the D_ID and payload as follows:
        i. If the L_Port is an FL_Port and the received D_ID = hex ‘000000’, the loop initialization sequence is from another FL_Port. If its Port_Name is algebraically:
           - lower than the Port_Name in the payload, the FL_Port transmits a LISM loop initialization sequence with the payload containing its own Port_Name.
           - higher than the Port_name in the payload, the FL_Port retransmits the same LISM that it received and goes to the MONITORING state in nonparticipating mode (another FL_Port won the role of loop master).
        ii. If the L_Port is an FL_Port and the received D_ID is not equal to hex ‘000000’, the FL_Port discards the received sequence. This allows an FL_Port to become the loop master.
        iii. If the L_Port is a NL_Port and the received D_ID equals hex ‘000000’, the NL_Port retransmits the received Loop Initialization Sequence. This allows an FL_Port to become the loop master.
        iv. If the L_Port is an NL_Port and the received D_ID is not equal to hex ‘000000’, the Loop Initialization Sequence is from another NL_Port. If its Port_Name is algebraically:

---

1. Frames are sent continuously because they may be discarded by any L_Port that does not have a receive buffer available (flow control is not used during initialization).
- lower than the Port_Name in the payload, the NL_Port transmits a LISM loop initialization sequence with the payload containing its Port_Name.
- higher than the Port_name in the payload, the NL_Port retransmits the received Loop Initialization Sequence.

Each L_Port continues with steps 2a through 2d.

c. If the L_Port receives an ARB(F0), it continues at step 4.
d. If it receives anything else, the value is discarded and the port continues with steps 2a to 2d.

3. Loop master — transmit remaining Loop Initialization Sequences

a. The loop master continuously transmits ARB(F0)s until it receives its own ARBx (ARB(F0)).
b. The L_Port transmits the LIFA, LIPA, LIHA, and LISA loop initialization sequences. These sequences contain a 16-byte AL_PA bit map in the payload. Each bit represents one AL_PA. See figure 10 and tables 13 and 17.

Table 17: Loop initialization sequence AL_PA bit map

<table>
<thead>
<tr>
<th>Bits</th>
<th>3322 2222 2222 1111 1111 11 1098 7654 3210</th>
</tr>
</thead>
<tbody>
<tr>
<td>Word</td>
<td>0</td>
</tr>
<tr>
<td></td>
<td>1</td>
</tr>
<tr>
<td></td>
<td>2</td>
</tr>
<tr>
<td></td>
<td>3</td>
</tr>
</tbody>
</table>

Except for the L bit, each bit in table Table 17 represents a valid AL_PA. The L bit is set by the FL_Port or F/NL_Port to indicate that the configuration has changed. Setting the L bit implicitly logs out all NL_Ports. Private NL_Ports are implicitly logged out since the public NL_Ports with which they may have been communicating, may have a new AL_PA.

The loop master transmits the four loop initialization sequences that contain the 16-byte AL_PA bit maps as follows:

**LIFA**
The L_Port primes the AL_PA bit map with binary zero (0) and sets the bit that corresponds to its Fabric Assigned AL_PA to one (1). If the L_Port is an FL_Port, it sets the bit associated with AL_PA 00h. The L_bit is set if this is the first initialization attempt of an FL_Port or of an NL_Port that has assumed the role of an F/NL_Port.

**LIPA**
The L_Port primes the AL_PA bit map with the AL_PA bit map of the previous LIFA loop initialization sequence. The L_Port checks to see if the bit that corresponds to its previously acquired AL_PA is set. If it is not set to 1, the L_Port sets the bit (unless a bit was set in LIFA); if the bit is already set to 1, the L_Port assumes a soft assigned AL_PA.

**LIHA**
The L_Port primes the AL_PA bit map with the AL_PA bit map of the previous LIPA loop initialization sequence. The L_Port checks to see if the bit that corresponds to its hard assigned AL_PA is set. If it is not set to 1, the L_Port sets the bit (unless a bit was set in LIFA or LIPA); if the bit is already set to 1, the L_Port assumes a soft assigned AL_PA.

If ESI activity is underway when the request for the hard address is received, the drive shall use the last known value of the hard address before the current ESI activity started. For more information on ESI, refer to Section 10.5.

**LISA**
The L_Port primes the AL_PA bit map with the AL_PA bit map of the previous LIHA loop initialization sequence.
sequence. The L_Port sets the first available bit to 1 (unless a bit was set in LIFA, LIPA, or LIHA) which corresponds to its soft assigned AL_PA. If a bit was available, the L_Port adjusts its AL_PA according to which bit it set. If no bits are available, the L_Port remains in the nonparticipating mode; the L_Port may attempt to re-initialize at the request of the node. If the L_Port does not support the AL_PA position mapping loop initialization sequences, it sets byte 2 of the loop initialization identifier to 00h.

c. When the loop master receives the LISA sequence, it checks the loop initialization identifier value. If the value is 11050100h, the loop master transmits two additional loop initialization sequences as follows:

LIRP
The L_Port sets the AL_PA position map to all hex 'FF', enters an offset of 01h followed by its AL_PA. For example, if AL_PA = 05h, the AL_PA position map contains 0105FFFFFFFF...FFh.

LILP
The L_Port transmits the AL_PA position map of the previous LIRP loop initialization sequence.

d. When the last loop initialization sequence (identifier = LISA or LILP) is returned, the loop master transmits CLS to place all L_Ports into Monitoring state. When the loop master received CLS, the L_Port makes the transition to the Monitoring state and relinquishes its loop master role. At this time, all possible AL_PA values have been assigned for the number of L_Ports and every L_Port that has a valid AL_PA is in participating mode.

If any frame is received that is not formatted according to figure 10, the frame is discarded and the loop master restarts initialization at step 3b.

The loop master uses the E_D_TOV timer to wait for each of the above loop initialization sequences and the CLS. If the timer expires before each transmitted loop initialization sequence of CLS is received, the L_Port goes to the Initializing state. The L_Port continues at step 5.

4. Non loop master L_Port—select unique AL_PA
A non loop master L_Port retransmits any received ARB(F0)s and prepares to receive (e.g. empties its receive buffers) and retransmits the following LIFA, LIPA, LIHA, LISA, LIRP, and LILP loop initialization sequences followed by CLS. The loop initialization sequences contain a 16-byte AL_PA bit map in the payload. Each bit represents one AL_PA (see figure 10 and tables 17 and 14).

LIFA
The L_Port checks to see if the bit that corresponds to its fabric-assigned AL_PA is set. If it is not set to 1, the L_Port sets the bit; if the bit is already set to 1, the L_Port assumes a soft-assigned AL_PA. The L_Port retransmits the loop initialization sequence.

LIPA
The L_Port checks to see if the bit that corresponds to its previously-acquired AL_PA is set. If it is not set to 1, the L_Port sets the bit; if the bit is already set to 1, the L_Port assumes a soft-assigned AL_PA. The L_Port retransmits the loop initialization sequence.

LIHA
The L_Port checks to see if the bit that corresponds to its hard-assigned AL_PA is set. If it is not set to 1, the L_Port sets the bit (unless a bit was set in LIFA or LIPA); if the bit is already set to 1, the L_Port assumes a soft-assigned AL_PA. The L_Port retransmits the loop initialization sequence.
To get the hard address, the drive must abort all Enclosure Services Interface (ESI) activity that may be in process. Both ESI initiated by a receive or send diagnostic command and Enclosure Initiated ESI will be aborted. Refer to Section 10.0 for more information on ESI.

LISA
The L_Port sets the first available bit to 1 (unless a bit was set in LIFA, LIPA, or LIHA above) that corresponds to its soft-assigned AL_PA. If a bit was available, the L_Port adjusts its AL_PA according to which bit was set. If no bits are available, the L_Port remains in nonparticipating mode; the L_Port may attempt to reinitialize at 10.3 at the request of the node. If the L_Port does not support the AL_PA position mapping loop initialization sequences, it sets byte 2 of the loop initialization identifier to 00h. The L_Port retransmits the loop initialization sequence.
LIRP
If LIRP is received, the L_Port reads the left-most byte (offset), increment it by one, store the offset, and store its AL_PA into the offset position. The L_Port retransmits the loop initialization sequence.

LILP
If LILP is received, the L_Port may use the AL_PA position map to save the relative positions of all L_Ports on the loop. This information may be useful for error recovery. The L_Port retransmits the loop initialization sequence.

If any frame is received that is not formatted according to figure 10 and as specified in step 3 on page 55, the frame is discarded. If a LIRP or LILP frame is received by an L_Port which does not support the AL_PA position map, the frame is discarded.

Each L_Port uses the E_D_TOV timer to wait for each of the above loop initialization sequences and the CLS. If the timer expires before each loop initialization sequence of CLS is received, the L_Port goes to the Initializing state. One possible reason for this is that the loop master was removed from the loop.

When CLS is received, the L_Port retransmits CLS and goes to the Monitoring state in participating mode (if it acquired a valid AL_PA).

The L_Port continues at step 5.

5. Select final AL_PA and exit initialization

a. If an FL_Port is in participating mode, it has completed initialization with an AL_PA of 00h and exits the loop initialization.

b. If a private NL_Port is in participating mode, the NL_Port has completed initialization with an AL_PA in the range of 01h - EFh and exits loop initialization. If during initialization, the NL_Port detected that the L_bit (Login required) was set to 1, it implicitly logs out with all other NL_Ports.

c. If a public NL_Port is in participating mode, the NL_Port has discovered an AL_PA in the range of 01h - EFh. If one of the following occurred, the NL_Ports implicitly logout with all ports and attempt a fabric login to the address FFFFFFFEh - AL_PA 00h:
   • the NL_Port detected that the L_bit (login required) was set to 1 in a LIFA, LIPA, LIHA, or LISA loop initialization sequence;
   • the NL_Port was unable to set to 1 its fabric-assigned AL_PA bit or its previously-acquired AL_PA bit in the LIFA or LIPA loop initialization sequence (i.e., another NL_Port is using the AL_PA);
   • the NL_Port has not previously executed a fabric login.

Normal responses to a fabric login request are:
• the transmitted OPN(00,AL_PS) and login extended link service sequence are returned to the NL_Port. No L_Port on the loop has accepted this request. The NL_Port sets its native address identifier to 0000xxh (where xx is its AL_PA).

If the NL_Port is capable of providing fabric services in the absence of an FL_Port (i.e., it recognizes the well-known alias address FFFFFFFEh as well as its own native address identifier), this NL_Port (also known as an F/NL_Port) recognizes OPN(00,x) in addition to its own AL_PA. If this is the first time that the NL_Port is assuming the responsibility of an F/NL_Port, to ensure that all previous login requests are reset, the F/NL_Port goes to the Initializing state (REQ(initialize)) and sets the L_bit (login required) to 1 in the LIFA loop initialization sequence.

Note. To prevent another L_Port from winning arbitration, this F/NL_Port should not relinquish control of the loop until it is prepared to receive OPN(00,AL_PS).

If the NL_Port is not capable of becoming an F/NL_Port, the NL_Port exits loop initialization.
• the NL_Port receives an Accept (ACC) link service sequence. The NL_Port uses the D_ID in the ACC sequence as its native address identifier and bits 7 - 0 of the D_ID as its fabric-assigned AL_PA. The NL_Port compares the fabric-assigned AL_PA in the ACC sequence with the AL_PA acquired prior to step 5. If they are equal, the NL_Port exits loop initialization. If they are not equal, the NL_Port goes to the Initializing state (REQ(initialize)) to re-initialize and acquire the fabric-assigned AL_PA value.
8.2.1 Loop initialization state machine

The loop initialization process is used whenever any unusual event occurs on the loop. One such event would be inserting a new NL_Port into the loop. Invoking the loop initialization process permits the new NL_Port to acquire an AL_PA so it can begin operations.

Figure 11. Loop initialization state machine
8.2.2 Loop reinitialization

Loop reinitialization occurs when an L_Port is added to the loop, removed from the loop, or for error recovery.

Figure 12. Loop state machine (simplified)

8.3 Accessing another L_Port

Each port has its own private arbitration primitive (ARBx) signal. Each port uses this ARBx signal to arbitrate for and win access rights to the loop. This must be done before communicating with another port.

When an L_Port is not communicating with another port, it is in a monitoring state to see if some other L_Port is trying to communicate with it. The L_Port is also retransmitting the stream of transmission words it is receiving. If a port needs to communicate, it sends out its arbitration primitive signal by replacing the fill words\(^1\) between frames. If the arbitration primitive signal (ARBx) travels completely around the loop without being

---

1.Fill words may be Idles, ARBx’s, or ARB(F0)’s. Fill words are transmitted between frames and may be deleted for clock skew management purposes.
replaced, that port has won arbitration of the loop and is free to open the loop between its receiver and transmitter and is also free to stop retransmitting received transmission words. This means the NL_Port is no longer in repeat mode and all words transmitted on its outbound fiber are generated by the NL_Port.

When operating in full duplex mode, the port that wins arbitration sends out a special primitive signal called OPNyx to select a destination port on the loop and to identify the port sending the OPNyx primitive signal. The “y” value of OPNyx is the arbitrated loop physical address of the destination device (AL_PD). If a port receives an OPNyx and recognizes its AL_PD, the L_Port opens the loop at its L_Port. This L_Port and the one that sent the OPNyx begin normal FC-2 protocols.

When the port that won arbitration and sent the OPNyx to initiate communication between itself and another L_Port wants to close communication with the port, it uses another primitive signal called close (CLS) to signal the other port of its intent to close the loop. The receiving L_Port finishes its work and then transmits a CLS back to the originating L_Port. At this point, the two ports return to the monitoring state, and other L_Ports can start communicating.

When operating in half duplex mode, OPNyx only identifies the destination port; therefore, the sending port cannot be determined.

8.3.1 Access fairness

So what happens when two or more ports happen to request access to the loop at exactly the same time? Or what happens when one port has already won access to the loop and others then want to arbitrate to win access? The answers lie in the access fairness algorithm that most NL_Ports on a loop use. This access fairness algorithm ensures that all participating NL_Ports will have equal access to the loop. NL_Ports that use the access fairness algorithm are called “fair” NL_Ports. Seagate Fibre Channel drives fully implement the access fairness algorithm.

Remember that each L_Port can continuously arbitrate to access the loop. Each L_Port has a priority assigned to it based on its’ Arbitrated Loop Physical Address (AL_PA). AL_PA “01” has the highest priority for an NL_Port and AL_PA “EF” has the lowest priority (except for the special arbitration primitive signal ARB(F0) discussed below). See section 8.1 for additional information about AL_PAs. The access fairness algorithm creates an access window in which all L_Ports are given an opportunity to arbitrate and win access to the loop regardless of its’ assigned priority. After all L_Ports have had an opportunity to win access to the loop, a new access window is started; however, all L_Ports don’t have to actually choose to win access, they just have to be given the opportunity to win access in each access window.

When a fair L_Port has arbitrated for and won access to the loop, that L_Port will not arbitrate again until it receives at least one Idle. The time between the first L_Port to win arbitration and transmitting an Idle is an access window. A special arbitration Primitive Signal (ARB(F0)) prevents the access window from being reset too early.

When a fair NL_Port has arbitrated for and won access to the loop and does not detect that another L_Port is arbitrating, it may keep the existing circuit open indefinitely or close the circuit and retain control of the loop (without rearbitrating) to open another L_Port on the loop.

How does the open NL_Port know when another port is arbitrating? The open NL_Port transmits ARB(F0) primitive sequences and monitors to see if its ARB(F0) is replaced by a higher priority address. Since xF0 is the lowest-priority address, any other NL_Port that is arbitrating will replace the ARB(F0) with its’ ARBx (which will always be higher-priority than ARB(F0)) and the highest priority arbitrating L_Port will win arbitration. If the OPEN L_port receives the ARB(F0) back it knows that no other NL_Port is arbitrating for the loop.

Once an L_Port has won access to the loop, it may retain control of the loop indefinitely; however, if access is denied longer than the Error Detect Timeout Value (E_D_TOV), the offended L_Port can reset the access window to force arbitration. When a Seagate drive is operating as a target, it closes the loop when it has transmitted all of the frames it needed to send. Seagate drives do not hold the loop open to monitor the input stream for ARBs.
When a fair NL_Port has arbitrated for and won access to the loop and does detect that another L_Port is arbitrating, the NL_Port closes the loop at the earliest possible time and arbitrates again in the next access window before opening a different L_Port.

### 8.3.2 Access unfairness

Some loops may require that certain NL_Ports have more access to the loop than just one access per access window. Examples of this situation include an NL_Port for a subsystem controller or file server. Any NL_Port can be initialized to not use the fairness algorithm. If this occurs, the NL_Port is called an “unfair” NL_Port. An NL_Port can also be configured to be temporarily unfair.

When an unfair L_Port has arbitrated for and won access to the loop and does not detect that another L_Port is arbitrating, that L_Port may keep the existing circuit open indefinitely or close that circuit and retain ownership of the loop without re-arbitrating to open another L_Port on the loop.

When an unfair NL_Port controls the loops and detects that another L_Port is arbitrating, the unfair NL_Port may close the loop at the earliest possible time.

If you have a public loop connected to a fabric, the participating FL_Port is always the highest priority L_Port on the loop based on its AL_PA value of 00h (see Section 8.1).

**Note.** There can be only one participating FL_Port on any one loop. Additional FL_Ports can be present, but they will be in nonparticipating mode.

The FL_Port doesn’t use the access fairness algorithm because it must control communications with the attached fabric. This means the FL_Port will always win arbitration.

### 8.3.3 Clock skew management

Each loop port receives an input stream from the port upstream that is based on the clock frequency of that upstream device. The clock frequency of the upstream device may be slightly faster or slower than the frequency the loop port is using to transmit data. Each loop port captures data using the receive clock frequency and retimes it to its own transmit clock before forwarding the data. Over time, if the receive clock frequency is faster than the transmit frequency, data will start backing up in the port. If the receive clock frequency is slower than the transmit frequency, the port may run out of data.

To compensate for this difference in clock frequencies, each loop port contains an Elasticity (smoothing) FIFO (First In, First Out) buffer that allows words to be inserted or deleted as necessary to account for this difference in clock frequencies. Only fill words such as Idles or ARB’s may be inserted or deleted during times when frames or R_RDYs are not being transmitted. Each port originating frames is required to transmit at least six fill words between frames. This gives all monitoring loop ports an opportunity to insert or delete the fill words. A minimum of two fill words must be left between frames when it arrives at its destination.

### 8.4 Loop ports

A loop port (L_Port) is a port designed specifically to operate in a Fibre Channel Arbitrated Loop topology. NL_Ports have special additional functions which permit them to operate in the arbitrated loop topology as well as in the point-to-point physical topology when attached to an N_Port or an F_Port. N_Ports are designed for point-to-point physical topologies, and F_Ports are designed for fabric topologies.

#### 8.4.1 Maximum number of NL_Ports

Up to 126 NL_Ports may be participating on a single arbitrated loop. There can be more than 126 NL_Ports attached to the loop, but only 126 will be able to obtain a valid arbitrated loop physical address (AL_PA). Only one (rare) or two NL_Ports communicate at any one time, except during loop initialization. Only one NL_Port can send frames but several NL_Ports may copy that frame into their buffer.
8.4.2 Blocking switch emulation

When two NL_Ports open communication with each other (see the OPNyx discussion in Section 8.3), communication between other devices is effectively blocked (other than to retransmit frames or insert fill words). This is known as a blocking environment since the two communicating ports block operation between any other L_Ports.

8.4.3 Non-meshed environment

FC-AL is called a non-meshed environment due to the fact that there is only one route to any other port. A meshed environment (like a fabric) is like a telephone system in which there are many routes possible between two communicating entities.
8.4.4 Assigned AL_PA values

All AL_PAs that are used in the loop protocol are listed in table 13. The AL_PAs are assigned to the 16-byte AL_PA bit maps of table 17 as shown in table 18.

Table 18: AL_PA mapped to bit maps

<table>
<thead>
<tr>
<th>AL_PA (hex)</th>
<th>Bit map Word</th>
<th>Bit map Bit</th>
<th>AL_PA (hex)</th>
<th>Bit map Word</th>
<th>Bit map Bit</th>
<th>AL_PA (hex)</th>
<th>Bit map Word</th>
<th>Bit map Bit</th>
<th>AL_PA (hex)</th>
<th>Bit map Word</th>
<th>Bit map Bit</th>
</tr>
</thead>
<tbody>
<tr>
<td>00</td>
<td>0</td>
<td>30</td>
<td>43</td>
<td>1</td>
<td>30</td>
<td>74</td>
<td>2</td>
<td>30</td>
<td>B4</td>
<td>3</td>
<td>30</td>
</tr>
<tr>
<td>01</td>
<td>0</td>
<td>29</td>
<td>45</td>
<td>1</td>
<td>29</td>
<td>75</td>
<td>2</td>
<td>29</td>
<td>B5</td>
<td>3</td>
<td>29</td>
</tr>
<tr>
<td>02</td>
<td>0</td>
<td>28</td>
<td>46</td>
<td>1</td>
<td>28</td>
<td>76</td>
<td>2</td>
<td>28</td>
<td>B6</td>
<td>3</td>
<td>28</td>
</tr>
<tr>
<td>04</td>
<td>0</td>
<td>27</td>
<td>47</td>
<td>1</td>
<td>27</td>
<td>79</td>
<td>2</td>
<td>27</td>
<td>B9</td>
<td>3</td>
<td>27</td>
</tr>
<tr>
<td>08</td>
<td>0</td>
<td>26</td>
<td>49</td>
<td>1</td>
<td>26</td>
<td>7A</td>
<td>2</td>
<td>26</td>
<td>BA</td>
<td>3</td>
<td>26</td>
</tr>
<tr>
<td>0F</td>
<td>0</td>
<td>25</td>
<td>4A</td>
<td>1</td>
<td>25</td>
<td>7C</td>
<td>2</td>
<td>25</td>
<td>BC</td>
<td>3</td>
<td>25</td>
</tr>
<tr>
<td>10</td>
<td>0</td>
<td>24</td>
<td>4B</td>
<td>1</td>
<td>24</td>
<td>80</td>
<td>2</td>
<td>24</td>
<td>C3</td>
<td>3</td>
<td>24</td>
</tr>
<tr>
<td>17</td>
<td>0</td>
<td>23</td>
<td>4C</td>
<td>1</td>
<td>23</td>
<td>81</td>
<td>2</td>
<td>23</td>
<td>C5</td>
<td>3</td>
<td>23</td>
</tr>
<tr>
<td>18</td>
<td>0</td>
<td>22</td>
<td>4D</td>
<td>1</td>
<td>22</td>
<td>82</td>
<td>2</td>
<td>22</td>
<td>C6</td>
<td>3</td>
<td>22</td>
</tr>
<tr>
<td>1B</td>
<td>0</td>
<td>21</td>
<td>4E</td>
<td>1</td>
<td>21</td>
<td>84</td>
<td>2</td>
<td>21</td>
<td>C7</td>
<td>3</td>
<td>21</td>
</tr>
<tr>
<td>1D</td>
<td>0</td>
<td>20</td>
<td>51</td>
<td>1</td>
<td>20</td>
<td>88</td>
<td>2</td>
<td>20</td>
<td>C9</td>
<td>3</td>
<td>20</td>
</tr>
<tr>
<td>1E</td>
<td>0</td>
<td>19</td>
<td>52</td>
<td>1</td>
<td>19</td>
<td>8F</td>
<td>2</td>
<td>19</td>
<td>CA</td>
<td>3</td>
<td>19</td>
</tr>
<tr>
<td>1F</td>
<td>0</td>
<td>18</td>
<td>53</td>
<td>1</td>
<td>18</td>
<td>90</td>
<td>2</td>
<td>18</td>
<td>CB</td>
<td>3</td>
<td>18</td>
</tr>
<tr>
<td>23</td>
<td>0</td>
<td>17</td>
<td>54</td>
<td>1</td>
<td>17</td>
<td>97</td>
<td>2</td>
<td>17</td>
<td>CC</td>
<td>3</td>
<td>17</td>
</tr>
<tr>
<td>25</td>
<td>0</td>
<td>16</td>
<td>55</td>
<td>1</td>
<td>16</td>
<td>98</td>
<td>2</td>
<td>16</td>
<td>CD</td>
<td>3</td>
<td>16</td>
</tr>
<tr>
<td>26</td>
<td>0</td>
<td>15</td>
<td>56</td>
<td>1</td>
<td>15</td>
<td>9B</td>
<td>2</td>
<td>15</td>
<td>CE</td>
<td>3</td>
<td>15</td>
</tr>
<tr>
<td>27</td>
<td>0</td>
<td>14</td>
<td>59</td>
<td>1</td>
<td>14</td>
<td>9D</td>
<td>2</td>
<td>14</td>
<td>D1</td>
<td>3</td>
<td>14</td>
</tr>
<tr>
<td>29</td>
<td>0</td>
<td>13</td>
<td>5A</td>
<td>1</td>
<td>13</td>
<td>9E</td>
<td>2</td>
<td>13</td>
<td>D2</td>
<td>3</td>
<td>13</td>
</tr>
<tr>
<td>2A</td>
<td>0</td>
<td>12</td>
<td>5C</td>
<td>1</td>
<td>12</td>
<td>9F</td>
<td>2</td>
<td>12</td>
<td>D3</td>
<td>3</td>
<td>12</td>
</tr>
<tr>
<td>2B</td>
<td>0</td>
<td>11</td>
<td>63</td>
<td>1</td>
<td>11</td>
<td>A3</td>
<td>2</td>
<td>11</td>
<td>D4</td>
<td>3</td>
<td>11</td>
</tr>
<tr>
<td>2C</td>
<td>0</td>
<td>10</td>
<td>65</td>
<td>1</td>
<td>10</td>
<td>A5</td>
<td>2</td>
<td>10</td>
<td>D5</td>
<td>3</td>
<td>10</td>
</tr>
<tr>
<td>2D</td>
<td>0</td>
<td>9</td>
<td>66</td>
<td>1</td>
<td>9</td>
<td>A6</td>
<td>2</td>
<td>9</td>
<td>D6</td>
<td>3</td>
<td>9</td>
</tr>
<tr>
<td>2E</td>
<td>0</td>
<td>8</td>
<td>67</td>
<td>1</td>
<td>8</td>
<td>A7</td>
<td>2</td>
<td>8</td>
<td>D9</td>
<td>3</td>
<td>8</td>
</tr>
<tr>
<td>31</td>
<td>0</td>
<td>7</td>
<td>69</td>
<td>1</td>
<td>7</td>
<td>A9</td>
<td>2</td>
<td>7</td>
<td>DA</td>
<td>3</td>
<td>7</td>
</tr>
<tr>
<td>32</td>
<td>0</td>
<td>6</td>
<td>6A</td>
<td>1</td>
<td>6</td>
<td>AA</td>
<td>2</td>
<td>6</td>
<td>DC</td>
<td>3</td>
<td>6</td>
</tr>
<tr>
<td>33</td>
<td>0</td>
<td>5</td>
<td>6B</td>
<td>1</td>
<td>5</td>
<td>AB</td>
<td>2</td>
<td>5</td>
<td>E0</td>
<td>3</td>
<td>5</td>
</tr>
<tr>
<td>34</td>
<td>0</td>
<td>4</td>
<td>6C</td>
<td>1</td>
<td>4</td>
<td>AC</td>
<td>2</td>
<td>4</td>
<td>E1</td>
<td>3</td>
<td>4</td>
</tr>
<tr>
<td>35</td>
<td>0</td>
<td>3</td>
<td>6D</td>
<td>1</td>
<td>3</td>
<td>AD</td>
<td>2</td>
<td>3</td>
<td>E2</td>
<td>3</td>
<td>3</td>
</tr>
<tr>
<td>36</td>
<td>0</td>
<td>2</td>
<td>6E</td>
<td>1</td>
<td>2</td>
<td>AE</td>
<td>2</td>
<td>2</td>
<td>E4</td>
<td>3</td>
<td>2</td>
</tr>
<tr>
<td>39</td>
<td>0</td>
<td>1</td>
<td>71</td>
<td>1</td>
<td>1</td>
<td>B1</td>
<td>2</td>
<td>1</td>
<td>E8</td>
<td>3</td>
<td>1</td>
</tr>
<tr>
<td>3A</td>
<td>0</td>
<td>0</td>
<td>72</td>
<td>1</td>
<td>0</td>
<td>B2</td>
<td>2</td>
<td>0</td>
<td>EF</td>
<td>3</td>
<td>0</td>
</tr>
</tbody>
</table>

Notes: ‘--’ is reserved for the L-bit (login required)
AL_PA = ‘00’ is reserved for the FL_Port.
9.0 Fibre Channel link services

Link service frames are used to perform functions at the Fibre Channel layer. They are used to establish the operating parameters, perform channel level error recovery, and check the status of the physical link between two devices. Link service frames are divided into two groups, Basic and Extended.
9.1 Basic link services

The drive supports the Abort Sequence (ABTS) and two basic services replies, Basic Accept (BA_ACC) and Basic Reject (BA_RJT). All other basic link services are discarded by the drive. Basic link service functions are identified by the R_CTL field of the header.

Table 19: Basic link services header

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit Byte</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>R_CTL</td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>D_ID</td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reserved (00h)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>4</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>S_ID</td>
<td></td>
<td></td>
</tr>
<tr>
<td>5</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>6</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Type (00)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>7</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>F_CTL</td>
<td></td>
<td></td>
</tr>
<tr>
<td>8</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>9</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>SEQ_ID</td>
<td></td>
<td></td>
</tr>
<tr>
<td>10</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>DF_CTL (00)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>11</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>SEQ_CNT</td>
<td></td>
<td></td>
</tr>
<tr>
<td>12</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>13</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>OX_ID</td>
<td></td>
<td></td>
</tr>
<tr>
<td>14</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>RX_ID (FFFFh)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>15</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Parameter</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

R_CTL (Routing Control)
The basic link service frame. See the description of each basic link service below for the R_CTL value.

D_ID (Destination Identifier)
The address of the drive for ABTS. This value must match the current address of the drive. For the basic link service replies, the D_ID is the address of the initiator.

S_ID (Source Identifier)
The address of the initiator that sent the frame for ABTS. For the basic link service replies, the S_ID contains the address of
the drive.

**Type**

00h Used for all basic link services frames.

**F_CTL (Frame Control)**

Set to 090000h for the ABTS. This indicates the ABTS is from the originator of the exchange, this is the last frame of the sequence, and sequence initiative is transferred for the drive to send the reply back.

For the reply frames, the drive sets the F_CTL to 990000h. This indicates the frame is from the responder, this is the last sequence, this is the last frame of the sequence, and sequence initiative is returned to the initiator.

**SEQ_ID (Sequence Identifier)**

Contains the last used SEQ_ID for the exchange for ABTS. The drive does not check the SEQ_ID as the entire exchange (command) is aborted by the error recovery process. The drive returns the same SEQ_ID in the reply frame as received from the initiator in the ABTS.

**DF_CTL (Data Field Control)**

Set to 00h to indicate no optional Fibre Channel headers are used.

**SEQ_CNT (Sequence Count)**

Set to 0000h to indicate this is the first frame of the Fibre Channel sequence.

**OX_ID (Originator Exchange Identifier)**

The OX_ID for the sequence being identified by the basic link service.

**RX_ID (Responder Identifier)**

Not used by the drive. The value of FFFFh indicates the RX_ID is not being used.

**Parameter**

Not used for basic link services.

### 9.1.1 Abort Sequence (ABTS)

The Abort Sequence (ABTS) is sent by the initiator to abort a single SCSI exchange (command) or FC exchange (one of the link service operations). The ABTS frame does not have a payload. All the information is included in the header.

The R_CTL is 81h.
9.1.2 Basic Accept (BA_ACC)

BA_ACC is sent by the drive in response to all correctly structured ABTS. If the ABTS identifies an exchange in execution or buffered for execution, the drive will discard the exchange.

The R_CTL is 84h.

Table 20: BA_ACC Payload

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>SEQ_ID Valid</td>
</tr>
<tr>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Last SEQ_ID</td>
</tr>
<tr>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reserved</td>
</tr>
<tr>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reserved</td>
</tr>
<tr>
<td>1</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>MSB</td>
<td>OX_ID</td>
</tr>
<tr>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>LSB</td>
<td>OX_ID</td>
</tr>
<tr>
<td>1</td>
<td>MSB</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>RX_ID</td>
</tr>
<tr>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>LSB</td>
<td>RX_ID</td>
</tr>
<tr>
<td>2</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>MSB</td>
<td>Low Seq_CNT</td>
</tr>
<tr>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>LSB</td>
<td>Low Seq_CNT</td>
</tr>
<tr>
<td>1</td>
<td>MSB</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>High SEQ_CNT</td>
</tr>
<tr>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>LSB</td>
<td>High SEQ_CNT</td>
</tr>
</tbody>
</table>

SEQ_ID Validity (Sequence Identifier Valid)

00h Marks the Last SEQ_ID field as invalid. The SEQ_ID is not used because the error recovery procedure requires the entire exchange (command) to be aborted.

80h Marks the Last SEQ_ID field as invalid. The SEQ_ID is not used because the error recovery procedure requires the entire sequence to be aborted.

Last SEQ_ID (Last Sequence Identifier)

Not used. Invalid (don’t care) for Abort Exchange or Set to SEQ_ID of last deliverable Sequence received from ABTS Initiator for Abort Sequence

OX_ID Aborted (Originator Exchange Identifier Aborted)

The same value as received in the ABTS.

RX_ID Aborted (Responder Identifier Aborted)

FFFFh.

Lowest SEQ_CNT (Lowest Sequence Count)

0000h Set to 0000h for Abort Exchange or Refer to FC-FS-2 for Abort Sequence

Highest SEQ_CNT (Highest Sequence Count)

FFFFh Set to FFFFh for Abort Exchange or Set to SEQ_CNT of ABTS Frame for Abort Sequence
9.1.3 Basic Reject (BA_RJT)

BA_RJT is sent by the drive in response to an ABTS with a RX_ID not set to FFFFh.

The R_CTL is 85h.

**Table 21: BA_RJT Payload**

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reserved (00h)</td>
</tr>
<tr>
<td>0</td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reason Code</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reason Explanation</td>
</tr>
<tr>
<td>0</td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Vendor Unique</td>
</tr>
</tbody>
</table>

**Reason Code**

03h  (Logical Error) is the only Reason code sent by the drive if the RX_ID sent with the ABTS is not FFFFh.

**Reason Explanation**

03h  No Additional Explanation. This is the only Reason Explanation code sent by the drive.

**Vendor Unique**

Not supported by the drives described in this manual.
9.2 Extended link services

The type of extended link service is identified by the LS Command Code in the first word of the payload. The 
R_CTL field of the frame header identifies whether the extended link service is a request or a reply to a 
request. The accept for extended link services varies with the function. A description of the accept for each 
request is included with the description of the request.

Table 22: Extended link services header

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>3</td>
<td>R_CTL (22h or 23h)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td>D_ID</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>3</td>
<td>Reserved (00h)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td>S_ID</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>3</td>
<td>Type (01h)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td>F_CTL</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>3</td>
<td>SEQ_ID</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td>DF_CTRL (00h)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td>SEQ_CNT</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>3</td>
<td>OX_ID</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td>RX_ID (FFFFh)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>3</td>
<td>Parameter</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**R_CTL (Routing Control)**

22h Extended link services and unsolicited control for extended link service request, PLOGI, PRLI, RLS, RRQ, and 
PDISC.
23h  Extended link services and solicited control for replies, ACC and LS_RJT.

**D_ID (Destination Identifier)**
Frame destination address.

**S_ID (Source Identifier)**
The address of the originator of the frame. This address is used by the destination to return any responses that may be required by the operation.

**Type**
01h  All extended link services frames.

**F_CTL (Frame Control)**
Set to 290000h for extended link service requests. This indicates the frame is from the originator of the exchange, this is the last frame of the sequence, and sequence initiative is transferred for the responder to send the reply back.
For the reply frames, the F_CTL is set to 990000h. This indicates the frame is from the responder, this is the last sequence, this is the last frame of the sequence, and sequence initiative is returned to the originator.

**SEQ_ID (Sequence Identifier)**
Not checked by the drive. For extended link services replies, the drive uses the SEQ_ID value received from the initiator. The drive sends SEQ_ID equal to FFh for extended link services requests it originates.

**DF_CTL (Data Field Control)**
Set to 00 to indicate no optional Fibre Channel headers are used.

**SEQ_CNT (Sequence Count)**
Not checked by the drive. For extended link services replies and requests sent by the drive, SEQ_CNT equals 0000.

**OX_ID (Originator Exchange Identifier)**
The drive sends 0000 to the OX_ID field in extended link services requests it originates. For extended link services replies, the drive uses the OX_ID value received from the initiator.

**RX_ID (Responder Identifier)**
Not used by the drive. The value of FFFh indicates the RX_ID is not being used.

**Parameter**
Not used for extended link services.
9.2.1 Port Login (PLOGI) (02x)

Port Login (PLOGI) is sent by the initiator to a drive to establish the Fibre Channel operating parameters. The PLOGI causes any open exchanges (commands) the initiator may have queued in the drive to be discarded.

Table 23: PLOGI Payload

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit Byte</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td></td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>LS Command Code (03h)</td>
</tr>
<tr>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>00h</td>
</tr>
<tr>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>00h</td>
</tr>
<tr>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>00h</td>
</tr>
<tr>
<td>1 - 4</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>N_Port Common Service Parameters</td>
</tr>
<tr>
<td>5, 6</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Port Name</td>
</tr>
<tr>
<td>7, 8</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Node Name</td>
</tr>
<tr>
<td>9 - 12</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Class 1 Service Parameters</td>
</tr>
<tr>
<td>13 - 16</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Class 2 Service Parameters</td>
</tr>
<tr>
<td>17 - 20</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Class 3 Service Parameters</td>
</tr>
<tr>
<td>21 - 24</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reserved</td>
</tr>
<tr>
<td>25 - 28</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Vendor Version</td>
</tr>
</tbody>
</table>

LS Command Code (Link Services Command Code)
03h PLOGI payload.

N_Port Common Service Parameters
See Table 25 on page 74.

Port Name and Node Name
Used to identify the device. The device may have multiple Fibre Channel ports with each having a unique Port Name. The drive is a dual-ported device.

The Port and Node Names in the PLOGI identify the initiator. The drive saves the Port Name of the initiator with the login parameters. If a change of the Port Name/AL_PA address association is detected during a Port Discovery (PDISC), an implicit logout occurs (any queued commands for the previous Port Name/AL_PA are discarded, the previous login is cleared) and a LS_RJT is returned to the initiator.

The Port and Node Names in the Port Login Accept (PLOGI ACC) identify the drive. The drive uses a format for the Port and Node Names defined as the IEEE extended address. See Table 24.
Table 24: Port/Node Name format

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit 7</th>
<th>Bit 6</th>
<th>Bit 5</th>
<th>Bit 4</th>
<th>Bit 3</th>
<th>Bit 2</th>
<th>Bit 1</th>
<th>Bit 0</th>
</tr>
</thead>
<tbody>
<tr>
<td>x</td>
<td>3</td>
<td>6</td>
<td>5</td>
<td>4</td>
<td>3</td>
<td>2</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td></td>
<td>Network Address ID (02h)</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>N_Port Identifier</td>
<td></td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>(MSB)</td>
<td></td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0</td>
<td>Company Identifier Assigned by IEEE</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>x + 1</th>
<th>3</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
<td></td>
</tr>
</tbody>
</table>

**Company Identifier Assigned by IEEE**
This Seagate-unique value is registered with the IEEE.

**N_Port Identifier**
Used by the drive to identify the name of a specific port or node.
000h  Drive Node Name
100h  Port A
200h  Port B

**Network Address ID**
2h  Defines the name as the IEEE extended format.

**Unique Drive Identifier Assigned by Seagate**
Assigned by Seagate. Uniquely assigned to each disc drive.

**Class 1 and 2 Service Parameters**
May be present in the PLOGI frame. The drive checks only for class 3 service parameters.

**Class 3 Service Parameters**
Required by the drive. See Table 27 on page 76

**Vendor Version**
Vendor-unique.
Table 25: N_Port Common Service Parameters

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit</th>
<th>Byte</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**Highest ANSI FC-PH Version and Lowest ANSI FC-PH Version fields**

The highest and lowest version of the ANSI Fibre Channel Physical and Signaling standards supported by the drive. A version level of 09h is defined for FC-PH, Rev. 4.3. A version level of 20h is defined for FC-PH-3, Rev. 9.4. Earlier drives supported by this manual require 09h in the highest and lowest version fields. An LS_RJT will be returned in response to any PLOGI that does not satisfy this requirement. Later drives do not check the version fields in PLOGI and
return 20h in the PLOGI ACC. The version fields are not considered an accurate indicator of functionality. Reference the drive product manual for specific behavior.

**Buffer to Buffer Credit field**
Not checked by the drive. The drive requires the Alternate Credit Model and assumes a Buffer to Buffer Credit of zero. When the drive opens a device on the loop, it waits until it receives a R_RDY or a Close. The drive returns 0000 in the accept.

**Common Features**
This is a bit significant field which requests options that are used in all classes of service by initiator login. Below is a list of the features and the drive requirements. The drive returns an LS_RJT to PLOGI requests that do not satisfy the requirements.

**Table 26: Common Features bits**

<table>
<thead>
<tr>
<th>Feature</th>
<th>Drive requirement</th>
</tr>
</thead>
<tbody>
<tr>
<td>Continuously Increasing Offset</td>
<td>Must be a one (1).</td>
</tr>
<tr>
<td>Random Relative Offset</td>
<td>Not checked. Port Login Accept returns a value of zero (0). Not supported.</td>
</tr>
<tr>
<td>Valid Vendor Version</td>
<td>X</td>
</tr>
<tr>
<td>F_Port (Fabric Port)</td>
<td>Must be a zero (0) to denote a N_Port.</td>
</tr>
<tr>
<td>Alternate Credit Model</td>
<td>Must be a one (1).</td>
</tr>
<tr>
<td>E_D_TOV Resolution</td>
<td>Not checked. Port Login ACC returns 0.</td>
</tr>
<tr>
<td>Dynamic Half Duplex</td>
<td>Not checked. Port Login ACC returns 0.</td>
</tr>
<tr>
<td>Continuous Increase SEQCNT</td>
<td>Not checked. Port Login ACC returns 0.</td>
</tr>
<tr>
<td>Payload Length</td>
<td>Not checked. Port Login ACC returns 0.</td>
</tr>
</tbody>
</table>

**Receive Data Field Size field**
In the common and class 3 service parameters. Current drives check this field for the range 256 < fs < 2112 and a multiple of four bytes. For multiple frame sequences, all frames but the last frame of the sequence must be this size. The drive uses the receive buffer field size in the class 3 parameters when it sends frames. The drive returns the receive buffer field size in the class 3 parameters from the initiator in the PLOGI ACC.

**Total Concurrent Sequences field**
Number of concurrent sequences across all classes of service. Sequences are concurrent if they are open and delivery verification has not been received. It is the initiator’s responsibility to not issue commands to the drive that will exceed the initiator’s capabilities for concurrent sequences.

The drive returns FFh in Concurrent Sequences field of the PLOGI ACC payload

**Relative Offset by Info Category field**
Indicates on a bit position basis which categories (e.g., solicited control, data descriptor) support the relative offset in the FC header. The drive does not require relative offset and does not check this field in the PLOGI. The drive sends Relative Offset in FCP Data frames (it sets 02h, bit 1 set, in the accept to indicate Relative Offset is supported for solicited data, category 0001b).

**E_D_TOV (Pt to Pt) (Error Detect Time Out Value) field**
This field is only for point-to-point connections and is not valid for loop operation.
<table>
<thead>
<tr>
<th>Word</th>
<th>Bit</th>
<th>Byte</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>3</td>
<td>0</td>
<td>Class Valid</td>
<td>Intermix Mode</td>
<td>Stacked Connect Request</td>
<td>Sequence Delivery</td>
<td>0</td>
<td>Reserved</td>
<td>0</td>
<td>Reserved</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>Service Options</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td></td>
<td></td>
<td>Reserved</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>1</td>
<td>X_ID Reassignment</td>
<td>Initial Process</td>
<td>ACK_0 Capable</td>
<td>ACK_N Capable</td>
<td>0</td>
<td>Reserved</td>
<td>0</td>
<td>Reserved</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>Initiator Control</td>
<td>Associator</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0</td>
<td></td>
<td></td>
<td>Reserved</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>3</td>
<td>0</td>
<td>ACK_0 Capable</td>
<td>ACK_N Capable</td>
<td>X_ID Interlock</td>
<td>Error Policy</td>
<td>Reserved</td>
<td>Categories per Sequence</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>Recipient Control</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td></td>
<td></td>
<td>Reserved</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td></td>
<td></td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>(MSB)</td>
<td>Reserved</td>
<td>Receive Data Field Size</td>
<td>(LSB)</td>
</tr>
<tr>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>3</td>
<td>2</td>
<td>Reserved</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>(MSB)</td>
<td>Concurrent Sequences</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>(MSB)</td>
<td>N_Port End to End Credit</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>3</td>
<td>2</td>
<td>Reserved</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>(MSB)</td>
<td>Open Sequences per Exchange</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td></td>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**Service Options field**

These bits are checked only for class 3 service parameters. The drive returns an LS_RJT to PLOGI requests that do not satisfy the drive’s requirement.
The transmit capabilities of the initiator in the PLOGI. The drive returns an LS_RJT to PLOGI requests that do not satisfy the drive’s requirements. The Initiator Control bits in the PLOGI ACC indicate the capabilities of the drive. The drive returns zero (0) for all bits that are not applicable for class 3 services and for all reserved bits.

**Table 28: Initiator Control fields**

<table>
<thead>
<tr>
<th>Option</th>
<th>Drive requirement</th>
</tr>
</thead>
<tbody>
<tr>
<td>Class Valid</td>
<td>Must be a one (1).</td>
</tr>
<tr>
<td>Intermix Mode</td>
<td>Not applicable. Not valid for class 3.</td>
</tr>
<tr>
<td>Stacked Connect Request</td>
<td>Not applicable. Not valid for class 3.</td>
</tr>
<tr>
<td>Sequence Delivery</td>
<td>Not applicable. Valid only for Fabric login.</td>
</tr>
<tr>
<td>X_ID (Exchange Identifier) Reassignment</td>
<td>Not applicable. Not valid for class 3.</td>
</tr>
<tr>
<td>Initial Process Associator</td>
<td>00 Initial Process Associator not supported.</td>
</tr>
<tr>
<td></td>
<td>01 Initial Process Associator supported.</td>
</tr>
<tr>
<td></td>
<td>10 Reserved.</td>
</tr>
<tr>
<td></td>
<td>11 Initial Process Associator Values of 10 or 11 cause the Login to be rejected. Other values are accepted.</td>
</tr>
<tr>
<td>ACK_0 (Acknowledge 0) Capable</td>
<td>Not applicable. Not valid for class 3.</td>
</tr>
<tr>
<td>ACK_N (Acknowledge N) Capable</td>
<td>Not applicable. Not valid for class 3.</td>
</tr>
</tbody>
</table>

The drive does not check the Categories per Sequence bits in the PLOGI. The drive originates only one category per sequence. The drive returns 00 in the PLOGI ACC to indicate it only supports receiving one category per sequence.

**Recipient Control field**

Indicates the receive capabilities of the initiator in the PLOGI. The Recipient Control bits in the PLOGI ACC indicate the capabilities of the drive. The drive returns zero (0) for all bits that are not applicable for class 3 services and for all reserved bits.

**Table 29: Recipient Control fields**

<table>
<thead>
<tr>
<th>Option</th>
<th>Drive requirement</th>
</tr>
</thead>
<tbody>
<tr>
<td>ACK_0 (Acknowledge 0) Capable</td>
<td>Not applicable. Not valid for class 3.</td>
</tr>
<tr>
<td>ACK_N (Acknowledge N) Capable</td>
<td>Not applicable. Not valid for class 3.</td>
</tr>
<tr>
<td>X_ID (Exchange Identifier) Interlock</td>
<td>Not applicable. Not valid for class 3.</td>
</tr>
<tr>
<td>Error Policy</td>
<td>00 Only discard supported.</td>
</tr>
<tr>
<td></td>
<td>01 Reserved.</td>
</tr>
<tr>
<td></td>
<td>10 Discard and process supported.</td>
</tr>
<tr>
<td></td>
<td>11 Reserved.</td>
</tr>
<tr>
<td>Categories per Sequence</td>
<td>The drive does not check the Categories per Sequence bits in the PLOGI. The drive originates only one category per sequence. The drive returns 00 in the PLOGI ACC to indicate it only supports receiving one category per sequence.</td>
</tr>
</tbody>
</table>
field size in the class 3 parameters when it sends frames. The drive returns the receive buffer field size in the class 3 parameters from the initiator in the PLOGI ACC.

**Concurrent Sequences field**
Must be greater than 0. The drive returns FFh in the PLOGI ACC.

**N_Port End to End Credit field**
Not valid for class 3.

**Open Sequences per Exchange field**
Must be greater than zero (0). The drive returns 01h in the PLOGI ACC.

The PLOGI ACC returns the drive's parameters to the initiator. The PLOGI ACC Payload has the same definition as the PLOGI Payload except the LS Command Code.
**Table 30: Port Login Accept Payload (PLOGI ACC)**

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit Byte</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>LS Command Code (02h)</td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>_ Common Services Parameter (16 Bytes)</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>_ LSB</td>
</tr>
<tr>
<td></td>
<td>1 - 4</td>
<td>_ MSB</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>N_Port_Name (8 Bytes)</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>_ LSB</td>
</tr>
<tr>
<td></td>
<td>5, 6</td>
<td>_ MSB</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Node Name (8 Bytes)</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>_ LSB</td>
</tr>
<tr>
<td></td>
<td>7, 8</td>
<td>_ MSB</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>9 - 12</td>
<td>_ MSB</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Class 1 Service Parameters (16 Bytes)</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>_ LSB</td>
</tr>
<tr>
<td></td>
<td>13 - 16</td>
<td>_ MSB</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Class 2 Service Parameters (16 Bytes)</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>_ LSB</td>
</tr>
<tr>
<td></td>
<td>17 - 20</td>
<td>_ MSB</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Class 3 Service Parameters (16 Bytes)</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>_ LSB</td>
</tr>
<tr>
<td></td>
<td>21 - 24</td>
<td>_ MSB</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reserved (16 Bytes)</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>_ LSB</td>
</tr>
<tr>
<td></td>
<td>25 - 28</td>
<td>_ MSB</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Vendor Version Level (16 Bytes)</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>_ LSB</td>
</tr>
</tbody>
</table>

**LS Command Code (Link Services Command Code) field**
02h PLOGI ACC.

**N_Port Common Service Parameters field**
See Table 25.

**Port Name and Node Name field**
Used to identify the device. The device may have multiple Fibre Channel ports with each having a unique Port Name. The drive is a dual-ported device.

The Port and Node Names in the PLOGI identify the initiator. The drive saves the Port Name of the initiator with the login parameters. If a change of the Port Name/AL_PA address association is detected during a Port Discovery (PDISC), an
implicit logout occurs (any queued commands for the previous Port Name/AL_PA are discarded, the previous login is cleared) and a LS_RJT is returned to the initiator.
The Port and Node Names in the PLOGI ACC identify the drive. The drive uses a format for the Port and Node Names defined as the IEEE extended address. The format is in Table 24.

**Class 1 and 2 Service Parameters field**
May be present in the PLOGI frame. The drive returns only class 3 service parameters. Class parameters for all other classes of service are set to all zeros.

**Class 3 Service Parameters field**
Sent by the drive. See Table 27 for details.

**Vendor Version field**
Vendor-unique. Not supported.
9.2.2 Port Logout (PLOGO) (03x)

Port Logout (PLOGO) is sent by the target in response to any frame from an initiator that has not completed N_Port Login. PLOGO may also be sent by an initiator when it has no further need for a target.

Table 31: LOGO Payload

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit</th>
<th>Byte</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>00h</td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>00h</td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>00h</td>
</tr>
<tr>
<td>1</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

LS Command Code (Link Services Command Code) field
05h PLOGO Payload.

N_Port Identifier field
The three-byte address used in the D_ID and S_ID fields of the frame headers.

Port Name field
The unique eight-byte address assigned to the port.

Table 32: PLOGO Accept

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit</th>
<th>Byte</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>00h</td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>00h</td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>00h</td>
</tr>
</tbody>
</table>

LS Command Code (Link Services Command Code) field
02h PLOGO Accept.
9.2.3 Fabric Login (FLOGI) (04)

Fabric Login (FLOGI) is sent by the drive to the fabric to establish the Fibre Channel operating parameters in a public loop environment. When the drive sends FLOGI, any open exchanges (commands) queued in the drive are discarded.

Table 33: FLOGI Payload

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1 - 4</td>
<td>-</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>5, 6</td>
<td>-</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>7, 8</td>
<td>-</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>9 - 12</td>
<td>-</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>13 - 16</td>
<td>-</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>17 - 20</td>
<td>-</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>21 - 24</td>
<td>-</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>25 - 28</td>
<td>-</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>3</td>
<td></td>
<td>LS Command Code (05h)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0</td>
<td></td>
<td>00h</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td></td>
<td>00h</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td></td>
<td>00h</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0</td>
<td></td>
<td>00h</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1 - 4</td>
<td>-</td>
<td>N_Port Common Service Parameters</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>5, 6</td>
<td>-</td>
<td>Port Name</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>7, 8</td>
<td>-</td>
<td>Node Name</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>9 - 12</td>
<td>-</td>
<td>Class 1 Service Parameters</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>13 - 16</td>
<td>-</td>
<td>Class 2 Service Parameters</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>17 - 20</td>
<td>-</td>
<td>Class 3 Service Parameters</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>21 - 24</td>
<td>-</td>
<td>Reserved</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>25 - 28</td>
<td>-</td>
<td>Vendor Version</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

LS Command Code (Link Services Command Code) field
04h FLOGI payload.

N_Port Common Service Parameters field
See Table 34 on page 83.

Port Name and Node Name field
Used to identify the device. The drive is a dual-ported device.
The Port and Node Names in the FLOGI uniquely identify the drive and the port sending the FLOGI.
The drive uses a format for the Port and Node Names defined as the IEEE extended address. See Table 24.

Class 1 and 2 Service Parameters field
The drive supports only class 3 service parameters. The drive sends all zeros for all other classes of service.

Class 3 Service Parameters field
Sent by the drive. See Table 36 on page 84.

Vendor Version field
Vendor-unique. Not supported.
Table 34: F_Port Common Service Parameters

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit</th>
<th>Byte</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

- **Highest ANSI FC-PH Version**
- **Lowest ANSI FC-PH Version**
- **Buffer to Buffer Credit**

<table>
<thead>
<tr>
<th>1</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Contin Increasing Offset</td>
<td>Random Relative Offset</td>
<td>Valid Vendor Version</td>
<td>F_Port</td>
</tr>
<tr>
<td></td>
<td>Common Features</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>Hunts Groups</td>
<td>Dedicated Simplex</td>
<td>Reserved</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Dynamic Half Duplex</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>Continuous Increase SEQ_CNT</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>Payload Length</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>Reserved</td>
<td>(MSB)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0</td>
<td>Receive Data Field Size</td>
<td>(LSB)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>2, 3</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**Highest ANSI FC-PH Version and Lowest ANSI FC-PH Version fields**
The highest and lowest version of the ANSI Fibre Channel Physical and Signaling standards supported by Public loop drives is FC-PH-3 Rev. 9.4
A version level of 20h is defined for FC-PH-3 Rev. 9.4.

**Buffer to Buffer Credit field**
The drive sets the Alternate Credit Model and sets the Buffer to Buffer Credit to zero.

**Common Features field**
This is a bit significant field which indicates the options that are supported by the drive. Below is a list of the features. The drive returns an LS_RJT to FLOGI ACC for requests that do not satisfy the drive’s requirements.

**Table 35: Common Features bits**

<table>
<thead>
<tr>
<th>Feature</th>
<th>Drive</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Support (FLOGI)</td>
<td>Requirement (FLOGI ACC)</td>
</tr>
<tr>
<td>Continuously Increasing Offset</td>
<td>1</td>
<td>Must = 1</td>
</tr>
<tr>
<td>Random Relative Offset</td>
<td>0</td>
<td>Not checked</td>
</tr>
<tr>
<td>Valid Vendor Version</td>
<td>0</td>
<td>Not checked</td>
</tr>
<tr>
<td>F_Port (Fabric Port)</td>
<td>0</td>
<td>Must = 1</td>
</tr>
<tr>
<td>Alternate Credit Model</td>
<td>1</td>
<td>Must = 1</td>
</tr>
<tr>
<td>E_D_TOV Resolution</td>
<td>0</td>
<td>Not checked. Not valid for loop operation.</td>
</tr>
<tr>
<td>Multicast</td>
<td>0</td>
<td>Not checked</td>
</tr>
<tr>
<td>Broadcast</td>
<td>0</td>
<td>Not checked</td>
</tr>
<tr>
<td>Hunt Groups</td>
<td>0</td>
<td>Not checked</td>
</tr>
<tr>
<td>Dedicated Simplex</td>
<td>0</td>
<td>Not checked</td>
</tr>
<tr>
<td>Dynamic Half Duplex</td>
<td>0</td>
<td>Not checked</td>
</tr>
<tr>
<td>Payload Length</td>
<td>0</td>
<td>Not checked</td>
</tr>
</tbody>
</table>

**Receive Data Field Size field**
The drive sends a receive buffer size of 2112 bytes.

**Table 36: Class 3 Service Parameters**

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit Byte</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>Class Valid</td>
<td>Intermix Mode</td>
<td>Stacked Connect Request</td>
<td>Sequence Delivery</td>
<td>Dedicated Simplex</td>
<td>Camp-on</td>
<td>Buffered Class 1</td>
<td></td>
</tr>
</tbody>
</table>

**Service Options**
The following class 3 service parameters are sent by the drive.

**Table 37: Service Option Class 3 fields**

<table>
<thead>
<tr>
<th>Option</th>
<th>Drive Support (FLOGI)</th>
<th>Drive Requirement (FLOGI ACC)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Class Valid</td>
<td>1</td>
<td>Must = 1</td>
</tr>
<tr>
<td>Intermix Mode</td>
<td>0, not applicable or valid for class 3.</td>
<td>Not checked</td>
</tr>
<tr>
<td>Stacked Connect Request</td>
<td>00, not applicable or valid for class 3.</td>
<td>Not checked</td>
</tr>
<tr>
<td>Sequence Delivery</td>
<td>1</td>
<td>Must = 1</td>
</tr>
<tr>
<td>Dedicated Simplex</td>
<td>0, Not applicable or valid for class 3</td>
<td>Not checked</td>
</tr>
<tr>
<td>Camp-on</td>
<td>0, not applicable or valid for class 3</td>
<td>Not checked</td>
</tr>
<tr>
<td>Buffered Class 1</td>
<td>0, not applicable or valid for class 3</td>
<td>Not checked</td>
</tr>
<tr>
<td>Priority</td>
<td>0, not applicable or valid for class 3</td>
<td>Not checked</td>
</tr>
</tbody>
</table>

**Table 38: Fabric Login Accept Payload (FLOGI ACC)**

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit Byte</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
<th>F_Port Common Service Parameters</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>LS Command Code (02h)</td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>00h</td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>00h</td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>00h</td>
</tr>
<tr>
<td>1 - 4</td>
<td>-</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>F_Port Common Service Parameters</td>
</tr>
<tr>
<td>5, 6</td>
<td>-</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Port Name</td>
</tr>
<tr>
<td>7, 8</td>
<td>-</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Node Name</td>
</tr>
<tr>
<td>9 - 12</td>
<td>-</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Class 1 Service Parameters</td>
</tr>
<tr>
<td>13 - 16</td>
<td>-</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Class 2 Service Parameters</td>
</tr>
<tr>
<td>17 - 20</td>
<td>-</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Class 3 Service Parameters</td>
</tr>
<tr>
<td>21 - 24</td>
<td>-</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reserved</td>
</tr>
<tr>
<td>25 - 28</td>
<td>-</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Vendor Version</td>
</tr>
</tbody>
</table>

**LS Command Code (Link Services Command Code)**

02h FLOGI ACC.

**F_Port Common Service Parameters**

See Table 25.

**Port Name and Node Name**

Used to identify the fabric. The device may have multiple Fibre Channel ports with each having a unique Port Name.
The Port and Node Names in the FLOGI ACC identify the fabric. The drive saves the Port Name of the fabric with the login parameters. If a change of the Port Name is detected during loop initialization, an implicit logout occurs (any queued commands for the previous Port Name/AL_PA are discarded, the previous login is cleared).

**Class 1 and 2 Service Parameters**
May be present in the FLOGI ACC frame. The drive returns only class 3 service parameters. Class parameters for all other classes of service are set to all zeros.

**Class 3 Service Parameters**
Sent by the drive. See Table 37 for details.

**Vendor Version**
Vendor-unique. Not supported.
9.2.4 Process Login (PRLI)

Process Login (PRLI) is sent by the initiator to a target to establish the SCSI FCP operating features.

### Table 39: PRLI Payload

<table>
<thead>
<tr>
<th>Word</th>
<th>bits</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>3</td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

- **LS Command Code (Link Services Command Code)**
  - 20h PRLI Payload.

- **Page Length**
  - Length of the service parameter page (in bytes). For a SCSI FCP service page, the length is 10h.

- **Payload Length**
  - Length of PRLI payload (in bytes). The count includes the LS Command Code. The drive supports one service parameter page per PRLI.
  - The Payload Length must be 14h (20 decimal).

- **Type Code**
  - 08h SCSI FCP process as included in the frame header for FCP frames.
Type Code Extension
0 Not defined for SCSI FCP and must be set to zero (0).

Orig Proc Assc Valid (Originator Process Associator Valid)
Not supported by the drive. Not checked by the drive.

Resp Proc Assc Valid (Responder Process Associator Valid)
Not supported by the drive. Not checked by the drive.

Data Overlay Allow
Not supported by the drive. The Data Overlay Allowed bit is not checked.

Command/Data Mixed Allowed
1 The initiator sends data in the same sequence as the command. The drive does not support Command/Data Mixed. It will accept a PRLI with the Allow bit set, but will return a 0 in the accept to indicate the function cannot be used.

Data/Response Mix Allowed
1 Allows the drive to send the FCP RSP in the same sequence as the data. The drive does not support the Data/Response Mix. It will accept a PRLI with the Allow bit set, but will return zero (0) in the accept to indicate the function cannot be used.

Establish Image Pair
1 The drive establishes a SCSI login for the initiator.
0 The PRLI is only an inquiry of the drive’s support of the process. The accept is still returned, but the login is not retained.

Initiator Function
Must be set to one (1) for the drive to accept the login request.

RD XFR RDY Disable (Read Transfer Ready Disable)
1 FCP_XFR_RDY will not be sent before read data. The drive requires this bit to be set to one (1).

Target Function
May be set in addition to the Initiator Function bit. The drive does not check this bit.

WR XFR RDY Disable (Write Transfer Ready Disable)
0 The drive requires this bit to be set to zero (0). The drive also requires the use of the WR_XFR_RDY bit.
The PRLI recipient returns a PRLI Accept or a LS_RJT to a PRLI request. The PRLI Accept may indicate success or failure of the process login request in the Response Code field. A LS_RJT is returned to a PRLI with a basic format error, e.g. page length error, payload length, and type code.

**Table 40: PRLI Accept Payload**

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit</th>
<th>Byte</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>3</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>3</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**LS Command Code (Link Services Command Code)**  
02h  PRLI Accept Payload.

**Page Length**  
Length of the service parameter page (in bytes). For a SCSI FCP service page, the length is 10h.

**Payload Length**  
Length of PRLI payload (in bytes). The count includes the LS Command Code. The drive supports one service parameter page per PRLI.

**Type Code**  
08h  SCSI FCP process as included in the frame header for FCP frames.
Type Code Extension
0  Not defined for SCSI FCP and must be set to zero (0).

Orig Proc Assc Valid (Originator Process Associator Valid)
Not supported by the drive. Not checked by the drive.
Must be set to zero (0).
The Payload length must be 14h (20 decimal).

Resp Proc Assc Valid (Responder Process Associator Valid)
Not supported by the drive. Not checked by the drive.
Must be set to zero (0).

Establish Image Pair
1  The drive establishes a SCSI login for the initiator.
0  The PRLI is only an inquiry of the drive's support of the process. The accept is still returned, but the login is not retained.

Response Code
The result of the PRLI request.
Only codes 1 and 7 are supported by the drive.
0  Reserved.
1  Request executed.
2  The target has no resources available for establishing the login.
3  Initialization is not complete. The PRLI may be retried.
4  The Image Pair does not exist.
5  The Image Pair cannot be established due to a predefined configuration.
6  Request executed conditionally. Some of the parameters were not able to be set to their requested state.
7  The destination port is unable to process multiple page PRLI request. The PRLI request may be retried as a single page request.

Data Overlay Allow
Not supported by the drive. The Data Overlay Allowed bit is not checked.

Initiator Function
Must be set to one (1) for the drive to accept the login request.

Target Function
May be set in addition to the Initiator Function bit. The drive does not check this bit.

Command/Data Mixed Allowed
1  The initiator sends data in the same sequence as the command. The drive does not support Command/Data Mixed.
   It will accept a PRLI with the Allow bit set, but will return a 0 in the accept to indicate the function cannot be used.

Data/Response Allowed
1  Allows the drive to send the FCP RSP in the same sequence as the data. The drive does not support the Data/Response Mix.
   It will accept a PRLI with the Allow bit set, but will return a 0 in the accept to indicate the function cannot be used.

Rd XFR RDY Disable (Read Transfer Ready Disable)
1  FCP_XFR_RDY will not be sent before read data. The drive requires this bit to be set to one (1).

Wr XFR RDY Disable (Write Transfer Ready Disable)
1  FCP_XFR_RDY will not be sent to request write data.
0  The drive requires this bit to be set to zero (0). The drive also requires the use of the WR_XFR_RDY bit.
9.2.5 Process Logout (PRLO)

Process Logout (PRLO) is sent by the initiator to a target to remove an existing SCSI login. This frees target resources for use by other initiators.

Table 41: PRLO Payload

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td>Orig Proc Assc Valid</td>
<td>Resp Proc Assc Valid</td>
<td>Reserved</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td>Reserved</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>3</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>3</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

LS Command Code (Link Services Command Code)
21h PRLO Payload.

Page Length
Length of the service parameter page in bytes. For a SCSI FCP service page, the length is 10h.

Payload Length
Length of PRLO payload (in bytes). The count includes the LS Command Code. The drive supports one service parameter page per PRLO. The Payload Length must be 14h (20 decimal).

Type Code
00h All FC-4 processes between the initiator and target are removed. The drive treats 08h and 00h the same.
08h The SCSI-FCP process will be removed.

**Type Code Extension**
Not defined for SCSI-FCP and must be zero (0).

**Orig Proc Assc Valid (Originator Process Associator Valid)**
Not supported by the drive. Not checked by the drive.
Must be set to zero (0).
The Payload length must be 14h (20 decimal).

**Resp Proc Assc Valid (Responder Process Associator Valid)**
Not supported by the drive. Not checked by the drive.
Must be set to zero (0).

**Process Associators**
Not supported by the drive. The process associator fields are not checked by the drive.
**Table 42: PRLO Accept Payload**

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>LS Command Code (02h)</td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Page Length (10h)</td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Payload Length (14h)</td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reserved</td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reserved</td>
</tr>
<tr>
<td></td>
<td>1</td>
<td>Orig Proc Assc Valid</td>
<td>Resp Proc Assc Valid</td>
<td>Reserved</td>
<td>Reserved</td>
<td>Response Code</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reserved</td>
</tr>
<tr>
<td>2</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(MSB)</td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Originator Process Associator</td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(MSB)</td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Responder Process Associator</td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reserved</td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**LS Command Code (Link Services Command Code)**

02h PRLO Accept Payload.

**Page Length**

Length of the service parameter page in bytes. For a SCSI FCP service page, the length is 10h.

**Payload Length**

Length of PRLO payload (in bytes). The count includes the LS Command Code. The drive supports one service parameter page per PRLO. The Payload Length must be 14h (20 decimal).

**Orig Proc Assc Valid (Originator Process Associator Valid)**

Not supported by the drive. Not checked by the drive.

Must be set to zero (0).

The Payload length must be 14h (20 decimal).

**Resp Proc Assc Valid (Responder Process Associator Valid)**

Not supported by the drive. Not checked by the drive.

Must be set to zero (0).
**Process Associators**
Not supported by the drive. The originator and responder process associator valid bits must be set to zero (0). The process associator fields are not checked by the drive.

**Response Code**
The result of the PRLO request.
Codes 1, 4, and 7 are supported by the drive.

<table>
<thead>
<tr>
<th>Code</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>Reserved</td>
</tr>
<tr>
<td>1</td>
<td>Request executed.</td>
</tr>
<tr>
<td>2</td>
<td>Reserved</td>
</tr>
<tr>
<td>3</td>
<td>Reserved</td>
</tr>
<tr>
<td>4</td>
<td>The Image Pair does not exist.</td>
</tr>
<tr>
<td>5</td>
<td>Reserved</td>
</tr>
<tr>
<td>6</td>
<td>Reserved</td>
</tr>
<tr>
<td>7</td>
<td>The destination port is unable to process a multiple page PRLO request. The PRLO request may be retried as a single page request.</td>
</tr>
</tbody>
</table>
9.2.6 Third Party Process Logout (TPRLO)

Third Party Process Logout (TPRLO) is sent by the initiator to a target to remove an existing SCSI login. This frees target resources for use by other initiators.

Table 43: TPRLO Payload

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>LS Command Code (04h)</td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Page Length (10h)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Payload Length (14h)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Type Code (08h)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Type Code Extension</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reserved</td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Third Party Originator Process Associator</td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Third Party Responder Process Associator</td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reserved</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td>Third Party Originator N_Port ID</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**LS Command Code (Link Services Command Code)**

24h TPRLO Payload.

**Page Length**

The length must be in the range of 10h to 14h.

**Payload Length**

Length of TPRLO payload (in bytes). The count includes the LS Command Code. The drive supports one service parameter page per TPRLO. The Payload Length must be in the range of 14h to 18h.

**Type Code**

00h All FC-4 processes between the initiator and target are removed. The drive treats 08h and 00h the same.

08h The SCSI-FCP process will be removed.
**Type Code Extension**
Not defined for SCSI-FCP and must be zero (0).

**Third Party Orig Proc Assc Valid (Originator Process Associator Valid)**
Not supported by the drive. Not checked by the drive.
Must be set to zero (0).
The Payload length must be 14h (20 decimal).

**Third Party Resp Proc Assc Valid (Responder Process Associator Valid)**
Not supported by the drive. Not checked by the drive.
Must be set to zero (0).

**Third Party Originator N_Port ID Validity**
00h  Third Party Originator N_Port ID field is not valid.
01h  Third Party Originator N_Port ID field is valid.

**Global Process Logout**
00h  Only the process login for the port identified in the N_Port ID field and type code is removed.
01h  All process logins for the specified type code are removed.

**Process Associators**
Not supported by the drive. The originator and responder process associator valid bits must be set to zero (0). The process associator fields are not checked by the drive.

**Third Party Originator N_Port ID**
This field specifies the N_Port address associated with the process login to be removed.
Table 44: TPRLO Accept Payload

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit Byte</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td></td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td></td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td></td>
<td>3</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>0</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td></td>
<td>3</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>0</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>4</td>
<td></td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>2</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>0</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**LS Command Code (Link Services Command Code)**

02h TPRLO Accept Payload.

**Page Length**

Length of the service parameter page in bytes. For a SCSI FCP service page, the length is 10h.

**Payload Length**

Length of TPRLO payload (in bytes). The count includes the LS Command Code. The drive supports one service parameter page per TPRLO. The Payload Length must be 14h (20 decimal).

**Third Party Orig Proc Assc Valid (Originator Process Associator Valid)**

Not supported by the drive. Not checked by the drive.

Must be set to zero (0).

The Payload length must be 14h (20 decimal).

**Third Party Resp Proc Assc Valid (Responder Process Associator Valid)**

Not supported by the drive. Not checked by the drive.

Must be set to zero (0).
Third Party Originator N_Port ID Validity
00h  Third Party Originator N_Port ID field is not valid.
01h  Third Party Originator N_Port ID field is valid.

Global Process Logout
00h  Only the process login for the port identified in the N_Port ID field and type code is removed.
01h  All process logins for the specified type code are removed.

Process Associators
Not supported by the drive. The process associator fields are not checked by the drive.

Third Party Originator N_Port ID
This field specifies the N_Port address associated with the process login to be removed.

Response Code
The result of the TPRLO request.
Codes 1, 4, and 7 are supported by the drive.
0  Reserved.
1  Request executed.
2  Reserved.
3  Reserved.
4  The Image Pair does not exist.
5  Reserved.
6  Reserved
7  The destination port is unable to process a multiple page TPRLO request. The TPRLO request may be retried as a single page request.
9.2.7 Read Link Error Status Block (RLS)

Read Link Error Status Block (RLS) is sent by the initiator to request the drive to return the Fibre Channel link error information. The error information is contained in the Link Error Status Block (LESB) that is returned in the accept to the RLS. The drive maintains a separate LESB for each port.

Table 45: RLS Payload

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit</th>
<th>Byte</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td></td>
<td></td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>LS Command Code (0Fh)</td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>00h</td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>00h</td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>00h</td>
<td></td>
</tr>
<tr>
<td>1</td>
<td></td>
<td></td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reserved</td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Port Identifier</td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

LS Command Code (Link Services Command Code)
0Fh RLS Payload.

Port Identifier
This field is interpreted to determine whether to return the Link Error Status Block for port A or B.
0   Return the LESB for the port the RLS was received on.
1   Return the LESB for port A.
2   Return the LESB for port B.
Others LS_RJT is returned with Invalid N_Port Identifier.
The RLS Accept includes the LS Command Code and the LESB. The LESB counts are not cleared by a reset. There is no protocol for clearing the counts. The requester must compare the current values with those read previously.

### Table 46: RLS Accept Payload

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit</th>
<th>Byte</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>LS Command Code (02h)</td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>00h</td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>00h</td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>00h</td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>3</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Link Failure Count</td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>3</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Loss of Synchronization Count</td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>3</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Loss of Signal Count</td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>3</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Primitive Sequence Protocol Error</td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>3</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Invalid Transmission Word</td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>6</td>
<td>3</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Invalid CRC Count</td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**LS Command Code (Link Services Command Code)**
02h RLS Accept Payload.

**Link Failure Count**
Number of times synchronization was lost for greater than R_T_TOV (Receiver Transmitter Timeout Value). A Link Failure
results in sending Loop Initialization Primitive Sequence (LIP).

**Loss of Synchronization Count**
Number of times the drive detects loss of synchronization.

*Note.* This count includes the Link Failure Count.

**Loss of Signal Count**
Not supported.

**Primitive Sequence Protocol Error**
Not supported.

**Invalid Transmission Word**
Number of invalid transmission words received while in word sync. Reference the specific drive product manual to determine if this field is supported.

**Invalid CRC Count**
Number of frames discarded due to CRC errors while the drive is in an “open” state. Reference the specific drive product manual to determine if this field is supported.
9.2.8 Reinstate Recovery Qualifier (RRQ)

The Reinstate Recovery Qualifier (RRQ) is sent by the initiator to the drive to indicate the Recovery Qualifier (S_ID, D_ID, OX_ID, RX_ID, SEQ_ID, and SEQ_CNT) for an aborted exchange may be reused. The drive allows reuse of the Recovery Qualifier immediately after sending the accept to an ABTS and does not require RRQ. It returns accepts to all RRQs.

Table 47: RRQ Payload

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit Byte</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>3</td>
<td>LS Command Code (12h)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td>00h</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td>00h</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td>00h</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>3</td>
<td>Reserved</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2 (MSB)</td>
<td>Originator S_ID</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>3 (MSB)</td>
<td>OX_ID</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1 (MSB)</td>
<td>RX_ID</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>3 - 10</td>
<td>(MSB)</td>
<td>Association Header (optional)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>(LSB)</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

LS Command Code (Link Services Command Code)
12h RRQ Payload.

Originator S_ID (Originator Source Identifier)
Address identifier of the port that originated the exchange.

OX_ID (Originator Exchange Identifier)
The RRQ is the OX_ID of the Recovery Qualifier.

RX_ID (Responder Identifier)
The RRQ is the RX_ID of the Recovery Qualifier

Association Header
Not supported by the drive.
9.2.9 Port Discovery (PDISC)

Port Discovery (PDISC) is sent by an initiator to a drive after loop initialization to verify addresses have not changed. The PDISC transfers the same information as the PLOGI except that the LS Command code in the first word of the payload is 50000000h. The PDISC does not cause the open exchanges (commands) to be discarded if the initiator address and parameters have not changed. For other contents of the payload, see Section 9.2.1, Port Login (PLOGI).

The accept for the PDISC is the same as for the PLOGI if the drive detects the initiator AL_PA (Physical Address) or parameters have not changed from a previous login. Section 9.2.1, Port Login (PLOGI). If the drive detects either the initiator addresses or parameters have changed from a previous login, the drive will not send an accept. The drive will return a LOGO to indicate to the initiator a PLOGI is required.

9.2.10 Discover Address (ADISC)

Discover Address (ADISC) is sent by an initiator to a drive after loop initialization to verify addresses have not changed or to verify the drive was able to obtain the hard address select through the interface connector (SEL Lines) during loop initialization.

Table 48: RRQ Accept Payload

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit Byte</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

LS Command Code (Link Services Command Code)

Not supported by the drive.
The ADISC allows the drive to compare the initiator’s address and Port Name with previous login values. If after the loop initialization process the address and Port Name pair provided by the initiator does not match the login values, the initiator is implicitly logged out.

Table 49: ADISC Payload

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit</th>
<th>Byte</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>LS Command Code (52h)</td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>00h</td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>00h</td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>00h</td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reserved</td>
</tr>
<tr>
<td></td>
<td>2</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Hard Address of Originator</td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td>2, 3</td>
<td></td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Port Name of Originator</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td>4, 5</td>
<td></td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Node Name of Originator</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td>6</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>-Reserved</td>
</tr>
<tr>
<td></td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>N_Port ID of Originator</td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
</tbody>
</table>

**LS Command Code (Link Services Command Code)**

52h ADISC Payload.

**Hard Address of Originator**

This is the 24 bit NL_Port Identifier. The lower 8 bits are the AL_PA the initiator attempts to acquire in the LIHA sequence during loop initialization. If the initiator does not have a hard address, this 24 bit field is zeros. If the initiator has a hard address and is able to acquire it during the loop initialization process, the Hard Address and N_Port ID fields of the ADISC will be the same.

If ESI activity is underway when the request for the hard address is received, the drive shall use the last known value of the hard address before the current ESI activity started. For more information on ESI, refer to Section 10.5.

**Port Name of Originator**

This is the unique 8 byte identifier for the initiator port sending ADISC. Refer to Table 24 for the format of the Port Name.

**Node Name of Originator**

This is the unique 8 byte identifier for the initiator sending the ADISC. Refer to Table 24 for the format of the Node Name.

**N_Port ID of Originator**

This is the 24 bit NL_Port Identifier used in the S_ID of the ADISC. The lower 8 bits are the AL_PA the initiator acquired during loop initialization.
Table 50: ADISC Accept Payload

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit</th>
<th>Byte</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>LS Command Code (02h)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>00h</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>00h</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>00h</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reserved</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Hard Address of Responder</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2, 3</td>
<td></td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Port Name of Responder</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>4, 5</td>
<td></td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Node Name of Responder</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>6</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reserved</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>N_Port ID of Responder</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

LS Command Code (Link Services Command Code)

02h ADISC Accept Payload.

Hard Address of Responder
This is the 24 bit NL_Port Identifier. The lower 8 bits are the AL_PA the drive attempts to acquire in the LIHA sequence during loop initialization. This field represents the address indicated on the drive interface connector. If the drive does not have a hard address, this 24 bit field is zeros. If the drive has a hard address and is able to acquire it during the loop initialization process, the Hard Address and N_Port ID fields of the ADISC Accept will be the same.

Port Name of Responder
This is the unique 8 byte identifier for the drive port sending the ADISC Accept. Refer to Table 24 for the format of the Port Name.

Node Name of Responder
This is the unique 8 byte identifier for the drive sending the ADISC Accept. Refer to Table 24 for the format of the Node Name.

N_Port ID of Responder
This is the 24 bit NL_Port Identifier used in the S_ID of the ADISC Accept header. The lower 8 bits are the AL_PA the drive acquired during loop initialization.
9.2.11  Report Node Capabilities (RNC)

Report Node Capabilities (RNC) is sent to a target node to request that the node report on its capabilities. The node does this by returning a list of specifications and the supported revision level of the specification.

Table 51: RNC Payload

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>LS Command Code</td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reserved</td>
</tr>
<tr>
<td></td>
<td>1</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Payload Length</td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td>1</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>RNC Flags</td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reserved</td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reserved</td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>VU Information Length</td>
</tr>
<tr>
<td>2, 3</td>
<td>-</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Vendor Identifier - RNC Accept Payload only</td>
</tr>
<tr>
<td>4</td>
<td>3 - 0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Capability Entries</td>
</tr>
</tbody>
</table>

LS Command Code (Link Services Command Code)

02h  RNC Accept Payload.
53h  RNC Payload.

Payload Length

Length of the RNC Payload (in bytes). This count includes the LS Command Code.

RNC Flags

00h  Report on all available capabilities.
80h  Report on the selected capabilities listed in the capability entries.

VU Information Length

00h  Length of the Vendor Unique Information in the payload. This feature is not supported and this field must = 0.

Vendor Identifier - RNC Accept Payload only

Eight bytes of ASCII data identifying the vendor of the product (node).

Capability Entries

There may be from zero to n number of capability entries (see the table below for the format of capability entries). The limit is that the payload length can not be greater than 256 bytes.

Table 52: Capability Entries

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Flags</td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Document Identifier</td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Low Revision - RNC Accept Payload only</td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>High Revision - RNC Accept Payload only</td>
</tr>
</tbody>
</table>
Flags
Bit 7 = 0 - report on this capability.
Bit 7 = 1 - Invalidate this capability selection.
Bit 6 = 1 - There is an extension on the capability entry. This is not supported. This bit must = 0.
If either bit 5 or bit 4 = 1 - The Document Identifier is vendor unique. This feature is not supported.
Bits 4 and 5 must = 0.

Document Identifier
This number identifies the document. Valid document numbers range from:
01h through 05h
10h through 13h, and
20h through 27h

Low Revision - RNC Accept Payload only
This field contains the lowest revision of the specified document that is supported. The values in the revision fields represent decimal revisions between 0.0 (00h) and 25.5 (FFh).

High Revision - RNC Accept Payload only
This field contains the highest revision of the specified document that is supported. The values in the revision fields represent decimal revisions between 0.0 (00h) and 25.5 (FFh).
9.2.12 Link Service Reject (LS_RJT)

Link Service Reject (LS_RJT) is a reply to an extended link service request that has been rejected. A reason code is included to communicate additional information about the reject.

Table 53: LS_RJT Payload

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>3</td>
<td>LS Command Code (01h)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>0</td>
<td>00h</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>00h</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0</td>
<td>0</td>
<td>00h</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>3</td>
<td>Reserved</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>0</td>
<td>Reason Code</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>Reason Explanation</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0</td>
<td>0</td>
<td>Vendor Unique</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

LS Command Code (Link Services Command Code)
01h LS_RJT Payload.

Reason Code
03h Logical Error. This reason code is sent in response to rejected PLOGI and PRLI requests. See Reason Explanation codes 01h, 03h, 07h, 09h, and 0Fh for PLOGI errors. For PRLI errors, the Reason Explanation code is 00, unspecified. The PRLI errors are Page Length Not 16, Type Code Not 8, and Payload Length Not 20.
09h Unable to perform command request. This reason code is sent in response to PLOGI. See Reason Explanation code 29h.
0Bh Not supported. This reason code is returned in response to extended link service frames that are not supported.

Reason Explanation
The following reason code explanations are returned:
01h Invalid options. Returned in response to a PLOGI if class 3 parameters are not valid.
03h Initiator Control class service options. Returned in response to a PLOGI if the initiator requires process associators.
07h Receive data field size. Returned in response to a PLOGI if the initiator sends a receive buffer size in the common or class 3 service parameters that is not a multiple of four bytes or not in the range of 256–2112 bytes.
09h Invalid service parameter—concurrent sequences. Returned in response to a PLOGI if the initiator sets zero (0) concurrent sequences.
0Bh Invalid service parameter—credit. Returned in response to a PLOGI if the alternate credit model is not supported by the initiator.
0Fh Invalid common service parameters. Returned in response to a PLOGI if common service parameters contain an unsupported version of FC-PH, continuously increasing offset is not supported, or the F_Port bit is set.
1Fh Invalid N_Port identifier. This code is returned in response to a RLS if the port identifier value is not in the range of 0–2.
29h Insufficient resources for login. This code is returned to PLOGI if the login table is full and no initiator can be logged out (all logged in initiators have active commands in the queue). The PLOGI may be retried.
2Ch Request not supported.

Vendor Unique
Not supported by the drive.
9.3 FC common transport

FC common transport (CT) provides a transport for service applications such as the fabric name server. The type of FC common transport service is identified by the command code in the third word of the payload. The R_CTL field of the frame header identifies whether the common transport service is a request or a response to a request. The response for a common transport service varies with the function. A description of the accept for each request is included with the description of the request.

Table 54: Common transport header

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit Byte</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>R_CTL (02h)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2 (MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>D_ID</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td>1</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reserved</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2 (MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>S_ID</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td>2</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Type (20h)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2 (MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>F_CTL</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td>3</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>SEQ_ID</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2 (MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1 (MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>SEQ_CNT</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td>4</td>
<td>3 (MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>OX_ID</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>RX_ID (FFFFh)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>3 (MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Parameter</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
</tbody>
</table>

R_CTL (Routing Control)
02h Unsolicited control for CT service request, PDISC.
03h Solicited control for responses.

D_ID (Destination Identifier)
Frame destination address.
**S_ID (Source Identifier)**
The address of the originator of the frame. This address is used by the destination to return any responses that may be required by the operation.

**Type**
20h Fibre Channel services.

**F_CTL (Frame Control)**
Set to 290000h for CT service requests. This indicates the frame is from the originator of the exchange, this is the last frame of the sequence, and sequence initiative is transferred for the responder to send the reply back.
For the reply frames, the F_CTL is set to 990000h. This indicates the frame is from the responder, this is the last sequence, this is the last frame of the sequence, and sequence initiative is returned to the originator.

**SEQ_ID (Sequence Identifier)**
Not checked by the drive. For CT services requests, the drive uses the SEQ_ID value equal to FFh.

**DF_CTL (Data Field Control)**
Set to 00 to indicate no optional Fibre Channel headers are used.

**SEQ_CNT (Sequence Count)**
Not checked by the drive. For extended link services replies and requests sent by the drive, SEQ_CNT equals 0000.

**OX_ID (Originator Exchange Identifier)**
The drive sends 0000 to the OX_ID field in extended link services requests it originates. For extended link services replies, the drive uses the OX_ID value received from the initiator.

**RX_ID (Responder Identifier)**
Not used by the drive. The value of FFFFh indicates the RX_ID is not being used.

**Parameter**
Not used for CT services.
### 9.3.1 Register FC-4 Types Name Service (RFT_ID)

Register FC-4 Types Name Service (RFT_ID) is used to register the drive’s Port_Identifier and FC-4 type (SCSI-FCP) with the fabric name server.

**Table 55: RFT_ID Payload**

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>FC-CT Revision (01h)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reserved</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>FC Services Type Code (FFh) (Directory Service Application)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>FC Services Sub Type (02h) (Name Service)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Options</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reserved</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Command Code: RFT_ID (17h)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reserved</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reason Code</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Explanation Code</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Vendor Unique</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reserved</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>S_ID of Requesting N_Port</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>00h</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>00h</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>01h</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>00h</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>6 - 12</td>
<td>-</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>00h - 000h</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

*Bytes 20-51 contain a bit map of supported FC-4 types. The 1 in byte 22 indicates type 08h (SCSI-FCP)*

**FC-CT Revision**
Revision level of the FC-CT.

**FC Services Type Code**
FCh  FC Services.

**FC Service Sub Type**
02h  Name Service.

**Options**
00h  Single Exchange.

**Command Code: RFT_ID**
0217h  RFC-4
Explanation Code
00h Used only for reject responses.

Reason Code
00h Used only for reject responses.

S_ID (Source Identifier) of Requesting N_Port
This field contains the Native Port IDentifier of the port registering its FC-4 types.

Vendor Unique
Words 5 through 12 contain a bit map of supported FC-4 types. The 01h in byte 1 of word 5 indicates type 08h (SCSI-FCP)
10.0 Enclosure services interface (ESI)

ESI provides a path for the drive to input data from the enclosure and, optionally, transfer data to the enclosure. All transfers between the drive and its enclosure are initiated by the drive in response to SCSI Send Diagnostic and Receive Diagnostic Results commands from the host system.

There are two levels of ESI capability defined by specifications developed in the Small Form Factor (SFF) industry group. These specifications use the –Parallel_ESI (–P_ESI) and the seven Select_ID (SEL_(6:0)) pins in the drive interface connector to implement the ESI interface. The drives covered by this manual support both levels of capability.

The first level of functionality is defined by the SFF 8045 specification. It provides the simple capability for the drive to input up to seven signals of enclosure information at the direction of a Receive Diagnostic Results command and return the status to the host.

The second level of functionality is defined by the SFF 8067 specification. It defines a bidirectional capability that enables the drive to transfer information to and from its enclosure.

ESI data is transferred in diagnostic pages. The drive does not check the page contents to see if they are valid. It only provides a transfer function between the host and the enclosure. Reference the SCSI-3 Enclosure Services Command Set (SES) standard for details of the page contents.

10.1 Discovery process

The drive uses a discovery process to determine whether its enclosure supports an ESI and which specification is supported. The discovery process is initiated for each Send Diagnostic and Receive Diagnostic Results command with a Page Code of 01h to 0Fh. The discovery process is initiated for each retry on an ESI operation and for each enclosure initiated operation. Reference Figure 13 for a flow diagram of the discovery process.

The drive enters the discovery phase by asserting –Parallel ESI low. The enclosure has a maximum of 1 µsec to respond.

There are three reactions for the enclosure:
1. The SEL_(6:0) pins do not change.
2. SEL_(3:0) change to the binary complement of the address. This indicates that 8067 mode may be supported, but more discovery steps are required.
3. The SEL_(6:0) pins change, but SEL_(3:0) do not equal the binary complement of the address.

In cases 1 and 3, the 8067 support level is not available. The drive processes both cases as 8045 mode and returns only the seven bits of ESI status. In case 1, the drive is not able to detect whether the enclosure does not support ESI for this drive location or if ESI equals Select_ID. It is the host’s responsibility to determine whether the returned information is ESI or Select_ID information. This may be accomplished by issuing a Receive Diagnostic Results command to a device location with redundant ESI capability.
In case 2, the drive continues the discovery process. The drive waits up to 1 second for the enclosure to assert the –ENCL_ACK (SEL_.4) low. The time is allowed for the enclosure processor to complete processing other possible ESI requests through other devices.

If –ENCL_ACK is not asserted within one second, the drive assumes the enclosure has 8067 support but is not responding. The SCSI diagnostic command is failed with ASC/ASQ 35 02. If ENCL_ACK is detected, the drive asserts –DSK_WR and –DSK_RD, SEL(6) and SEL(5) respectively, low.

The enclosure is required to respond to –DSK_WR and –DSK_RD by negating –ENCL_ACK within 100 µsec. The drive responds to the negating of –ENCL_ACK by negating –DSK_WR and –DSK_RD and moving to the ESI command phase.

![Discovery process flow diagram](image)

**Figure 13. Discovery process flow diagram**
10.2  8045 mode

In 8045 mode, the enclosure places the binary complement of ESI on the Select_ID pins, SEL_(6:0).

10.2.1  8045 ESI pinouts

The table below is a mapping of the Select pins to the SFF 8045 ESI function. The sense of the ESI information is complemented and the address function of the select pins is true. The drive returns the true state of the ESI in the diagnostic page.

Table 56:  SFF 8045 ESI pinouts

<table>
<thead>
<tr>
<th>Pin</th>
<th>ESI function</th>
</tr>
</thead>
<tbody>
<tr>
<td>SEL 6</td>
<td>–ESI In 6</td>
</tr>
<tr>
<td>SEL 5</td>
<td>–ESI In 5</td>
</tr>
<tr>
<td>SEL 4</td>
<td>–ESI In 4</td>
</tr>
<tr>
<td>SEL 3</td>
<td>–ESI In 3</td>
</tr>
<tr>
<td>SEL 2</td>
<td>–ESI In 2</td>
</tr>
<tr>
<td>SEL 1</td>
<td>–ESI In 1</td>
</tr>
<tr>
<td>SEL 0</td>
<td>–ESI In 0</td>
</tr>
</tbody>
</table>
10.3 8067 mode

Transfers on an 8067 interface are started by the drive pulling the –P_ESI pin low to enter the Discovery phase. For each transfer, there are three phases:

1. Discovery
2. ESI command
3. Data

The data phase is either a read or write to the enclosure depending on the SCSI command.

![Figure 14. ESI transfer phases](image)

10.3.1 8067 ESI command

When the discover process determines 8067 mode is supported, the drive generates an ESI command to the enclosure based on the SCSI Send Diagnostic or Receive Diagnostic Results command received from the host. The format of the ESI command is shown in Table 57.

| Word | Bit Byte | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
|------|----------|---|---|---|---|---|---|---|---|---|
| 0    |          |   |   |   |   |   |   |   |   |   |
| 0    |          |   |   |   |   |   |   |   |   |   |
| 0    |          |   |   |   |   |   |   |   |   |   |
| 0    |          |   |   |   |   |   |   |   |   |   |
| 0    |          |   |   |   |   |   |   |   |   |   |

Page Code
The Page Code is from the SCSI Send Diagnostic or Receive Diagnostic Results command diagnostic page that initiated the ESI transfer.

Send
0 The ESI data transfer is from the enclosure to the drive.
1 The ESI data transfer is from the drive to the enclosure.

Send Diagnostic Parameter Length
For a Send Diagnostic command, the Send Diagnostic parameter length is the page length from the diagnostic page header incremented by 4 to include the ESI command bytes, and reflects the total number of bytes that will be transferred to the enclosure unless the transfer is truncated by a shorted allocation length in the CDB. The Send Diagnostic parameter length is 0 for Receive Diagnostic Results commands.
10.3.2  8067 ESI interface pinouts

In 8067, the ESI function becomes a bi-directional interface. Three pins are defined for control functions and the remaining four pins become a 4-bit nibble interface. Table 58 is a mapping of the Select pins to the 8067 ESI interface function. 8067 specifies that open-collector type drivers be used for signals on the P_ESI and Select lines.

Table 58:  SFF 8067 ESI pinouts

<table>
<thead>
<tr>
<th>Pin</th>
<th>ESI function</th>
</tr>
</thead>
<tbody>
<tr>
<td>SEL 6</td>
<td>–DSK_WR</td>
</tr>
<tr>
<td>SEL 5</td>
<td>–DSK_RD</td>
</tr>
<tr>
<td>SEL 4</td>
<td>–ENCL_ACK</td>
</tr>
<tr>
<td>SEL 3</td>
<td>Data (3)</td>
</tr>
<tr>
<td>SEL 2</td>
<td>Data (2)</td>
</tr>
<tr>
<td>SEL 1</td>
<td>Data (1)</td>
</tr>
<tr>
<td>SEL 0</td>
<td>Data (0)</td>
</tr>
</tbody>
</table>

10.3.3  8067 information format

ESI in 8067 mode is transferred on the ESI interface a nibble (4 bits) at a time. Refer to Table 59 for the transfer order in bits and Table 60 for byte order.

Table 59:  Bit order in 8067 mode ESI transfers

<table>
<thead>
<tr>
<th>Bit order byte</th>
<th>MSB</th>
<th>LSB</th>
</tr>
</thead>
<tbody>
<tr>
<td>Transfer order on ESI interface</td>
<td>First Nibble</td>
<td>Second Nibble</td>
</tr>
<tr>
<td>Bit order is ESI data</td>
<td>D(3)</td>
<td>D(2)</td>
</tr>
</tbody>
</table>

Table 60:  Byte order in 8067 mode ESI transfers

| Byte order in SCSI transfer | 0 | 1 | ........ | n – 1 | n |
| Byte order is ESI transfer | first | second | ........ | n – 1 | n |
10.4  ESI command transfer

Figure 15. ESI command transfers

10.4.1  ESI read transfer

To receive data from the enclosure, the drive pulls –DSK_RD pin low to request information from the enclosure. The enclosure responds by driving the Data pins with ESI and pulling –ENCL_ACK low to signal that the data is valid. The drive strobes the data and allows high –DSK_RD to return high. The enclosure responds to the –DSK_RD going high by allowing –ENCL_ACK to return to high. This sequence may be repeated for as many bytes, two nibble each, requested by the host. The drive exits the ESI mode by not pulling –P_ESI low—the pull up resistor returns –P_ESI to a high level.

Figure 16. ESI reads

10.4.2  ESI write transfer

To send data to the enclosure, the drive places ESI data on the data pins and pulls –DSK_WR in low. The enclosure strobos the data and responds by pulling –ENCL_ACK low to signal the drive it has taken the data. The drive stops pulling –DSK_WR low, allows the pin to return to a high. The enclosure responds to the –
DSK_WR going high by allowing –ENCL_ACK to return to high. This sequence may be repeated for as many
bytes, two nibble each, sent by the host. The drive exits the ESI mode by allowing –P_ESI low to return to a
high.

![Diagram of ESI Interface]

**Figure 17. ESI writes**

### 10.5 Enclosure-initiated ESI transfer

Enclosure-initiated ESI (EIE) provides a means for the enclosure to request information or action from a drive
that supports an 8067 ESI interface. The transfer of information is independent of the SCSI interface. The for-
mat of the information, however, is similar to the SES information transferred on the SCSI interface for ease of
implementation.

#### 10.5.1 EIE Discovery

A modified discovery phase is defined to allow the enclosure to initiate an information request and allow the
drive to detect the request.

If the drive supports detection of the Un-Mated condition of the START_1 and START_2 signals (case 1), and
supports Enclosure Initiated ESI (EIE) transfers, it monitors the START_1 and START_2 signals. When the
device detects a transition from one state to another, the drive will wait 100 ms and check the lines again. If
the lines are still at their new state, it will assert the –PARALLEL ESI line. To avoid the drive spinning down, it is
highly recommended that when the enclosure changes the lines to initiate an ESI transfer, it does not change
them to the Un-mated condition. If the drive is requesting an ESI transfer with a change in the Start lines, it will
return the START_1 and START_2 lines to their original condition a minimum of 100 nanoseconds before
asserting the –ENCL_ACK signal. The discovery phase continues. Figure 18 illustrates a successful discovery
of an EIE transfer request.
If discovery determines the enclosure does not support an 8067 capable interface or the enclosure does not return the START_1 and START_2 signals to a valid mated condition, the device negates –PARALLEL_ESI and prepares for power removal. Figure 19 shows a case where the enclosure does support 8067 ESI transfer but the enclosure is requesting the drive to prepare for removal. See SCSI Command Reference Manual, 100293068, Section 1.6.7 for more information on the motor spin-up options.
10.5.2 EIE operations

Following successful discovery of an EIE transfer request, the drive transfers an ESI command to the enclosure using the write and command phase procedure defined in the SFF-8067 Specification for 40-pin SCA-2 Connector w/Bidirectional ESI, Rev. 3.0, section 6.4.2.2. The contents of the command are defined in 8.3. The device follows the command with a read phase procedure as described in the SFF-8067 Specification for 40-pin SCA-2 Connector w/Bidirectionally ESI, Rev. 3.0, section 6.4.2.3 to retrieve the transfer request information from the enclosure.

If the enclosure is requesting information, the drive sends an ESI command with Send = 1 to indicate to the enclosure it is ready to transfer the requested information. The command is followed by a write of the information requested by the enclosure. This information is defined in the SFF-8067 Specification for 40-pin SCA-2 Connector w/Bidirectionally ESI, Rev. 3.0, section 8.3. Following the write, the drive negates –Parallel ESI to end the operation. Figure 20 is a summary of these operations.

*1 Enclosure Services Processor changes START_1 and START_2 to indicate that it is requesting communication with the SCSI device.

*2 SCSI device asserts -PARALLEL ESI to indicate it is ready to begin communication with the Enclosure Services Processor.

*3 SCSI device determines that enclosure is SFF-807 compliant by noting that SEL_(0:3) bits have inverted and that SEL_5 and SEL_6 have the value that the device is presenting.

*4 The Enclosure Services Processor asserts -ENCL_ACK to indicate it is ready to begin communication with the SCSI device.

*5 The SCSI device negates -PARALLEL ESI to end the ESI transfer and prepares for power removal as described in 10.3.1.

Figure 19. Prepare for Removal
If any errors or timeouts are detected during the EIE operation, the drive aborts the operation and continues normal operation. Errors are not reported.

10.5.3 Enclosure requested information

If the enclosure services interface transfer is initiated by the enclosure, the drive sends ESI Command Phase information as defined in the SFF-8067 Specification for 40-pin SCA-2 Connector w/Bidirectional ESI, Rev. 3.0, table 7-3 to the enclosure following successful discovery. The page code in the ESI command is 00h. This page code is reserved for SCSI diagnostic commands between the host and the drive and will not appear in ESI transfers initiated by SCSI commands. An exception to this is the ESI Data Validation (EDV) (see Section 10.5.3.9). The ESI command is a read operation, SEND=0 with parameter length of 6h. During the second command phase in EIESI, if the drive is writing data, SEND=1 with the parameter length equal to the amount of data being transferred in the subsequent write phase including the four bytes of header in the write data.

The enclosure responds to the ESI command from the drive with an ESI request as defined in Table 61. The information requested by the enclosure is identified by the action code.

If the ESI request contains a valid Action Code and non-zero Parameter Length, the drive responds with a write operation with the requested information. Table 63 defines the format of the Enclosure Initiated ESI (EIE) page. Tables 64, 66, 62, 67, and 68 define the page contents for the identified action codes.

Table 61: Enclosure Request

<table>
<thead>
<tr>
<th>Bit</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td>Page Code (00h)</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td></td>
<td></td>
<td>Reserved</td>
<td>Action Code</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td></td>
<td></td>
<td>Reserved</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td></td>
<td></td>
<td>Action Specific</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>4</td>
<td></td>
<td></td>
<td>(MSB)</td>
<td>Parameter Length</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>5</td>
<td></td>
<td></td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Page Code
The Page Code identifies the page of enclosure data being transferred.

Note. The Page Code is always 00h for Enclosure Initiated ESI.

Action Code
The information requested by the enclosure is identified by one of the action codes listed below.
- 00h Device Standard Inquiry Data (see Section 10.5.3.1).
- 01h Device Address (see Section 10.5.3.2).
- 02h Loop Position Map (see Section 10.5.3.3).
- 03h Initiate Loop Initialization.
**Note:** The Initiate Loop Initialization Action Code (03h) does not include a transfer of information to the enclosure. The Action Specific bits in the Enclosure Request define the operation to be performed. See Table 62.

04h  Device Identification (see Section 10.5.3.4).
05h  Device Temperature (see Section 10.5.3.5).
06h  Port Parameters (see Section 10.5.3.6).
07h  Link Status (see Section 10.5.3.7).
08h  Spin-Down Control (see Section 10.5.3.8).
09h  ESI Data Validation (see Section 10.5.3.9).
0Ah-0Fh  Reserved.

**Action Specific**

The Action Specific byte determines behavior unique to each action code. Refer to Table 63 for the Action Specific byte for Action Code 3 and Table 71 for the Action Specific byte for Action Code 8. If an Action Specific field is not defined for a given action code, its value shall be zero.

**Initiate LIP Action Specific Bits**

Table 62 describes the Action Specific byte 3 of the Enclosure Request shown in Table 61.

**Table 62: Initiate LIP Action Specific Bits**

<table>
<thead>
<tr>
<th>Bit</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>Byte</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>LIP Loop B</td>
</tr>
</tbody>
</table>

**Note.** The LIP Loop B and LIP Loop A bits are only defined as such if the Action Code field is set to 03 (Initiate Loop Initialization).

**LIP Loop A/B**

The drive enters the Loop Initialization Process on either loop A, B, or both as indicated by these bits. The drive originates a LIP(F7,AL_PS) if it has a valid AL_PA. The drive originates a LIP(F7,F7) if it does not have a valid AL_PA.

**Parameter Length**

The parameter length in the enclosure request is set by the enclosure to the number of bytes it is requesting including the four header bytes. The drive sends the actual length of the requested information or the length identified in the request parameter length whichever is less. If the parameter length is equal to 0, The drive ends the ESI transfer by negating Parallel_ESI.
Table 63: Enclosure Initiated ESI Page Format

<table>
<thead>
<tr>
<th>Bit</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>ESI Page (00h)</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td></td>
<td></td>
<td>Action Code</td>
</tr>
<tr>
<td>2</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td>Page Length (n – 3)</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>n</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Data</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
</tbody>
</table>

ESI Page
00h Identifies the ESI page code used (00h).

Action Code
The Action Code requested by the enclosure.

Page Length
The length of the ESI page (n – 3 bytes).

Data
The first n - 4 bytes of ESI data.

10.5.3.1 Device Standard Inquiry Data page

Table 64: Device Standard Inquiry Data page

<table>
<thead>
<tr>
<th>Bit</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>ESI Page (00h)</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td></td>
<td></td>
<td>Action Code (00h)</td>
</tr>
<tr>
<td>2</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td>Page Length (24h)</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>n</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Inquiry Data</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
<td></td>
</tr>
</tbody>
</table>

ESI Page
00h Identifies the ESI page code used (00h).

Action Code
00h Device Standard Inquiry Data

Page Length
24h The length of the ESI page (in bytes).
Inquiry Data
The first 36 bytes of Standard Inquiry data. Refer to SCSI Command Reference Manual, 100293068, Section 1.6 for a definition of this data. Note: the vendor specific, VS, bit in byte 6 is not valid.

10.5.3.2 Device Address page

Table 65: Device Address page

<table>
<thead>
<tr>
<th>Bit</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>ESI Page (00h)</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>Action Code (01h)</td>
</tr>
<tr>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Page Length (24h)</td>
</tr>
<tr>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td>4</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Node Name</td>
</tr>
<tr>
<td>5</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td>12</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Port A (01h)</td>
</tr>
<tr>
<td>13</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Port A Port_Identifier</td>
</tr>
<tr>
<td>14</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td>15</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Port A Position</td>
</tr>
<tr>
<td>16</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>00</td>
</tr>
<tr>
<td>17</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Port A Name</td>
</tr>
<tr>
<td>18</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td>19</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Port B (02h)</td>
</tr>
<tr>
<td>20</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Port B Port_Identifier</td>
</tr>
<tr>
<td>21</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td>22</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Port B Position</td>
</tr>
<tr>
<td>31</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>00</td>
</tr>
<tr>
<td>32</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Port B Name</td>
</tr>
<tr>
<td>33</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
</tbody>
</table>

ESI Page
00h Identifies the ESI page code used (00h).

Action Code
01h Device Address
Page Length
24h  The length of the ESI page (in bytes).

Node Name
The 64-bit Fibre Channel unique Name_Identifier assigned to the drive.

Port-Identifier
The FC 24-bit address assigned to the port. The lower byte is the current FC-AL AL_PA for this port. If the port does not have a Port-Identifier, a value of FF FF FFh is returned in the Port-Identifier field.

Port Position
The offset value for this port's AL_PA in the FC-AL AL Loop Initialization Loop Position (LILP) Frame. If the port does not have an AL_PA, a value of FFh is returned in the Port Position field.

Port Name
The 64-bit Fibre Channel unique Name_Identifier assigned to the port.

10.5.3.3 Loop Position Map page

Table 66: Loop Position Map page

<table>
<thead>
<tr>
<th>Bit Byte</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>ESI Page (00h)</td>
</tr>
<tr>
<td>1</td>
<td></td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>Action Code (02h)</td>
</tr>
<tr>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Page Length (m – 3)</td>
</tr>
<tr>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td>4</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Offset Port A (n – 4)</td>
<td></td>
</tr>
<tr>
<td>5</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Loop Map Port A</td>
<td></td>
</tr>
<tr>
<td>n</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td>n + 1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Offset Port B (m – n + 1)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>n + 2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Loop Map Port B</td>
<td></td>
<td></td>
</tr>
<tr>
<td>m</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
</tbody>
</table>

ESI Page
00h  Identifies the ESI page code used (00h).

Action Code
02h  Device Address

Page Length
The total transfer length depends on the number of valid ALPA's on the loop.

Offset Port x
This field Indicates the number of bytes of offset from the FC-AL LILP frame in the Loop Map. A value of 00h indicates the Loop Map is not available for the port.
Loop Map Port x
This field contains the valid AL_PA entries from the payload of the FC-AL LILP frame. Only the valid AL_PA entries are transferred to minimize the transfer time on the ESI interface. The maximum Loop Map size is 127 bytes.

10.5.3.4 Device Identification page

Table 67: Device Identification page

<table>
<thead>
<tr>
<th>Bit</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>4</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>n</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

ESI Page
00h Identifies the ESI page code used (00h).

Action Code
04h Device Identification.

Page Length
The length is (bytes) of the Device Identification Page. It will reflect if the allocation length is too small to transfer all the page.

Device ID Data
This field contains the same data as the SCSI Vital Product Data Device Identification page (83h). See SCSI Command Reference Manual, 100293068, Section 1.6.4 for the complete SCSI Vital Product Data device Identification information.
10.5.3.5  Device Temperature page

Table 68:  Device Temperature page

<table>
<thead>
<tr>
<th>Bit</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>ESI Page (00h)</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td></td>
<td></td>
<td>Action Code (05h)</td>
</tr>
<tr>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Page Length (06h)</td>
</tr>
<tr>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(MSB)</td>
</tr>
<tr>
<td>4</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td>5</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Temperature</td>
<td></td>
</tr>
<tr>
<td>9</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reserved</td>
<td></td>
</tr>
</tbody>
</table>

Field definitions (listed alphabetically)

**Action Code**
05h  Device Temperature

**ESI Page**
00h  Identifies the ESI page code used (00h).

**Page Length**
06h  The length of the ESI page (in bytes).

**Temperature**
The value of the drive temperature sensor in degrees Celsius, offset by +20 degrees. The range expresses a temperature between –19 and +235 degrees Celsius. The value of 0 is reserved.
10.5.3.6 Port Parameters page

Table 69: Port Parameters page

<table>
<thead>
<tr>
<th>Bit Byte</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>ESI Page (00h)</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td>Action Code (06h)</td>
</tr>
<tr>
<td>2 3</td>
<td>(MSB)</td>
<td>Page Length (06h)</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>4</td>
<td></td>
<td>Drive Capabilities</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>5</td>
<td></td>
<td>Reserved</td>
<td>LSP CHG</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>6</td>
<td>0</td>
<td>0</td>
<td>Port A Link Fail</td>
<td>Port A Bypass</td>
<td>0</td>
<td>Port A Link Rate</td>
<td></td>
<td></td>
</tr>
<tr>
<td>7</td>
<td></td>
<td>Reserved</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>8</td>
<td>0</td>
<td>0</td>
<td>Port B Link Fail</td>
<td>Port B Bypass</td>
<td>0</td>
<td>Port B Link Rate</td>
<td></td>
<td></td>
</tr>
<tr>
<td>9</td>
<td></td>
<td>Reserved</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

ESI Page
Identifies the ESI page code used (00h).

Action Code
06h Port Parameters.

Page Length
06h The length of the ESI page (in bytes).

Drive Capabilities
This is a bit-significant field that indicates which device control codes defined by SFF 8045 are supported by the drive. The corresponding device control code is supported. For example, a one in bits 7 and 6 indicates that device control codes 7 and 6 are supported. This indicates the drive supports Fibre Channel link rates or 1 and 2 GHz.

LSP CHG (Link Status Page Change)
This bit-significant field indicates which device control codes defined in SFF 8045 are supported by the drive.
0 The Link Status Page data has not changed.
1 The date in the Link Status Page has changed since the enclosure last read the Link Status Page.

Port Link Fail
0 The drive is not currently detecting a loop failure condition as defined in FC-AL for the port.
1 The drive is currently detecting a loop failure condition as defined in FC-AL for the port.

Port Bypass
0 The drive is not requesting bypass.
1 The drive is asserting the –ENBL BYP CH signal in the SCA connector for the port.

Port Link Rate
This field contains the value defined for the Fibre Channel link rate by the Device Control Code inputs in SFF 8045. For example, seven represents 1 GHz.
10.5.3.7 Link Status page

All fields are supported unless specifically listed as not supported under Field Definitions below.

When the drive changes a value in this page, the drive sets the LSP CHG bit in the Port Parameter page. The enclosure may poll the Port Parameter page to determine if it needs to read and process the Link Status page. When the Link Status page is read by the enclosure, the LSP CHG bit is cleared.
The fields listed in this section are extracted from the FC-PH defined Link Error Status Block (LESB).

**Table 70: Link Status page**

<table>
<thead>
<tr>
<th>Bit Byte</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>ESI Page (00h)</td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td></td>
<td></td>
<td>Action Code (07h)</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td>(MSB)</td>
<td></td>
<td></td>
<td>Page Length (60h)</td>
<td>(LSB)</td>
</tr>
<tr>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td>(MSB)</td>
<td></td>
<td></td>
<td>Link Failure Count, Port A</td>
<td>(LSB)</td>
</tr>
<tr>
<td>4</td>
<td></td>
<td></td>
<td></td>
<td>(MSB)</td>
<td></td>
<td></td>
<td>Loss of Sync Count, Port A</td>
<td>(LSB)</td>
</tr>
<tr>
<td>7</td>
<td></td>
<td></td>
<td></td>
<td>(MSB)</td>
<td></td>
<td></td>
<td>Loss of Signal Count, Port A</td>
<td>(LSB)</td>
</tr>
<tr>
<td>8</td>
<td></td>
<td></td>
<td></td>
<td>(MSB)</td>
<td></td>
<td></td>
<td>Primitive Sequence Protocol Error, Port A</td>
<td>(LSB)</td>
</tr>
<tr>
<td>11</td>
<td></td>
<td></td>
<td></td>
<td>(MSB)</td>
<td></td>
<td></td>
<td>Invalid Transmission Word Count, Port A</td>
<td>(LSB)</td>
</tr>
<tr>
<td>12</td>
<td></td>
<td></td>
<td></td>
<td>(MSB)</td>
<td></td>
<td></td>
<td>Invalid CRC Count, Port A</td>
<td>(LSB)</td>
</tr>
<tr>
<td>15</td>
<td></td>
<td></td>
<td></td>
<td>(MSB)</td>
<td></td>
<td></td>
<td>LIP F7 Initiated Count, Port A (valid in loop mode only)</td>
<td>(LSB)</td>
</tr>
<tr>
<td>16</td>
<td></td>
<td></td>
<td></td>
<td>(MSB)</td>
<td></td>
<td></td>
<td>LIP F7 Received Count, Port A (valid in loop mode only)</td>
<td>(LSB)</td>
</tr>
<tr>
<td>19</td>
<td></td>
<td></td>
<td></td>
<td>(MSB)</td>
<td></td>
<td></td>
<td>LIP F8 Initiated Count, Port A (valid in loop mode only)</td>
<td>(LSB)</td>
</tr>
<tr>
<td>20</td>
<td></td>
<td></td>
<td></td>
<td>(MSB)</td>
<td></td>
<td></td>
<td>LIP F8 Received Count, Port A (valid in loop mode only)</td>
<td>(LSB)</td>
</tr>
<tr>
<td>24</td>
<td></td>
<td></td>
<td></td>
<td>(MSB)</td>
<td></td>
<td></td>
<td>LIP F7 Initiated Count, Port A</td>
<td>(LSB)</td>
</tr>
<tr>
<td>27</td>
<td></td>
<td></td>
<td></td>
<td>(MSB)</td>
<td></td>
<td></td>
<td>LIP F7 Received Count, Port A</td>
<td>(LSB)</td>
</tr>
<tr>
<td>32</td>
<td></td>
<td></td>
<td></td>
<td>(MSB)</td>
<td></td>
<td></td>
<td>LIP F8 Initiated Count, Port A</td>
<td>(LSB)</td>
</tr>
<tr>
<td>35</td>
<td></td>
<td></td>
<td></td>
<td>(MSB)</td>
<td></td>
<td></td>
<td>LIP F8 Received Count, Port A</td>
<td>(LSB)</td>
</tr>
<tr>
<td>40</td>
<td></td>
<td></td>
<td></td>
<td>(MSB)</td>
<td></td>
<td></td>
<td>LIP F7 Initiated Count, Port A</td>
<td>(LSB)</td>
</tr>
<tr>
<td>43</td>
<td></td>
<td></td>
<td></td>
<td>(MSB)</td>
<td></td>
<td></td>
<td>LIP F7 Received Count, Port A</td>
<td>(LSB)</td>
</tr>
<tr>
<td>44</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reserved</td>
<td></td>
</tr>
<tr>
<td>52</td>
<td></td>
<td></td>
<td></td>
<td>(MSB)</td>
<td></td>
<td></td>
<td>Link Failure Count, Port B</td>
<td>(LSB)</td>
</tr>
<tr>
<td>55</td>
<td></td>
<td></td>
<td></td>
<td>(MSB)</td>
<td></td>
<td></td>
<td>Loss of Sync Count, Port B</td>
<td>(LSB)</td>
</tr>
<tr>
<td>56</td>
<td></td>
<td></td>
<td></td>
<td>(MSB)</td>
<td></td>
<td></td>
<td>Loss of Sync Count, Port B</td>
<td>(LSB)</td>
</tr>
<tr>
<td>59</td>
<td></td>
<td></td>
<td></td>
<td>(MSB)</td>
<td></td>
<td></td>
<td>Loss of Sync Count, Port B</td>
<td>(LSB)</td>
</tr>
</tbody>
</table>
Table 70: Link Status page (Continued)

<table>
<thead>
<tr>
<th>Bit Byte</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>60 . 63</td>
<td>(MSB)</td>
<td>Loss of Signal Count, Port B</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>64 . 67</td>
<td>(MSB)</td>
<td>Primitive Sequence Protocol Error, Port B</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>68 . 71</td>
<td>(MSB)</td>
<td>Invalid Transmission Word Count, Port B</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>72 . 75</td>
<td>(MSB)</td>
<td>Invalid CRC Count, Port B</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>76 . 79</td>
<td>(MSB)</td>
<td>LIP F7 Initiated Count, Port B</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>80 . 83</td>
<td>(MSB)</td>
<td>LIP F7 Received Count, Port B</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>84 . 87</td>
<td>(MSB)</td>
<td>LIP F8 Initiated Count, Port B</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>88 . 91</td>
<td>(MSB)</td>
<td>LIP F8 Received Count, Port B</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>92 . 99</td>
<td></td>
<td>Reserved</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

ESI Page
00h Identifies the ESI page code used (00h).

Action Code
07h Link Status.

Page Length
60h The length of the Link Status page (in bytes).

Link Failure Count
Count of the number of Loss of Sync conditions that have occurred on the port which exceeded 100 ms in duration.

Loss of Sync Count
The count of the number of short (< 100 ms) Loss of Synchronization conditions that have occurred on the port.

Loss of Signal Count
00h The count of the number of Loss of Signal conditions on the port (not supported).

Primitive Sequence Protocol Error, Port A
00h The number of FC-PH defined Primitive Sequence Protocol Errors on port A. This field is not valid in loop mode (not supported).
Invalid Transmission Word Count
The count of the number of invalid transmission words/running disparity errors that have been detected on the port.

Invalid CRC Count
The count of the number of write data frames that have been received with invalid CRCs on the port. These errors are only detected when this drive is the target of the data transfer.

LIP F7 Initiated Count
Count of the number of loop initialization processes originated by the port with LIP – F7’s (Initialize LIP).

LIP F7 Received Count
Count of the number of loop initialization processes initiated on the port by receiving LIP – F7’s (Initialize LIP).

LIP F8 Initiated Count
Count of the number of loop initialization processes originated by the port with LIP – F8’s (Failure LIP).

LIP F8 Received Count
Count of the number of loop initialization processes initiated on the port by receiving LIP – F8’s (Failure LIP).

Primitive Sequence Protocol Error, Port B
00h The number of FC-PH defined Primitive Sequence Protocol Errors on port B. This field is not valid in loop mode (not supported).

10.5.3.8 Spin-Down Control Action Specific Bits

Table 71: Spin-Down Control Action Specific Bits

<table>
<thead>
<tr>
<th>Bit Byte</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>Read Status</td>
<td>Enable Spin Down</td>
<td>Enable SpinDn Ctrl</td>
</tr>
</tbody>
</table>

Read Status
0  Device updates the state of the Enable Spin-Down and Enable SpinDn Ctrl as directed by the corresponding bits in the request.
1  Device ignores the state of the Enable Spin-Down and the Enable SpinDn Ctrl bits in the request and returns the current state of these enables without changing their state.

Enable Spin-Down
0  Device will not spin-down if Enable SpinDn Ctrl bit is 1, when it detects an Un-Mated Case on the motor control lines.
1  Device performs a spin-down if the Enable SpinDn Ctrl bit is a 1 and the Un-Mated Case is present on the motor control lines at the end of Enclosure Initiated ESI discovery. The bit will remain valid for a maximum of 3 seconds.

Enable SpinDn Ctrl
0  Un-Mated Case is present at the completion of Enclosure Initiated ESI discovery, this device performs a spin-down.
1  Device performs a spin-down if Enable Spin-Down bit is a 1 and the Un-Mated Case is present on the motor control lines at the end of Enclosure Initiated ESI discovery.

Note.  Refer to SCSI Command Reference Manual, 100293068, Section 1.6.7, Jumper Settings, for Motor Spin-Up options.

After processing a Spin-Down Control Enclosure Request, the device responds with the resulting status of the Spin-Down Control bits as formatted in Table 72.
10.5.3.8.1 Spin-Down Control Status

Table 72: Spin-Down Control Status

<table>
<thead>
<tr>
<th>Bit Byte</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td>ESI Page (00h)</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>Action Code (08h)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td>Page Length (06h)</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>4</td>
<td></td>
<td></td>
<td></td>
<td>Reserved</td>
<td>Enable Spin-Down</td>
<td>Enable SpinDn Ctrl</td>
<td></td>
<td></td>
</tr>
<tr>
<td>5 - 9</td>
<td></td>
<td></td>
<td></td>
<td>Reserved</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

ESI Page
00h Identifies the ESI page code used (00h).

Action Code
08h Spin-Down Control Status.

Page Length
06h The maximum length of the ESI page (in bytes).

10.5.3.9 ESI data validation

ESI Data Validation (EDV) provides a mechanism to verify correct data is transferred over the ESI. This function is optional with support discovered by negotiation. When EDV is enabled, a checksum is calculated and appended to each ESI transfer phase, ESI command, data, and Enclosure request.

The drive will request ESI data validation when the following occur:

1. The first host request to send or receive ESI data after a power on, a firmware download, or a drive reset caused by receiving a LIP(AL_PD, AL_PS), a LIP(FF, AL_PS) or a command frame with Target Reset bit set.

2. If EDV has already been negotiated and then a failure occurs during an ESI transfer which is not the result of a checksum error being detected.

If ESI data validation is negotiated through EI ESI after events 1 or 2, but before a new ESI command is received from the host, EDV will not be renegotiated when the host sends the command.

The Parameter Length field for the EDV command phase during EDV negotiation is 00h if the drive initiated EDV because of a host initiated ESI operation. If the drive is sending an EDV accept because of an enclosure initiated EDV request, the drive will set the Parameter length to 06h during the command phase for the EDV accept frame.
For ESI transfers initiated by a SCSI command, the drive will attempt up to two retries if communication with the enclosure initially fails or if the drive does not detect in discovery that the enclosure is SFF-8067 compliant. This applies to all product families that support ESI. During each retry the drive attempts the entire transfer beginning with discovery. If EDV has been negotiated with the enclosure previously, then EDV is renegotiated after each failed attempt and before the next if the failure does not occur because of a checksum failure.

**Table 73: ESI data validation accept**

<table>
<thead>
<tr>
<th>Bit</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>1</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>5 - 9</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**ESI Page**

00h Identifies the ESI page code used (00h).

**Action Code**

09h ESI Data Validation Accept.

**Page Length**

02h The maximum length of the ESI page (in bytes).
11.0  SCSI operations

SCSI information is transported in Fibre Channel frames. All SCSI Fibre Channel Protocol (SCSI-FCP) operations start with an FCP CMND (Command) frame and end with an FCP RSP (Response) frame. Operations initiated with an FCP CMND may be SCSI commands such as read or write data. The operations also include control operations called Task Management functions. Task Management functions provide reset and Task Set (queue) control.

11.1  SCSI-FCP

SCSI-FCP is an FC-4 mapping protocol for applying the SCSI command set to the Fibre Channel. This protocol retains the half-duplex nature of parallel SCSI within each I/O operation. For example, a single operation, such as a Read command, operates over a single port pair between the initiator and target.

11.1.1  FC-4 mapping layer

All devices communicating with a Seagate Fibre Channel disc drive must implement the SCSI-FCP mapping protocol. The FC-4 mapping layer uses the services provided by FC-PH to execute the steps required to perform the functions defined by the FC-4.

Note. The Command Descriptor Block (CDB) definitions in this clause are for reference only. A more complete description of the CDBs are found in the SCSI Command manual Seagate Publication Number 100293068. Commands supported by a particular drive will be included in the Product Manual for the drive.
### 11.2 FCP CMND

The content of the FCP CMND frame is shown below. Details of the FCP CMND contents are in Table 74 and Table 75.

<table>
<thead>
<tr>
<th>Frame header</th>
<th>Payload</th>
<th>CRC</th>
<th>EOFt</th>
</tr>
</thead>
</table>

Table 74: FCP CMND frame header

<table>
<thead>
<tr>
<th>Word</th>
<th>Byte</th>
<th>Bit</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>R_CTL (06h)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>D_ID</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reserved (00h)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>S_ID</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Type (08h)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>F_CTL</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>SEQ_ID</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>DF_CTL (00)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>SEQ_CNT (0000h)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>OX_ID</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Parameter</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
**R_CTL (Routing Control) field**
Set to Unsolicited Command data sequence for the FCP CMND frame.

**D_ID (Destination Identifier) field**
The address of the drive. This value must match the current address of the drive.

**S_ID (Source Identifier) field**
The address of the initiator that sent the frame. This address is used by the drive to return any responses that may be required by the operation.

**TYPE field**
All SCSI FCP frames must be set to 08h.

**F_CTL (Frame Control) field**
290000h for FCP CMND frames. This indicates the frame is the first sequence of the exchange and last frame of that sequence. Sequence initiative is also transferred so the drive may respond to the FCP CMND.

**SEQ_ID (Sequence Identifier) field**
May be set to any value. The OX_ID field uniquely identifies each command between the initiator and the drive.

**DF_CTL (Data Field Control) field**
The bits in the DF_CTL field indicate any optional headers that may be present. The DF_CTL field shall be set to 00h (i.e., no optional headers) or 40h (i.e., Encapsulating Security Payload).

- **00h** No optional Fibre Channel headers are used.

**SEQ_CNT (Sequence Count) field**
0000h value indicates that this is the first frame of the Fibre Channel sequence. The FCP CMND is a single frame sequence.

**OX_ID (Originator Exchange Identifier) field**
Assigned by the initiator. This value must be unique for all commands issued by this initiator to the drive. If duplicate OX_IDs are detected by the drive for uncompleted commands from an initiator, an overlap command error will be returned.

**RX_ID (Responder Identifier) field**
Not used by the drive. FFFFh indicates the RX_ID is not being used.

**Parameter field**
Not used for the FCP CMND.
### Logical Unit Number (LUN) field

Addresses physical devices or virtual devices attached to a target.

0  Zero is the only valid LUN number for the drives supported by this manual.

The drive will reject Inquiry, Test Unit Ready, and Request Sense commands that select an invalid LUN by sending Check Condition status in the FCP RSP frame. Inquiry commands will return Inquiry Data with the Peripheral Device Type field set to Logical Unit Not Present (7Fh). Request Sense and Inquiry commands will send Check Condition status in response to an invalid LUN selection. For all other commands, the Logical Unit field is not tested.

### COMMAND REFERENCE NUMBER field

The COMMAND REFERENCE NUMBER (CRN) field contains the number sent by the initiator FCP_Port to assist in performing precise delivery checking for FCP commands. If precise delivery is enabled, a nonzero value in the CRN field shall be treated as a command reference number in determining the receipt and ordering of commands from a particular initiator FCP_Port to the particular logical unit as described in FCP-3 section 4. If precise delivery is enabled, a zero value in the CRN field indicates that command shall not be verified for precise delivery. If precise delivery checking is not enabled, the COMMAND REFERENCE NUMBER field shall be ignored by the device server. If the FCP_CMND IU

---

### Table 75: FCP CMND Payload

<table>
<thead>
<tr>
<th>Bit</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>(MSB)</td>
<td>LOGICAL UNIT NUMBER (FCP_LUN)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>4</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>5</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>6</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>7</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td>8</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>COMMAND REFERENCE NUMBER</td>
<td></td>
</tr>
<tr>
<td>9</td>
<td>Reserved</td>
<td>PRIORITY</td>
<td></td>
<td>TASK ATTRIBUTE</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

#### TASK MANAGEMENT FLAGS

<table>
<thead>
<tr>
<th>10</th>
<th>Term Task</th>
<th>Clear ACA</th>
<th>LUN Reset</th>
<th>Reserved</th>
<th>Clear Task Set</th>
<th>Abort Task Set</th>
<th>Reserved</th>
</tr>
</thead>
<tbody>
<tr>
<td>11</td>
<td></td>
<td>ADDITIONAL FCP_CDB LENGTH = (n-27)</td>
<td>RDDATA</td>
<td>WRDATA</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>12</td>
<td>(MSB)</td>
<td>FCP_CDB</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>13</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>14</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>15</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>16</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>17</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>18</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>19</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>20</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>21</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>22</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>23</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>24</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>25</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>26</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>27</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td>28</td>
<td></td>
<td></td>
<td>ADDITIONAL FCP_CDB (if any)</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>n</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>n+1</td>
<td>(MSB)</td>
<td>FCP_DL</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>n+2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>n+3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>n+4</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>n+5</td>
<td>(MSB)</td>
<td>FCP_BIDIRECTIONAL_READ_DL (if any)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>n+6</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>n+7</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>n+8</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
</tbody>
</table>

---

140

Fibre Channel Interface Manual, Rev. A
specifies a task management function, the CRN field shall be reserved and set to zero and the FCP_CMND IU shall not be verified for precise delivery.

**PRIORITY field**
The PRIORITY field specifies the relative scheduling of this task in relation to other tasks already in the task set for processing by the device server (see SAM-3). If the TASK ATTRIBUTE field contains a value other than SIMPLE, then this field is reserved.

**Task Attribute field**
Specifies the type of command queue management requested for the SCSI command in the CDB field of this FCP CMND. The drive supports:

- **0** Simple Queue. Specifies that the I/O process be placed in the drive’s I/O process queue for execution. The order of execution can be arranged by the disc drive in accordance with a performance optimization algorithm.
- **1** Head of Queue. Specifies that the I/O process be placed first in that logical unit’s queue for the initiator originating the I/O process. An I/O process already being executed by the drive is not preempted. A subsequent I/O process received with a Head of Queue attribute is placed at the head of the queue for execution in last-in, first-out order.
- **2** Ordered Queue. Specifies that the I/O process be placed in the disc drive’s I/O process queue for execution in the order received, with respect to other commands with Ordered Queue attributes, except for I/O processes received with a Head of Queue, which are placed at the head of the queue.
- **4** ACA Queue. When an ACA condition is active in the drive, only FCP CMNDs with the ACA Queue attribute received from the initiator that originated the I/O process that caused the ACA condition will be executed.
- **5** Untagged Queue. Allows the drive to accept only one command from each initiator. If another command is received for an initiator with an active command, the drive will return a check in the FCP RSP.

**Task Management Flags field**
The TASK MANAGEMENT FLAGS field consists of the Terminate Task (TERM TASK), Clear ACA, LOGICAL UNIT RESET, CLEAR TASK SET, AND ABORT TASK SET bits. These flags request that a task management function be performed. Task management functions shall be requested by the initiator FCP_Port (Exchange Originator) using a new Exchange. If any task management flag bit is set to one, the FCP_CDB field, the FCP_DL field, the TASK ATTRIBUTE field, the RDDATA bit, and the WRDATA bit shall be ignored. If any bit in the TASK MANAGEMENT FLAGS field is set to one, the FCP_BIDIRECTIONAL_READ_DL field shall not be included in the FCP_CMND IU payload. If more than one task management flag bit is set to one in any FCP_CMND IU, the task management functions shall not be processed and the FCP_RSP IU shall contain the RSP_CODE field set to 02h (i.e., FCP_CMND fields invalid).

**Table 76: TASK MANAGEMENT FLAGS field**

<table>
<thead>
<tr>
<th>Bit</th>
<th>Task Management Function a</th>
</tr>
</thead>
<tbody>
<tr>
<td>7</td>
<td>Obsolete</td>
</tr>
<tr>
<td>6</td>
<td>CLEAR ACA</td>
</tr>
<tr>
<td>5</td>
<td>Obsolete</td>
</tr>
<tr>
<td>4</td>
<td>LOGICAL UNIT RESET</td>
</tr>
<tr>
<td>3</td>
<td>Reserved</td>
</tr>
<tr>
<td>2</td>
<td>CLEAR TASK SET</td>
</tr>
<tr>
<td>1</td>
<td>ABORT TASK SET</td>
</tr>
<tr>
<td>0</td>
<td>Reserved</td>
</tr>
</tbody>
</table>

a The ABORT TASK SET management function is specified in FCP-3 Clause 4.9
Term Task (Terminate Task)
Not supported by the drive. Declared Obsolete by T10.

Clear ACA (Clear Auto Contingent Allegiance)
When this bit is set to one (1) by the initiator that caused the ACA condition, the drive will clear the ACA condition and allow the drive to resume normal processing of commands.

LUN (Logical Unit Number) Reset
When the drive receives a LUN Reset, it clears the command queue for all initiators and returns a Unit Attention status in response to the next command received from all initiators. N_Port and Process Logins are preserved. This bit was formerly called Target Reset.

Clear Task Set
Clears the queue for all initiators. A Unit Attention condition is created for all initiators with commands in the queue other than the initiator originating the Clear Task Set.

Abort Task Set
Clears only the queue of commands from the initiator originating the Abort Task Set.

ADDITIONAL FCP_CDB LENGTH field
The ADDITIONAL FCP_CDB LENGTH field contains the length in 4-byte words of the ADDITIONAL FCP_CDB field. The value of the ADDITIONAL FCP_CDB LENGTH field shall be set to zero for task management requests.

FCP_CDB (Command Descriptor Block) field
Always 16 bytes long. The actual contents depends on the command type. Unused bytes are not checked by the drive. If any of the Task Management flags are set in byte 10, the CDB field is ignored.

RDATA 9 (Read Data) bit
Set to one (1) when the command specified by the CDB field will result in a data transfer to the initiator.

WRDATA (Write Data) bit
Set to one (1) when the command specified by the CDB field will result in a data transfer from the initiator.

FCP_CDB field
The FCP_CDB field contains the CDB to be sent to the addressed logical unit. The maximum CDB length is 16 bytes unless the ADDITIONAL FCP_CDB_LENGTH field has specified that there is an ADDITIONAL_FCP_CDB field. The FCP_CDB shall be ignored if any task management flag is set to one.

The CDB format is defined by SAM-3 and the contents of the CDB are defined in the SCSI command standards. Bytes between the end of a CDB and the end of the FCP_CDB field or, if applicable, the ADDITIONAL_FCP_CDB field shall be reserved.

ADDITIONAL FCP_CDB field
The ADDITIONAL FCP_CDB field contains any CDB bytes beyond those contained within the 16 byte FCP_CDB field. The ADDITIONAL FCP_CDB field shall not be present if any task management flag is set to one. The contents of the field shall be those bytes of an extended CDB beyond the first 16 bytes of the CDB as defined in the SCSI command standards.

FCP_DL field
For a SCSI read operation, the FCP_DL field contains a count of the maximum number of all bytes to be transferred to the application client buffer in FCP_DATA IU payloads by the SCSI command. The FCP_DL field is the Data-In Buffer Size defined by SAM-3.

For a SCSI write operation, the FCP_DL field contains a count of the maximum number of all bytes to be transferred from the application client buffer in FCP_DATA IU payloads by the SCSI command. The FCP_DL field is the Data-Out Buffer Size defined by SAM-3.

For a bidirectional SCSI command, the FCP_DL field contains a count of the maximum number of all bytes to be transferred from the application client buffer in FCP_DATA IU payloads by the SCSI command. The FCP_DL field is the Data-Out Buffer Size defined by SAM-3.
An FCP_DL value of zero indicates that no data transfer is expected regardless of the state of the RDDATA and WRDATA bits and that no FCP_XFER_RDY or FCP_DATA IUs shall be transferred.

**FCP_BIDIRECTIONAL_READ_DL field**

For a bidirectional SCSI command, the FCP_BIDIRECTIONAL_READ_DL field contains a count of the maximum number of all bytes to be transferred to the application client buffer in FCP_DATA IU payloads by the SCSI command. The FCP_BIDIRECTIONAL_READ_DL field is the Data-In Buffer Size defined by SAM-3.

An FCP_BIDIRECTIONAL_READ_DL value of zero indicates that no read operation is expected regardless of the state of the RDDATA bit and that no FCP_DATA IUs shall be transferred for read data.

If either RDDATA or WRDATA is set to zero, the FCP_BIDIRECTIONAL_READ_DL field is not included in the FCP_CMND IU payload.
11.2.1 Command Descriptor Block (CDB)

A request by an initiator to a disc drive is performed by sending a Command Descriptor Block (CDB) to the disc drive. For several commands, the request is accompanied by a list of parameters sent in FCP DATA frames. See the specific commands for detailed information.

The Command Descriptor Block always has an operation code as the first byte of the command. This is followed by command parameters (if any) and a control byte. For all commands, if there is an invalid parameter in the Command Descriptor Block, the disc drive terminates the command without altering the medium. The format description for the Command Descriptor Block as supported by the disc drive is shown in Tables 78 and 79.

11.2.1.1 Operation Code

The Operation Code (Table 77) of the Command Descriptor Block has a Group Code field and a Command Code field. The three-bit Group Code field provides for eight groups of command codes. The five-bit Command Code field provides for 32 command codes in each group. Thus, a total of 256 possible operation codes exist. Operation codes are defined in SCSI Command Reference Manual, 100293068, Clause 3.0.

For the disc drive, the group code specifies one of the following groups:

- Group 0 - Six-byte commands (see Table 78)
- Group 1 - Ten-byte commands (see Table 79)
- Group 2 - Ten-byte commands (see Table 79)
- Group 3 - Reserved
- Group 4 - Sixteen-byte commands
- Group 5 - Twelve-byte commands
- Group 6 - Vendor specific
- Group 7 - Vendor specific (Variable length & Thirty Two byte commands)

Table 77: Operation Code format for CDB

<table>
<thead>
<tr>
<th>Bit</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Group Code</td>
<td>Command Code</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Table 78: Typical CDB for six-byte commands

<table>
<thead>
<tr>
<th>Bit</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Operation Code</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>Reserved</td>
<td>(MSB) Logical Block Address (if required)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td></td>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>Transfer Length (if required)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>Control Byte</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Operation code
See Section 11.2.1.3.
Logical Block Address
See Section 11.2.1.2.

Transfer Length
See Section 11.2.1.5.

Control Byte
See Section 11.2.1.6

Table 79: Typical CDB for ten-byte commands

<table>
<thead>
<tr>
<th>Bit Byte</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(MSB)</td>
<td>Logical Block Address (if required)</td>
<td></td>
</tr>
<tr>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>4</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>5</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td>6</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reserved</td>
<td></td>
</tr>
<tr>
<td>7</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(MSB)</td>
<td>Transfer Length (if required)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>8</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td>9</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Control Byte</td>
<td></td>
</tr>
</tbody>
</table>

Operation Code

RelAdr (Relative Address)
See Section 11.2.1.4.

Logical Block Address
See Section 11.2.1.2.

Transfer Length
See Section 11.2.1.5.

Control Byte
See Section 11.2.1.6.
Table 80. Typical CDB for 12-byte commands

<table>
<thead>
<tr>
<th>Bit</th>
<th>Byte</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>Misc</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>4</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>5</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td>6</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>7</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>8</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>9</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td>10</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>11</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Operation Code

RelAdr (Relative Address)
See Section 11.2.1.4.

Logical Block Address
See Section 11.2.1.2.

Transfer Length
See Section 11.2.1.5

Control Byte
See Section 11.2.1.6.
Table 81. Typical CDB for 16-byte commands

<table>
<thead>
<tr>
<th>Bit Byte</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>OPERACTION CODE</td>
</tr>
<tr>
<td>1</td>
<td>Misc</td>
<td>serv</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(if required)</td>
</tr>
<tr>
<td>2</td>
<td>(MSB)</td>
<td>log</td>
<td>(if required)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>4</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>5</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td>6</td>
<td>(MSB)</td>
<td>add</td>
<td>(if required)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>7</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>8</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>9</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td>10</td>
<td>(MSB)</td>
<td>tran</td>
<td>(if required)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>11</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>PARAMETER LIST LENGTH</td>
</tr>
<tr>
<td>12</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(if required)</td>
</tr>
<tr>
<td>13</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td>14</td>
<td></td>
<td>Misc</td>
<td>CDB information</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>15</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>CONTROL</td>
</tr>
</tbody>
</table>
The SERVICE ACTION field specifies the action being requested by the application client. The SERVICE ACTION field is required in the variable length CDB format and is described in SPC-3. Each service action code description defines a number of service action specific fields that are needed for that service action.

### 11.2.1.2 Logical block address

The logical block address in the Command Descriptor Block begins with block zero and is continuous up to the last logical block on drive.

Group 0 command descriptor block contains 21-bit logical block addresses. Groups 1 and 2 command descriptor blocks contain 32-bit logical block addresses.

The logical block concept implies that the initiator and target have previously established the number of data bytes per logical block. This may be established through the use of the Read Capacity command or the Mode Sense command or by prior arrangement.

The maximum logical block address for the disc drive which is accessible by the initiator is defined in Read Capacity Command data in SCSI Command Reference Manual, 100293068, Section 1.26.
11.2.1.3 **Operation code**

Operation codes are defined in SCSI Command Reference Manual, 100293068, Section 3.0.

11.2.1.4 **Relative address bit**

Relative addressing is a technique of accessing logical blocks relative to the logical blocks accessed in a previous linked command. Seagate fibre channel drives do not support relative addressing.

11.2.1.5 **Transfer length**

The Transfer Length field specifies the amount of data to be transferred, usually the number of blocks. For several commands, the Transfer Length indicates the requested number of bytes to be sent as defined in the command description. For these commands, the Transfer Length field may be identified by a different name. See the following descriptions and the individual command descriptions for further information.

Commands that use one byte for the Transfer Length field allow up to 256 blocks of data to be transferred by one command. A Transfer Length field value of 1 to 255 indicates the number of blocks that are transferred. A value of zero indicates 256 blocks.

Commands that use two bytes for the Transfer Length field allow up to 65,535 blocks of data to be transferred by one command. In this case, a Transfer Length of zero indicates that no data transfer takes place. A value of 1 to 65,535 indicates the number of blocks that are transferred.

For several commands, more than two bytes are allocated for the Transfer Length field. Refer to the specific command description for further information.

The Transfer Length field of the commands that are used to send a list of parameters to a disc drive is called the Parameter List Length field. The Parameter List Length field specifies the number of bytes sent during the FCP DATA sequences for the command.

The Transfer Length field of the commands used to return sense data (e.g., Request Sense, Inquiry, Mode Sense, etc.) to an initiator is called the Allocation Length field. The Allocation Length field specifies the number of bytes that the initiator has allocated for returned data. The disc drive terminates the data in the FCP DATA sequence when Allocation Length bytes have been transferred or when all available data have been transferred to the initiator, whichever is less.

11.2.1.6 **Control byte**

Normally all zeros unless the extended features of ACA or Link commands are being used.

**Table 83: Control byte**

<table>
<thead>
<tr>
<th>Bit</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>LAST</td>
<td>Reserved</td>
<td>NACA</td>
<td>Flag</td>
<td>Link</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**NACA (Normal Auto Contingent Allegiance)**

1 Enables ACA handling rules for the command. If a Check Condition occurs during the processing of the command, an ACA condition is entered.

0 Disables ACA handling rules for the command.

**Flag**

Drives supported by this manual do not use this bit.
Link
This bit is set to one (1) to indicate that the initiator desires an automatic link to the next command upon successful completion of the current command. If the link bit is one (1), upon successful termination of the command, the drive returns Intermediate status in the FCP RSP frame.

Note. The OX_ID must be the same for all linked commands because it is considered the same exchange.

### 11.3 FCP XFER RDY

The FCP XFER RDY (Transfer Ready) frame is sent by the drive when it requests data for a transfer to the drive. Examples of commands resulting in data transfers to the drive are Write, Mode Select, and Write Buffer.

<table>
<thead>
<tr>
<th>Table 84: FCP XFER RDY header</th>
</tr>
</thead>
<tbody>
<tr>
<td>Word</td>
</tr>
<tr>
<td>-------</td>
</tr>
<tr>
<td>0</td>
</tr>
<tr>
<td></td>
</tr>
<tr>
<td></td>
</tr>
<tr>
<td></td>
</tr>
<tr>
<td>1</td>
</tr>
<tr>
<td></td>
</tr>
<tr>
<td></td>
</tr>
<tr>
<td></td>
</tr>
<tr>
<td>2</td>
</tr>
<tr>
<td></td>
</tr>
<tr>
<td></td>
</tr>
<tr>
<td></td>
</tr>
<tr>
<td>3</td>
</tr>
<tr>
<td></td>
</tr>
<tr>
<td></td>
</tr>
<tr>
<td></td>
</tr>
<tr>
<td>4</td>
</tr>
<tr>
<td></td>
</tr>
<tr>
<td></td>
</tr>
<tr>
<td></td>
</tr>
<tr>
<td>5</td>
</tr>
<tr>
<td></td>
</tr>
<tr>
<td></td>
</tr>
<tr>
<td></td>
</tr>
</tbody>
</table>

**R_CTL (Routing Control)**
Set to Data Descriptor.
**D_ID (Destination Identifier)**
The address of the initiator that originated the command for which the data is being requested.

**S_ID (Source Identifier)**
Address of the drive.

**Type**
08h  For all SCSI FCP frames.

**F_CTL (Frame Control)**
Set to 890000h for FCP CMND frames. This indicates the frame is sent by the responder of the exchange, not the originator, and the frame is the last of the Fibre Channel sequence. Sequence initiative is transferred so the initiator may send the requested data.

**SEQ_ID (Sequence Identifier)**
The drive sends FFh.

**DF_CTL (Data Field Control)**
00h  No optional Fibre Channel headers are used.

**SEQ_CNT (Sequence Count)**
0000h  Indicates that this is the first frame of the Fibre Channel sequence. The FCP XFER RDY is a single frame sequence.

**OX_ID (Originator Exchange Identifier)**
The drive returns the OX_ID it received from the initiator with the FCP CMND.

**RX_ID (Responder Identifier)**
Not used by the drive. The value of FFFFh indicates the RX_ID is not being used.

**Parameter**
Not used for the FCP XFER RDY.

### Table 85: FCP XFER RDY Payload

<table>
<thead>
<tr>
<th>Bit Byte</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>4</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>5</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>6</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>7</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>8</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>9</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>10</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>11</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Relative Offset (FCP_DATA_RO)  (LSB)

Burst Length (FCP_BURST_LEN)  (LSB)

Reserved  (LSB)
Relative Offset (FCP_DATA_RO)
The byte offset of the requested transfer relative to the first byte of the data addressed in the CDB.

Burst Length (FCP_BURST_LEN)
The amount of data (in bytes) requested by the drive for transfer in this Fibre Channel sequence. The maximum length the drive will request is the remaining data to complete the transfer, the Maximum Burst Size in the Disconnect/Reconnect SCSI mode (page 2), or the maximum length that may be transferred in a Fibre Channel sequence, whichever is less.
### 11.4 FCP DATA

The payload of FCP DATA frames transfer the user data associated with a command.

<table>
<thead>
<tr>
<th>FCP DATA format</th>
<th>Note: EOFt is used on the last frame of a sequence. Single frame sequences end with an EOFt. All other FCP DATA frames use EOFn.</th>
</tr>
</thead>
<tbody>
<tr>
<td>SOFi3</td>
<td></td>
</tr>
<tr>
<td>Frame Header</td>
<td></td>
</tr>
<tr>
<td>Payload (Data)</td>
<td></td>
</tr>
<tr>
<td>CRC</td>
<td></td>
</tr>
<tr>
<td>EOFn or EOFt</td>
<td></td>
</tr>
</tbody>
</table>

#### Table 86: FCP DATA frame header

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit</th>
<th>Byte</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>3</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>3</td>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
**R_CTL (Routing Control)**
Set to Solicited Data for the FCP DATA frame.

**D_ID (Destination Identifier)**
The address of the drive if transfer is data to the drive and the address of the initiator originating the command if the transfer is from the drive.

**S_ID (Source Identifier)**
The address of the initiator originating the command if transfer is data to the drive and the address of the drive if the transfer is from the drive.

**Type**
08h For all SCSI FCP frames.

**F_CTL (Frame Control)**
This field is determined by the direction of the transfer and whether or not the frame is the last frame of the sequence.
For transfers to the drive, the initiator sets the F_CTL to 000008h or 000000h for all frames except the last frame of the sequence. These values indicate the frame is from for the originator of the exchange (command) and whether the parameter field is the relative offset. For the last frame of the sequence, the initiator sets the F_CTL to 09000Xh. This indicates the frame is the last frame of the sequence and sequence initiative is transferred so the drive may send an additional FCP XFER RDY or FCP RSP. The X represents the lower four bits of the F_CTL.
For transfers to the initiator, the drive sets the F_CTL to 800008h for all frames but the last frame of the sequence. This indicates the frame is from the responder of the exchange (command) and the parameter field is the relative offset. For the last frame of the sequence, the drive sets the F_CTL to 88000Xh. This indicates the frame is the last frame of the sequence. Sequence initiative is held for transfer of another sequence or the FCP RSP. The X has the same meaning as defined above.

**SEQ_ID (Sequence Identifier)**
For transfers to the drive, the drive captures the SEQ_ID from the first frame of the sequence and requires all subsequent frames of the sequence to have the same SEQ_ID. For transfers to the initiator, the drive sets the SEQ_ID to 00h for the first data sequence of a command (exchange). The SEQ_ID is sequentially increased for additional data sequences, if required, for the command.

**DF_CTL (Data Field Control)**
00h Indicates no optional Fibre Channel headers are used.

**SEQ_CNT (Sequence Count)**
Set to 0000h on the first frame transferred. The SEQ_CNT is required to continually increase through a sequence and across sequence boundaries if additional sequences are required to complete the transfer. The drive requires all received frames to be in sequential order. Frames originated by the drive are sent in sequential order.

**OX_ID (Originator Exchange Identifier)**
The value assigned by the initiator in the FCP CMND.
RX_ID (Responder Identifier)
Not used by the drive. The value of FFFFe indicates the RX_ID is not being used.

RO (Relative Offset)
The RO is a byte count offset between the first byte of the transfer address identified in the command and the first byte of data in the frame payload. The drive sends a continually increasing RO on data frames when sending data. The drive does not require or check the RO field on frames it receives. The drive uses the SEQ_CNT field to verify frames are received in order.
11.5 FCP RSP

An FCP Response (RSP) frame is returned by the drive for each FCP CMND operation unless the drive receives:

1. A Clear Task Set
2. An Abort Task Set
3. A Target Reset
4. A Loop Initialization Primitive Sequence (LIP) Reset
5. An Abort Sequence (ABTS) for the command

Table 88: FCP RSP header

<table>
<thead>
<tr>
<th>Word</th>
<th>Bit</th>
<th>Byte</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>7</td>
<td>6</td>
</tr>
<tr>
<td>0</td>
<td>3</td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td>(MSB)</td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>3</td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td>(MSB)</td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>3</td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td>(MSB)</td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>3</td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>3</td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>3</td>
<td></td>
</tr>
<tr>
<td></td>
<td>2</td>
<td></td>
</tr>
<tr>
<td></td>
<td>1</td>
<td></td>
</tr>
<tr>
<td></td>
<td>0</td>
<td></td>
</tr>
</tbody>
</table>

R_CTL (Routing Control)

Set to Command Status for the FCP RSP frame.
D_ID (Destination Identifier)
The address of the initiator that originated the command.

S_ID (Source Identifier)
The address of the drive.

Type
08h The value for all SCSI FCP frames.

F_CTL (Frame Control)
Set to 990000h for FCP RSP frames. This indicates the frame is sent by the responder of the exchange, not the originator, the frame is the last frame of the sequence (FCP RSP is a single frame sequence for this drive), and the sequence is the last for the Fibre Channel exchange.

SEQ_ID (Sequence Identifier)
Set to FFh by the drive.

DF_CTL (Data Field Control)
00h Indicates no optional Fibre Channel headers are used.

SEQ_CNT (Sequence Count)
Set to 0000h to indicate this the first frame of the Fibre Channel sequence. The FCP RSP is a single frame sequence.

OX_ID (Originator Exchange Identifier)
The value assigned by the initiator in the FCP CMND. The OX_ID field uniquely identifies each command between the initiator and the drive.

RX_ID (Responder Identifier)
Not used by the drive. The value of FFFFh indicates the RX_ID is not being used.

Parameter
Not used for the FCP RSP.
### Table 89: FCP RSP Payload

<table>
<thead>
<tr>
<th>Bit/Byte</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0 (MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>7</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reserved (LSB)</td>
</tr>
<tr>
<td>8 (MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>RETRY DELAY TIMER (LSB)</td>
</tr>
<tr>
<td>9</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td>10</td>
<td>BIDI RESP</td>
<td>BIDI READ RESP</td>
<td>BIDI READ RESP UNDER-RUN</td>
<td>CONF REQ</td>
<td>Resid Under Run</td>
<td>Resid Over Run</td>
<td>Sense Length Valid</td>
<td>RSP Length Valid</td>
</tr>
<tr>
<td>11</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>SCSI Status Code</td>
</tr>
<tr>
<td>12 (MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>FCP_RESID</td>
</tr>
<tr>
<td>13</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td>14</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>15</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td>16 (MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Sense Length (=n)</td>
</tr>
<tr>
<td>17</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>FCP_SNES_LEN</td>
</tr>
<tr>
<td>18</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td>19</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>20 (MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Response Length (=m)</td>
</tr>
<tr>
<td>21</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>FCP_RSP_LEN</td>
</tr>
<tr>
<td>22</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td>23</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>24 (MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Response Information (FCP_RESP_INFO) (m bytes long) (if any) (see table 90) (LSB)</td>
</tr>
<tr>
<td>24+m</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>24+m+n</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>SCXI Sense Information (FCP_SENS_INFO) (n bytes long) (if any) (LSB)</td>
</tr>
<tr>
<td>24+m+n+n</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>27+m+n+n</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Bidirectional Read Residue FCP_BIDIRECTIONAL_READ_RESID (if any) (LSB)</td>
</tr>
</tbody>
</table>

**RETRY DELAY TIMER field**

The RETRY DELAY TIMER field contains the retry delay timer code (see SAM-4).

**FCP_BIDI_RSP bit**

1. If the FCP_BIDI_RSP bit is set to one, the FCP_BIDIRECTIONAL_READ_RESID field is present, and the FCP_BIDI_READ_RESID_OVER and FCP_BIDI_READ_RESID_UNDER bits are valid.
0 If the FCP_BIDI_RSP bit is set to zero, the FCP_BIDIRECTIONAL_READ_RESID field is not present, and the FCP_BIDI_READ_RESID_OVER and the FCP_BIDI_READ_RESID_UNDER bits are not valid.

**FCP_BIDI_READ_RESID_UNDER bit**
1 If the FCP_BIDI_READ_RESID_UNDER bit is set to one, the FCP_BIDIRECTIONAL_READ_RESID field is valid and contains the count of bytes that were expected to be transferred, but were not transferred. The application client shall examine the FCP_BIDIRECTIONAL_READ_RESID FIELD field in the context of the command to determine whether or not an error condition occurred.

**FCP_BIDI_READ_RESID_OVER bit**
1 If the FCP_BIDI_READ_RESID_OVER bit is set to one, the FCP_BIDIRECTIONAL_READ_RESID field is valid and contains the count of bytes that were not transferred because the FCP_BIDIRECTIONAL_READ_DL value was not large enough. The application client shall examine the FCP_BIDIRECTIONAL_READ_RESID FIELD field in the context of the command to determine whether or not an error condition occurred.

**FCP_CONF_REQ bit**
1 If the FCP_CONF_REQ bit is set to one, the initiator FCP_Port shall transmit an FCP_CONF IU to confirm receipt of the FCP_RSP Sequence. If the FCP_CONF_REQ bit is set to zero, the initiator FCP_Port shall not transmit an FCP_CONF IU.

**Resid Under Run (Residual Under Run)**
1 The number of bytes transferred was less than the DL of the FCP CMND by the byte count in the Residual Count field, bytes 12–15.

**Resid Over Run (Residual Over Run)**
1 The byte count in the Residual Count field, bytes 12–15, is the number of bytes not transferred because the length of the transfer in the CDB exceeded the DL field in the FCP CMND.

**Sense Length Valid**
1 Additional SCSI sense information (extended sense data) is included in the payload. The length is given in the Length of Sense Information field, bytes 16–19.

**RSP Length Valid (Response Length Valid)**
1 If the FCP_RSP_LEN_VALID bit is set to one, the FCP_RSP_INFO field contains valid information, the FCP_RSP_LEN field is valid and non-zero and contains the count of bytes in the FCP_RSP_INFO field. The application client shall examine the FCP_RSP_INFO field to determine whether or not an error condition occurred. When the FCP_RSP_LEN_VALID bit is set to one, the content of the SCSI STATUS CODE field is not reliable and shall be ignored by the application client.

1 For task management functions transmitted to the logical unit using an FCP_CMND IU, the FCP_RSP_LEN_VALID bit shall be set to one, the FCP_RSP_LEN field shall be set to the specified value, and the information in the RSP_CODE field shall indicate the completion status of the task management function.

0 If the FCP_RSP_LEN_VALID bit is set to zero, the FCP_RSP_LEN field is not valid and shall be treated as if its value were zero. When the FCP_RSP_LEN_VALID bit is set to zero, the FCP_RSP_INFO field shall have a length of zero and shall not be present.

**SCSI Status**
The ending (returned) status for FCP CMND operations with a valid CDB field.

<table>
<thead>
<tr>
<th>Value</th>
<th>Definition</th>
</tr>
</thead>
<tbody>
<tr>
<td>00h</td>
<td>Good. This status indicates that the target has successfully completed the command.</td>
</tr>
<tr>
<td>02h</td>
<td>Check Condition. Any error, exception, or abnormal condition that causes sense data to be sent, causes a Check Condition status. The extended sense data the drive has regarding the nature of the condition will be in the SCSI Sense Information in the FCP RSP frame with the Check Condition.</td>
</tr>
<tr>
<td>08h</td>
<td>Busy. The drive is busy. This status is returned whenever a drive is unable to process the command from an otherwise acceptable initiator. The normal initiator recovery action is to issue the command again at a later time.</td>
</tr>
<tr>
<td>10h</td>
<td>Intermediate. This status is returned for every command in a series of linked commands (except the last command), unless an error, exception or abnormal condition causes a Check Condition status, a Reservation Con-</td>
</tr>
</tbody>
</table>
Conflict status or a Command Terminated status to be set. If this status is not returned, the chain of linked commands is broken; no further commands in the series are executed.

18h Reservation Conflict. This status is returned whenever a SCSI device attempts to access a logical unit or an extent within a logical unit that is reserved with a conflicting reservation type for another SCSI device (see Reserve and Reserve Unit command). The normal initiator recovery action is to issue the command again at a later time.

28h Task Set (queue) Full. This status is implemented if tagged queuing is implemented. This status is returned when a command is received and the command can not be accepted because the command queue is full. The command is not executed.

30h ACA Active. This status is returned when an auto contingent allegiance (ACA) exists with another initiator. The initiator may reissue the command after the ACA condition has been cleared.

RESID (Residual ID)
For read operations and write operations, if the FCP_RESID_UNDER bit is set to one, the FCP_RESID field contains a count of the number of residual data bytes that were not transferred in FCP_DATA IUs.
For read operations and write operations, if the FCP_RESID_OVER bit is set to one, the FCP_RESID field contains the excess of the number of bytes required by the SCSI command to be transferred over the number of bytes specified by the FCP_DL field.
For bidirectional SCSI commands, if the FCP_RESID_UNDER bit is set to one, the FCP_RESID field contains a count of the number of residual data bytes that were not transferred in the Data-Out FCP_DATA IUs for the command.
For bidirectional SCSI commands, if the FCP_RESID_OVER bit is set to one, the FCP_RESID field contains the excess of the number of bytes required to be transferred in the Data-Out FCP_DATA IUs by the command over the number of bytes specified in the FCP_DL field.
For bidirectional SCSI commands, the FCP_BIDIRECTIONAL_READ_RESID field (see FCP-3 9.5.13) contains the corresponding count for Data-In FCP_DATA IUs.
Upon successful completion of an FCP I/O operation, the residual value is normally zero and the FCP_RESID value is not valid. FCP devices having indeterminate data lengths may have a nonzero residual byte count after completing valid operations. There is no requirement to verify that the data length implied by the contents of the CDB does not cause an overrun or underrun before beginning the processing of a SCSI command.
If the FCP_RESID_UNDER bit is set to one, a transfer that did not fill the buffer to the expected displacement FCP_DL was performed and the value of FCP_RESID is defined as follows:

\[ \text{FCP.RESID} = \text{FCP.DL} - (\text{highest offset of any byte transmitted} + 1) \]

A condition of FCP_RESID_UNDER may not be an error for some FCP devices and some commands.
If the FCP_RESID_OVER bit is set to one, refer to FCP-3 9.4.2 or 9.4.3. The FCP_RESID value is defined as follows:

\[ \text{FCP.RESID} = (\text{transfer length required by command}) - \text{FCP.DL} \]

If the FCP_RESID_UNDER and the FCP_RESID_OVER bits are set to zero, the FCP_RESID field is not meaningful and may have any value. The FCP_RESID field is always included in the FCP_RSP IU.

Note. Some early implementations presented the FCP_RSP IU without the FCP_RESID, FCP_SNS_LEN, and FCP_RSP_LEN fields if the FCP_RESID_UNDER, FCP_RESID_OVER, FCP_SNS_LEN_VALID, and FCP_RSP_LEN_VALID bits were all set to zero. This non-standard behavior should be tolerated.

Length of Sense Information
1 The Length of Sense Information contains the byte length of the additional SCSI sense information in the frame. The Length of Sense Information field is always transferred in the FCP_RSP frame.

Length of Response Information
1 The Length of Response Information contains the byte length of the FCP response information in the frame. Valid lengths for the response information are 0, 4, and 8. The Length of Response Information field is always transferred in the FCP_RSP frame.

Response Information
The FCP_RSP_INFO field contains information describing only the protocol failures detected during the processing of an FCP I/O operation. If none of the specified protocol failures have occurred, the FCP_RSP_INFO field shall not be included in the FCP_RSP IU and the FCP_RSP_LEN_VALID bit shall be zero. The FCP_RSP_INFO does not contain link error information, since FC-FS-2 provides the mechanisms for presenting such errors. The FCP_RSP_INFO field does not contain SCSI logical unit error information, since that is contained in the FCP_SNS_INFO field as described in FCP-3 9.5.17.
The FCP_RSP_INFO field shall contain valid information if the target FCP_Port detects any of the conditions indicated by an FCP_RSP_CODE. The format of the FCP_RSP_INFO field is specified in table 90.

**Table 90: Response Information format**

<table>
<thead>
<tr>
<th>Bit Byte (*)</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0 (24)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(MSB)</td>
</tr>
<tr>
<td>2 (26)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reserved (optional) (LSB)</td>
</tr>
<tr>
<td>3 (27)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>RESPONSE INFORMATION CODE</td>
</tr>
<tr>
<td>4 (28)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(MSB)</td>
</tr>
<tr>
<td>7 (31)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
</tbody>
</table>

*Fibre Channel frame byte number shown in parenthesis.

The valid Response Code values are shown in table 91.

**Table 91: Response Code (RSP_CODE) Definitions**

<table>
<thead>
<tr>
<th>Value</th>
<th>RSP_CODE definitions</th>
</tr>
</thead>
<tbody>
<tr>
<td>00h</td>
<td>Task Management function complete</td>
</tr>
<tr>
<td>01h</td>
<td>FCP_DATA length different than FCP_BURST_LEN</td>
</tr>
<tr>
<td>02h</td>
<td>FCP_CMND fields invalid</td>
</tr>
<tr>
<td>03h</td>
<td>FCP_DATA parameter mismatch with FCP_DATA_RO</td>
</tr>
<tr>
<td>04h</td>
<td>Task Management function rejected</td>
</tr>
<tr>
<td>05h</td>
<td>Task Management function failed</td>
</tr>
<tr>
<td>09h</td>
<td>Task Management function incorrect logical unit number</td>
</tr>
<tr>
<td>06h - 08h</td>
<td>Reserved</td>
</tr>
<tr>
<td>0Ah - FFh</td>
<td>Reserved</td>
</tr>
</tbody>
</table>

*Only valid when responding to task management functions

The completion status of the task management function is indicated by the RSP_CODE. If the Exchange is aborted before the FCP_RSP IU is returned, the completion status is unknown. If the RSP_CODE is set to 05h (i.e., Task Management function failed), the state of the logical unit is unknown.

Activities started by a task management function may continue after the FCP_RSP IU for the task management has been delivered.

**SCSI Extended Sense Information**

Additional information related to a Check Condition returned in the SCSI Status byte. See Table 92 on page 162.
11.5.1  Extended Sense Data format

The drive is capable of sending 18 bytes of extended sense data. The Extended Sense Data format is summarized in Table 92. The 1s and 0s shown in the tables below represent the logical 1s and 0s as sent by the disc drive.

Table 92: Disc Drive Extended Sense Data Summary

<table>
<thead>
<tr>
<th>Bit Byte (*)</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0 (32)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Validity Bit</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1 (33)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>0</td>
<td></td>
</tr>
<tr>
<td>Segment Number (00h)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2 (34)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Filemark</td>
<td></td>
<td>EOM</td>
<td>ILI</td>
<td>0</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>3 (35)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>4 (36)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>5 (37)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>6 (38)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td>7 (39)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Additional Sense Length 10 decimal (Max)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>8 (40)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(MSB)</td>
</tr>
<tr>
<td>(MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>9 (41)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>10 (42)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>11 (43)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>(LSB)</td>
</tr>
<tr>
<td>12 (44)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Additional Sense Code (ASC)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>13 (45)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Additional Sense Code Qualifier (ASCQ)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>14 (46)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Reserved for Seagate internal use only</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>15 (47)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>SKSV</td>
</tr>
<tr>
<td>16 (48)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>17 (49)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>18-n (50-n)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Product Unique Sense Data</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

*Fibre Channel frame byte number shown in parenthesis.

Note. If the Sense Key, Additional Sense Code and Additional Sense Code Qualifier combination is 04/8086 (a Read IOEDC error, see clause 2 of the SCSI Command manual), bytes 14-17 are the four IOEDC words associated with the IOEDC error.

Validity Bit

1  The Information bytes (Bytes 3–6) are valid.
0  The Information bytes (Bytes 3–6) are not valid.

Error Code

70h  Current error.
71h  Deferred error.

These two error conditions are described in Section 11.5.2.

Segment Number

Always zeros.
**Filemark**
Always zero (0) for disc drives.

**EOM (End of Medium)**
Always zero (0) for disc drives.

**ILI (Incorrect Length Indicator)**
The requested (previous command) block of data did not match the logical block length of the data on the medium.

**Sense Key**
General error category. These are listed in Table 97. The code given in byte 12 provides additional clarification of errors. See the Additional Sense Code field definition for related information.

**Information**
If the validity bit is one (1), these bytes contain the unsigned logical block address associated with the sense key. Unless otherwise specified, the Information bytes contain the address of the current logical block. For example, if the sense key is Medium Error, it is the logical block address of the failure block.

**Additional Sense Length 10 (Max)**
Specifies additional sense bytes are to follow. This is limited to a maximum of 10 (decimal) additional bytes. If the Allocation Length of the Command Descriptor Block is too small to transfer all of the additional sense bytes, the additional sense length is not adjusted to reflect the truncation.

**Command Specific Data**
These four bytes contain data for the command.

**Additional Sense Code and Additional Sense Code Qualifier**
Provide additional clarification of errors when Sense Key is valid. Error code definitions are in the Seagate SCSI Command manual clause 2. If the condition is not reportable by the disc drive, the Additional Sense Code and Additional Sense Code Qualifier are set to No Additional Sense Information (Code 0000).

**SKSV (Sense Key Specific Valid) and Sense Key Specific**
The additional sense bytes field may contain command specific data, peripheral device specific data, or vendor-specific data that further defines the nature of the Check Condition status. See Section 11.5.1.1.

The Sense Key Specific field is defined by this specification when the value of the SKSV bit is one (1). The definition of this field is determined by the value of the Sense Key field. This field is reserved for sense keys described in Table 97.

**Note.** During a format started by a Format Immediate command or a Device Self-test started by a Send Diagnostic command, bytes 16 and 17 become a progress indicator with values ranging from 0h to FFFFh. Value 0h indicates the operation just started and FFFFh indicates the operation is complete.

**Product Unique Sense Data**
Not presently used.
11.5.1.1 Sense Key Specific Valid (SKSV) and Sense Key Specific

Refer to the appropriate tables in this section for sense key specific values.

Table 93: Sense Key Specific reference tables

<table>
<thead>
<tr>
<th>Sense Key field value</th>
<th>SKSV</th>
<th>Error description</th>
<th>See table</th>
</tr>
</thead>
<tbody>
<tr>
<td>05h (Illegal Request)</td>
<td>1</td>
<td>These fields point to illegal parameters in command descriptor blocks and data parameters sent by the initiator.</td>
<td>94</td>
</tr>
<tr>
<td>01h (Recovered Error)</td>
<td>1</td>
<td>These fields identify the actual number of retries used in attempting to recover from the error condition.</td>
<td>95</td>
</tr>
<tr>
<td>04h (Hardware Error) or 03h (Medium Error)</td>
<td>1</td>
<td>These fields are only defined for the Format Unit command with the Immed bit set to one (1).</td>
<td>96</td>
</tr>
</tbody>
</table>

Table 94: Field Pointer bytes

<table>
<thead>
<tr>
<th>Bit Byte</th>
<th>15</th>
<th>16</th>
<th>17</th>
</tr>
</thead>
<tbody>
<tr>
<td>SKSV</td>
<td>SKSV</td>
<td>C/D</td>
<td>Reserved</td>
</tr>
<tr>
<td>(MSB)</td>
<td></td>
<td>BPV</td>
<td></td>
</tr>
<tr>
<td>Bit Pointer</td>
<td>Field Pointer</td>
<td>(LSB)</td>
<td></td>
</tr>
</tbody>
</table>

SKSV (Sense Key Specific Valid)
This bit indicates whether the sense key specific data is valid.
0 Data is not valid.
1 Data is valid. See Table 93.

C/D (Command Data)
1 The illegal parameter is in the command descriptor block.
0 The illegal parameter is in the data parameters sent by the initiator.

BPV (Bit Pointer Valid) and Bit Pointer
0 The value in the bit pointer field is not valid.
1 The bit pointer field specifies the bit of the byte designated by the field pointer that is in error. When a multiple-bit field is in error, the bit pointer field points to the most significant (left-most) bit of the field.

Field Pointer
The byte of the command descriptor block or of the parameter data that was in error. Bytes are numbered starting from zero, as shown in the tables describing the commands and parameters. When a multiple-byte field is in error, the pointer points to the most significant (left-most) byte of the field.

Note. Bytes identified as being in error are not necessarily the place that has to be changed to correct the problem.

Table 95: Actual Retry Count bytes

<table>
<thead>
<tr>
<th>Bit Byte</th>
<th>15</th>
<th>16</th>
<th>17</th>
</tr>
</thead>
<tbody>
<tr>
<td>SKSV</td>
<td>SKSV</td>
<td>Reserved</td>
<td></td>
</tr>
<tr>
<td>(MSB)</td>
<td></td>
<td>Actual Retry Count</td>
<td></td>
</tr>
<tr>
<td>(LSB)</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
SKSV (Sense Key Specific Valid)
This bit indicates whether the sense key specific data is valid.
0 Data is not valid.
1 Data is valid. See Table 93.

Actual Retry Count
Returns implementation-specific information on the actual number of retries used in attempting to recover an error or exception condition.

Note. This field relates to the retry count fields specified within the Verify Error Recovery Page (07h) parameters of the Mode Select command. See SCSI Command Reference Manual, 100293068, Section 1.13.6.

Table 96: Format Indication bytes

<table>
<thead>
<tr>
<th>Bit Byte</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>15</td>
<td>SKSV</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>16 (MSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>17 (LSB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

SKSV (Sense Key Specific Valid)
This bit indicates whether the sense key specific data is valid.
0 Data is not valid.
1 Data is valid. See Table 93.

Progress Indication
Percent complete indication in which the returned value is the numerator that has 65536 (10000h) as its denominator. The progress indication is based upon the total format operation including any certification or initialization operations.
Support or non-support for format progress indication is given in individual drive's Product Manual, Volume 1.

Note. Bytes 18–n are not presently used.

Table 97 lists the sense keys in the extended sense data format that are used by the disc drive.

Table 97: Applicable disc drive sense keys

<table>
<thead>
<tr>
<th>Sense Key</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>0h</td>
<td>No Sense: There is no specific sense key information to be reported for the disc drive. This would be the case for a successful command or when the ILI bit = 1.</td>
</tr>
<tr>
<td>1h</td>
<td>Recovered Error: The last command completed successfully with some recovery action performed by the disc drive. When multiple recovered errors occur, the last error that occurred is reported by the additional sense bytes. Note: For some Mode settings, the last command may have terminated before completing.</td>
</tr>
<tr>
<td>2h</td>
<td>Not Ready: Indicates the logical unit addressed cannot be accessed. Operator intervention may be required to correct this condition.</td>
</tr>
<tr>
<td>3h</td>
<td>Medium Error: Indicates the command terminated with a nonrecovered error condition, probably caused by a flaw in the medium or an error in the recorded data.</td>
</tr>
<tr>
<td>4h</td>
<td>Hardware Error: Indicates the disc drive detected a nonrecoverable hardware failure while performing the command or during a self-test. This includes controller failure, device failure, etc.</td>
</tr>
</tbody>
</table>
Additional sense, and additional sense qualifier codes are found in the SCSI Command manual Seagate Publication Number 100293068.

### 11.5.2 Current and deferred errors

Error code 70h (current error) indicates that the Check Condition or Command Terminated status returned is the result of an error or exception condition on the command that returned the Check Condition or Command Terminated. This includes errors generated during execution of the command by the actual execution process. It also includes errors not related to any command that are first observed during execution of a command. Examples of this latter type of error include disc servo-mechanism off-track errors and power-up test errors.

Error code 71h (deferred error) indicates that the Check Condition status returned is the result of an error or exception condition that occurred during execution of a previous command for which Good status has already been returned. Such commands are associated with use of the Immediate bit (start unit), with some forms of caching, and with multiple command buffering.

The deferred error is indicated by returning Check Condition status to the appropriate initiator as described below. The subsequent execution of a Request Sense command returns the deferred error sense information.

If Check Condition status for a deferred error is returned, the current command has not performed any storage operations or output operations to the media. After the target detects a deferred error condition on a logical unit, it returns a deferred error according to the rules described below:

1. If a deferred error can be recovered with no external system intervention, a deferred error indication is not posted unless required by the error handling parameters of the Mode Select command. The occurrence of the error may be logged if statistical or error logging is supported.

2. If a deferred error can be associated with a causing initiator and with a particular function or a particular subset of data, and the error is either unrecovered or required to be reported by the mode parameters, a deferred error indication is returned to the causing initiator. If an initiator other than the causing initiator
attempts access to the particular function or subset of data associated with the deferred error, a Busy status is returned to that initiator in response to the command attempting the access.

Note. Not all devices may be sufficiently sophisticated to identify the function or data that has failed. Those that cannot should treat the error in the following manner:

If a deferred error cannot be associated with a causing initiator or with a particular subset of data, a deferred error indication is returned on behalf of the failing logical unit to each initiator. If multiple deferred errors have accumulated for some initiators, only the last error is returned.

3. If a current command has not yet started executing, and a deferred error occurs, the command is terminated with Check Condition status and deferred error information posted in the sense data. If a deferred error occurs while a current command is executing and the current command has been affected by the error, the command is terminated by Check Condition status and current error information is returned in the sense data. In this case, if the current error information does not adequately define the deferred error condition, a deferred error may be returned after the current error information has been recovered. If a deferred error occurs while a current command is executing and the current command completes successfully, the target may choose to return the deferred error information after the completion of the current command.

Deferred errors may indicate that an operation was unsuccessful long after the command performing the data transfer returned Good status. If data that cannot be replicated or recovered from other sources is being stored using such buffered write operations, synchronization commands should be performed before the critical data is destroyed in the host initiator. This is necessary to be sure that recovery actions can be taken if deferred errors do occur in the storing of the data. The synchronizing process provides the necessary commands to allow returning Check Condition status and subsequent returning of deferred error sense information after all buffered operations are guaranteed to be complete.

11.6 Parameter rounding

Certain parameters sent to a target with various commands contain a range of values. Targets may choose to implement only selected values from this range. When the target receives a value that it does not support, it either rejects the command (Check Condition status with Illegal Request sense key) or it rounds the value received to a supported value. The target rejects unsupported values unless rounding is permitted in the description of the parameter.

To enabled rounding, set Mode Select command, page code 00h, byte 2, bit 2.

Rounding of parameter values, when permitted, is performed as described below.

A target that receives a parameter value that is not an exact supported value adjusts the value to one that it supports and returns Check Condition status with a sense key of Recovered Error. The additional sense code is set to Rounded Parameter. The initiator is responsible for issuing an appropriate command to learn what value the target has selected.

Implementor: Generally, the target should adjust maximum-value fields down to the next lower supported value than the one specified by the initiator. Minimum value fields should be rounded up to the next higher supported value that the one specified by the initiator. In some cases, the type of rounding (up or down) is explicitly specified in the description of the parameter.
12.0 Drive features

This section describes several features included for disc drives. Refer to the individual drive’s product manual to determine if your drive supports these features.

12.1 Self-Monitoring Analysis and Reporting Technology

Self-Monitoring Analysis and Reporting Technology (S.M.A.R.T.) is designed to recognize conditions that indicate imminent drive failure and provide sufficient warning to the host system of impending failure. The host system may use the information provided to trigger it to perform diagnostic, preventative, and/or protective functions (e.g., data backup).

The initiator sets up the parameters for S.M.A.R.T. operation using Mode Select Informational Exceptions Control page 1Ch. The drive reports information about S.M.A.R.T. operation using Request Sense Additional Sense Code 5D 00 and Mode Sense data page 1Ch. Refer to SCSI Command Reference Manual, 100293068, sections 1.11, 1.12, 1.13, and 1.14 for descriptions of the Mode Select and Mode Sense commands. Refer to SCSI Command Reference Manual, 100293068, Section 1.16 for details on the Informational Exceptions Control page. Refer to the individual drive’s product manual to determine if your particular drive supports S.M.A.R.T. and the extent of its implementation of the S.M.A.R.T. system.

12.2 Self-test operations

Default Self-test (DST) technology is part of a system to recognize drive fault conditions that qualify it for return to Seagate. If the drive fails the test, remove it from service.

12.2.1 Default self-test

The default self-test is mandatory for all device types that support the Send Diagnostics command. The response is simply a Good status if the test is successful or a Check Condition status if the test fails. Additional status is available in the Default Self-test Diagnostics Results page by using the Receive Diagnostic Results command (see SCSI Command Reference Manual, 100293068, Section 1.33).

12.2.2 Short and extended device self-tests

There are two optional types of device self-test that may be invoked using the Self-test Code field in the Send Diagnostics command: a short test and an extended test. The goal of the short device self-test is to quickly identify if the logical unit is faulty. A goal of the extended device self-test routine is to simplify factory testing during integration by having logical units perform more comprehensive testing without application client intervention. A second goal of the extended device self-test is to provide a more comprehensive test to validate the results of a short device self-test if its results are judged by the application client to be inconclusive.

The criteria for the short device self-test are that it has one or more segments and completes in two minutes or less. The criteria for the extended device self-test are that it has one or more segments and that the completion time is vendor-specific. Any tests performed in the segments are vendor-specific.
The following are examples of segments:

a. An electrical segment wherein the logical unit tests its own electronics. The tests in this segment are vendor specific, but some examples of tests that might be included are: a read/write circuitry test and/or a test of the read/write head elements;

b. A seek/servo segment wherein a device tests its capability to find and servo on data tracks; and

c. A read/verify scan segment wherein a device performs read scanning of some or all of the medium surface.

The tests performed in the segments may be the same for the short and extended device self-tests. The time required by a logical unit to complete its extended device self-test is reported in the Extended Self-test Completion Time field in the Control Mode page (see SCSI Command Reference Manual, 100293068, Section 1.13.8).

12.2.3 Device self-test modes

There are two modes for short and extended self-tests: a foreground mode and a background mode. These modes are described in the following sections.

12.2.3.1 Foreground mode

When a device server receives a Send Diagnostics command specifying a self-test to be performed in the foreground mode, the device server returns status for that command after the self-test has been completed. Not all Seagate drives support this mode.

While performing a device self-test in foreground mode, the device server responds to all commands except Inquiry, Report LUNs, and Request Sense with a Check Condition status, a sense key of Not Ready and an additional sense code of Logical Unit Not Ready, Self-test In Progress.

If a device server is performing a device self-test in the foreground mode and a test segment error occurs during the test, the device server updates the Device Self-test results log page (see SCSI Command Reference Manual, 100293068, Section 1.10.4) and reports Check Condition status with a sense key of Hardware Error and an additional sense code of Logical Unit Failed Self-test. The application client may obtain additional information about the failure by reading the Device Self-test Results log page. If the device server is unable to update the Self-test Results log page, it returns a Check Condition status with a sense key of Hardware Error and an additional sense code of Logical Unit Unable To Update Self-test Log.

An application client should reserve the logical unit before initiating a device self-test in the foreground mode. An application client may terminate a device self-test that is being performed in the foreground mode using an Abort Task, Abort Task Set, or Clear Task Set task management function. If a task manager receives an Abort Task, Abort Task Set, or Clear Task Set task management function while performing a device self-test in the foreground mode, it aborts the device self-test and updates the Device Self-test Results log page (see SCSI Command Reference Manual, 100293068, Section 1.10.4).

12.2.3.2 Background mode

When a device server receives a Send Diagnostics command specifying a device self-test to be performed in the background mode, the device server returns status for that command as soon as the command descriptor block has been validated.

After returning status for the Send Diagnostics command specifying a device self-test to be performed in the background mode, the device server shall initialize the Device Self-test Results log page (see SCSI Command Reference Manual, 100293068, Section 1.10.4) as follows. The self-test code from the Send Diagnostics command shall be placed in the Self-test Code field in the log page. The Self-test Results field shall be set to Fh. After the Self-test Results log page is initialized, the device server shall begin the first self-test segment.
While the device server is performing a self-test in the background mode, it shall terminate with a Check Condition status any Send Diagnostics command it receives that meets one of the following criteria:

a. The Self-test bit is one; or

b. The Self-test Code field contains a value other than 000b or 100b.

When terminating the Send Diagnostics command, the sense key is set to Not Ready and the additional sense code is set to Logical Unit Not Ready, Self-test In Progress.

While performing a device self-test in the background mode, the device server suspends the device self-test to service any other commands received with the exceptions listed below.

**Exception commands for background self-tests**

- Send Diagnostics (with Self-test Code field set to 100b)
- Write Buffer (with the mode set to any download microcode option)
- Format Unit
- Start/Stop Unit (stop only)

Suspension of the device self-test to service the command occurs as soon as possible, but should never take longer than two seconds.

If one of the exception commands listed above is received, the device server shall abort the self-test, update the self-test log, and service the command as soon as possible but not longer than two seconds after the command descriptor block has been validated.

**Note.** An application client may terminate a self-test that is being performed in the background mode by issuing a Send Diagnostics command with the Self-test Code field set to 100b (Abort background self-test function).

12.2.3.3 Elements common to foreground and background self-test modes

The Progress Indication field returned in response to a Request Sense command (see SCSI Command Reference Manual, 100293068, Section 1.38) may be used by the application client at any time during execution of a device self-test to poll the logical unit's progress. While executing a self-test (unless an error has occurred), the device server responds to a Request Sense command by returning a sense key of Not Ready and an additional sense code of Logical Unit Not Ready, Self-test In Progress with the sense key specific bytes set for progress indication.

The application client may obtain information about the twenty most recently completed device self-tests by reading the Device Self-test Results log page (see SCSI Command Reference Manual, 100293068, Section 1.10.4). This is the only method an application client can use to obtain information about self-tests performed in the background mode.

Table 98 summarizes when a logical unit returns status after receipt of a self-test command, how an application client may abort a device self-test, how a logical unit handles new commands that are received while a device self-test is in progress, and how a logical unit reports a device self-test failure.
<table>
<thead>
<tr>
<th>Mode</th>
<th>When status is returned</th>
<th>How to abort the Device Self-test</th>
<th>Processing of subsequent commands while Device Self-test is executing</th>
<th>Device Self-test failure reporting</th>
</tr>
</thead>
<tbody>
<tr>
<td>Foreground</td>
<td>After the self-test is complete</td>
<td>Abort Task task management function</td>
<td>If the command is Inquiry, Report LUNs, or Request Sense, process normally. Otherwise, terminate with Check Condition status, Not Ready sense key, and Logical Unit Not Ready, Self-test In Progress additional sense code.</td>
<td>Terminate with Check Condition status, Hardware Error sense key, and Logical Unit Failed Self-test or Logical Unit Unable To Update Self-test Log additional sense code.</td>
</tr>
<tr>
<td>Background</td>
<td>After the CDB is validated</td>
<td>Send Diagnostics command with Self-test Code field set to 100b</td>
<td>Process the command, except as described in 12.2.3.2.</td>
<td>Application client checks Device Self-test results log page (see SCSI Command Reference Manual, 100293068, Section 1.10.4) after the Progress Indication field returned from Request Sense indicates the self-test is complete.</td>
</tr>
</tbody>
</table>
13.0 Seagate Technology support services

Online services

Internet

www.seagate.com for information about Seagate products and services. Worldwide support is available 24 hours daily by e-mail for your questions.

Presales Support: www.seagate.com/support/email/email_presales.html or Presales@Seagate.com
Technical Support: www.seagate.com/support/email/email_disc_support.html or DiscSupport@Seagate.com

mySeagate

my.seagate.com is the industry’s first Web portal designed specifically for OEMs and distributors. It provides self-service access to critical applications, personalized content and the tools that allow our partners to manage their Seagate account functions. Submit pricing requests, orders and returns through a single, password-protected Web interface—anytime, anywhere in the world.

spp.seagate.com

spp.seagate.com supports Seagate resellers with product information, program benefits and sales tools. You may register for customized communications that are not available on the web. These communications contain product launch, EOL, pricing, promotions and other channel-related information. To learn more about the benefits or to register, go to spp.seagate.com, any time, from anywhere in the world.

Seagate Service Centers

Presales Support

Our Presales Support staff can help you determine which Seagate products are best suited for your specific application or computer system, as well as drive availability and compatibility.

Technical Support

If you need help installing your drive, consult your system’s documentation or contact the dealer’s support services department for assistance specific to your system. Seagate technical support is also available to assist you online at support.seagate.com or through one of our call centers. Have your system configuration information and your drive’s “ST” model number available.

SeaTDD™ (+1-405-324-3655) is a telecommunications device for the deaf (TDD). You can send questions or comments 24 hours daily and exchange messages with a technical support specialist during normal business hours for the call center in your region.
Customer Service Operations

Warranty Service
Seagate offers worldwide customer support for Seagate drives. Seagate distributors, OEMs and other direct customers should contact their Seagate Customer Service Operations (CSO) representative for warranty-related issues. Resellers or end users of drive products should contact their place of purchase or one of the Seagate CSO warranty centers for assistance. Have your drive’s “ST” model number and serial number available.

Data Recovery Services
Seagate offers data recovery services for all formats and all brands of storage media. Our Data Recovery Services labs are currently located in North America. To get a free quick quote or speak with a case management representative, call 1-800-475-0143. Additional information, including an online request form and data loss prevention resources, is available at www.datarecovery.seagate.com.

Authorized Service Centers
In some locations outside the US, you can contact an Authorized Service Center for service.

USA/Canada/Latin America support services

<table>
<thead>
<tr>
<th>Seagate Service Centers</th>
</tr>
</thead>
<tbody>
<tr>
<td>Presales Support</td>
</tr>
<tr>
<td><strong>Call center</strong></td>
</tr>
<tr>
<td>Americas</td>
</tr>
<tr>
<td><strong>Technical Support</strong></td>
</tr>
<tr>
<td><strong>Call center</strong></td>
</tr>
<tr>
<td>Americas</td>
</tr>
</tbody>
</table>

Customer Service Operations

<table>
<thead>
<tr>
<th>Warranty Service</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Call center</strong></td>
</tr>
<tr>
<td>USA, Canada, Mexico and Latin America</td>
</tr>
<tr>
<td>Brazil</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Data Recovery Services</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Call center</strong></td>
</tr>
<tr>
<td>USA, Canada, and Mexico</td>
</tr>
</tbody>
</table>

¹Hours of operation are 8:00 A.M. to 6:00 P.M., Monday through Friday (Central time).
²Hours of operation are 8:00 A.M. to 8:00 P.M., Monday through Friday (Central time).
³Hours of operation are 8:00 A.M. to 5:00 P.M., Monday through Friday (Central time).
⁴Authorized Service Center
⁵Hours of operation are 8:00 A.M. to 8:00 P.M., Monday through Friday, and 9:00 A.M. to 5:00 P.M., Saturday (Eastern time).
European support services

For presales and technical support in Europe, dial the Seagate Service Center toll-free number for your specific location. If your location is not listed here, dial our presales and technical support call center at +1-405-324-4714 from 8:00 A.M. to 11:45 A.M. and 1:00 P.M. to 5:00 P.M. (Central Europe time) Monday through Friday. The presales and technical support call center is located in Oklahoma City, USA.

For European warranty service, dial the toll-free number for your specific location. If your location is not listed here, dial our European CSO warranty center at +31-20-316-7222 from 8:30 A.M. to 5:00 P.M. (Central Europe time) Monday through Friday. The CSO warranty center is located in Amsterdam, The Netherlands.

Seagate Service Centers

Toll-free support numbers

<table>
<thead>
<tr>
<th>Call center</th>
<th>Presales and Technical Support</th>
<th>Warranty Service</th>
</tr>
</thead>
<tbody>
<tr>
<td>Austria</td>
<td>—</td>
<td>00 800-47324289</td>
</tr>
<tr>
<td>Belgium</td>
<td>00 800-47324283 (00 800-4SEAGATE)</td>
<td>00 800-47324289</td>
</tr>
<tr>
<td>Denmark</td>
<td>00 800-47324283</td>
<td>00 800-47324289</td>
</tr>
<tr>
<td>France</td>
<td>00 800-47324283</td>
<td>00 800-47324289</td>
</tr>
<tr>
<td>Germany</td>
<td>00 800-47324283</td>
<td>00 800-47324289</td>
</tr>
<tr>
<td>Ireland</td>
<td>00 800-47324283</td>
<td>00 800-47324289</td>
</tr>
<tr>
<td>Italy</td>
<td>00 800-47324283</td>
<td>00 800-47324289</td>
</tr>
<tr>
<td>Netherlands</td>
<td>00 800-47324283</td>
<td>00 800-47324289</td>
</tr>
<tr>
<td>Norway</td>
<td>00 800-47324283</td>
<td>00 800-47324289</td>
</tr>
<tr>
<td>Poland</td>
<td>00 800-311 12 38</td>
<td>00 800-311 12 38</td>
</tr>
<tr>
<td>Spain</td>
<td>00 800-47324283</td>
<td>00 800-47324289</td>
</tr>
<tr>
<td>Sweden</td>
<td>00 800-47324283</td>
<td>00 800-47324289</td>
</tr>
<tr>
<td>Switzerland</td>
<td>00 800-47324283</td>
<td>00 800-47324289</td>
</tr>
<tr>
<td>Turkey</td>
<td>00 800-31 92 91 40</td>
<td>00 800-31 92 91 40</td>
</tr>
<tr>
<td>United Kingdom</td>
<td>00 800-47324283</td>
<td>00 800-47324289</td>
</tr>
</tbody>
</table>

FAX services—All Europe (toll call)

<table>
<thead>
<tr>
<th>Service Type</th>
<th>Number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Technical Support</td>
<td>+1-405-324-3339</td>
</tr>
<tr>
<td>Warranty Service</td>
<td>+31-20-653-3513</td>
</tr>
</tbody>
</table>

Africa/Middle East support services

For presales and technical support in Africa and the Middle East, dial our presales and technical support call center at +1-405-324-4714 from 8:00 A.M. to 11:45 A.M. and 1:00 P.M. to 5:00 P.M. (Central Europe time) Monday through Friday. The presales and technical support call center is located in Oklahoma City, USA.

For warranty service in Africa and the Middle East, dial our European CSO warranty center at +31-20-316-7222 from 8:30 A.M. to 5:00 P.M. (Central Europe time) Monday through Friday, or send a FAX to +31-20-653-3513. The CSO warranty center is located in Amsterdam, The Netherlands.
Asia/Pacific support services

For Asia/Pacific presales and technical support, dial the toll-free number for your specific location. The Asia/Pacific toll-free numbers are available from 6:00 A.M. to 10:45 A.M. and 12:00 P.M. to 6:00 P.M. (Australian Eastern time) Monday through Friday, except as noted. If your location is not listed here, direct dial one of our technical support locations.

Warranty service is available from 9:00 A.M. to 6:00 P.M. April through October, and 10:00 A.M. to 7:00 P.M. November through March (Australian Eastern time) Monday through Friday.

Seagate Service Centers

<table>
<thead>
<tr>
<th>Call center</th>
<th>Toll-free</th>
<th>Direct dial</th>
<th>FAX</th>
</tr>
</thead>
<tbody>
<tr>
<td>Australia</td>
<td>1800-14-7201</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td>China (Mandarin)</td>
<td>800-810-9668</td>
<td>+66-10-6225-5336</td>
<td>—</td>
</tr>
<tr>
<td>Hong Kong</td>
<td>800-90-0474</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td>Hong Kong (Cantonese)</td>
<td>001-800-0830-1730</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td>India</td>
<td>1-800-180-1104</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td>Indonesia</td>
<td>001-803-1-003-2165</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td>Japan</td>
<td>0034 800 400 554</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td>Korea</td>
<td>007 98 8521 7635</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td>Malaysia</td>
<td>1-800-80-2335</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td>New Zealand</td>
<td>0800-443988</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td>Singapore</td>
<td>800-1101-150</td>
<td>+65-6485-3595</td>
<td>+65-6485-4860</td>
</tr>
<tr>
<td>Taiwan (Mandarin)</td>
<td>00-800-0830-1730</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td>Thailand</td>
<td>001-800-11-0032165</td>
<td>—</td>
<td>—</td>
</tr>
</tbody>
</table>

Customer Service Operations

Warranty Service

<table>
<thead>
<tr>
<th>Call center</th>
<th>Toll-free</th>
<th>Direct dial</th>
<th>FAX</th>
</tr>
</thead>
<tbody>
<tr>
<td>Asia/Pacific</td>
<td>—</td>
<td>+65-6485-3595</td>
<td>+65-6485-4860</td>
</tr>
<tr>
<td>Australia</td>
<td>1800-12-9277</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td>India</td>
<td>—</td>
<td>+91-44-821-6164</td>
<td>+91-44-827-2461</td>
</tr>
</tbody>
</table>

---

1Hours of operation are 8:30 A.M. to 5:30 P.M., Monday through Friday (Australian Western time).
2Hours of operation are 9:00 A.M. to 6:00 P.M., Monday through Saturday.
3Hours of operation are 9:30 A.M. to 6:30 P.M., Monday through Friday.
4Authorized Service Center
Publication feedback survey

We are interested in your comments and suggestions regarding this publication. Please take a few minutes to participate in our survey at the following URL:

http://survey.seagate.com/survey/techpubs.nsf

Thank you for your time and comments.
Appendix A. Glossary

This section contains a glossary of terms used in this publication.

**ABTS**
Abort Sequence.

**ABTX**
Abort Exchange.

**ACC**
Accept.

**ACK**
Acknowledgment.

**Active**
The state of Sequence Initiator until all the Data frames for the Sequence have been transmitted. The state of Sequence Recipient until all the Data frames for the Sequence have been received. The period of time during which frames of a Sequence (or an Exchange) are actively being transmitted or received.

**Active Virtual Circuit Credit limit**
The maximum number of VC Credits available for a Virtual Circuit in the Active state. It represents the maximum number of VC Credits held by an N_Port on a given Virtual Circuit when the Circuit is in the active state.

**Address identifier**
an address value used to identify source (S_ID) or destination (D_ID) of a frame.

**ADVC**
Advise Credit.

**Alias**
Alias is a group address recognized by an N_Port if the N_Port has registered as a member of the group, with the Alias Server.

**Alias address identifier (alias)**
one or more address identifiers which may be recognized by an N_Port in addition to its N_Port Identifier. An alias address identifier is Fabric unique and may be common to multiple N_Ports.

**Alias Token**
a 12-byte field to indicate the type of Alias (such as Multicast, Hunt Group) and certain properties associated with the Alias (such as FC-PH TYPE, Node Name for the Common Controlling Entity).

**Application client**
an object that is the source of SCSI commands.

**Application client buffer offset**
offset in bytes from the start or base address of the application client’s data buffer to the location for the transfer of the first byte of a data delivery service request.

**Arbitrated loop topology**
a topology where L_Ports use arbitration to establish a point-to-point circuit. A configuration that allows multiple ports to be connected serially.

**Attenuation**
The transmission medium power loss expressed in units of dB.

**Available BB_Credit**
Also called “Available buffer-to-buffer credit.” A transmitter uses this variable to determine permission to transmit frames, and if so, the allowable number of frames to transmit. The transmitter may transmit a frame when the Available BB_Credit value is greater than 0. The rules for modifying Available BB_Credit are:

(a) in an OPN Initiator, Available BB_Credit may be initialized to a value less than or equal to the Login_BB_Credit of the OPN Recipient upon transmission of any OPN;
(b) in an OPN Recipient (acting as a Sequence Initiator), Available BB_Credit may be initialized to a value less than or equal to Login_BB_Credit of the OPN Initiator (acting as a Sequence Recipient) upon receipt of a full duplex OPN;
(c) Available BB_Credit is decremented upon transmission of a frame;
(d) Available BB_Credit is incremented upon receipt of R_RDY, except that following an OPN to or from a Sequence Recipient with Login_BB_Credit>0, one R_RDY is discarded for each frame sent until the number of R_RDYs discarded equals the Login_BB_Credit value.

Available_receive_buffers
In class 3, the number of buffers in a receiving port which are available for receiving frames at link rate. Equal to the largest number of R_RDYs an NL_port can issue immediately upon transmission or receipt of an OPN. BB Flow control rules keep this number greater than or equal to the Available BB_Credit variable in a transmitting port. The number of available receive buffers must be greater than or equal to Login_BB_Credit upon receipt of any OPN.

Bandwidth
Maximum effective transfer rate for a given set of physical variants such as communication model, Payload size, Fibre speed, and overhead specified by FC-PH.

Base address
The address of the lowest address byte to be transferred to or from an application client buffer.

Baud
The encoded bit rate per second.

BB Credit_CNT
Buffer-to-buffer Credit_Count.

BB_buffer
The buffer associated with buffer-to-buffer flow control.

BB_Credit
Buffer-to-buffer Credit.

Beginning Running Disparity
The Running Disparity present at a transmitter when Encoding of the Special Code associated with an Ordered Set is initiated, or at a receiver when Decoding of the Special Character associated with an Ordered Set is initiated.

BER
See Bit error rate.

Bit error rate (BER)
The statistical probability of a transmitted bit being erroneously received in a communication system. The BER is measured by counting the number of erroneous bits at the output of a receiver and dividing by the total number of bits.

Bit synchronization
The state in which a receiver is delivering retimed serial data at the required BER.

Block
A upper level construct of application data related to a single Information Category and transferred within a single Sequence.
**BNC**
Acronym for a Bayonet-Neil-Councilman Coaxial Cable Connector. Specifications for BNC style connectors are defined in EIA/TIA 403-A and MIL-C-39012.

**BSY**
Busy.

**Buffer**
A logical construct which holds the contents of a single frame.

**Byte**
An eight-bit entity with its least significant bit denoted as bit 0 and most significant bit as bit 7. The most significant bit is shown on the left side in FC-PH, unless specifically indicated otherwise. Bytes are packed four per 32-bit word, or eight per 64-bit word.

**Cable plant**
All passive communications elements (e.g., optical fibre, twisted pair, or coaxial cable, connectors, splices, etc.) between a transmitter and a receiver.

**CATV**
Central Antenna Television.

**CCITT**
Comité Consultatif International, Télégraphique et Téléphonique (see ITV-TS).

**CDB**
Command descriptor block.

**Center wavelength (LED)**
The average of the two wavelengths measured at the half amplitude points of the power spectrum.

**Centre wavelength (laser)**
The nominal value of the central wavelength of the operating, modulated laser. This is the wavelength where the effective optical power resides.

**Character**
Any Transmission Character associated by FC-1 transmission code with a FC-2 data byte or special code. Transmission characters are generated and interpreted only by FC-1.

**Circuit**
A bidirectional path within the Fabric.

**Class 1 service**
A service which establishes a dedicated connection between communicating N_Ports.

**Class 2 service**
A service which multiplexes frames at frame boundaries to or from one or more N_Ports with acknowledgement provided.

**Class 3 service**
A service which multiplexes frames at frame boundaries to or from one or more N_Ports without acknowledgement.

**Class 4 bidirectional circuits**
A pair of unidirectional virtual circuits between two communicating N_Ports.

**Class 4 Circuit Initiator**
The N_Port which initiates the setup of a Class 4 circuit.
Class 4 Circuit Recipient
The N_Port which accepts a Class 4 circuit with the Originator N_Port.

Class 4 end-to-end credit limit
The maximum amount of end-to-end credit available for a virtual circuit. It represents the maximum number of Class 4 end-to-end credit held by an N_Port on a given virtual circuit.

Class 4 service
A service that establishes virtual circuits to provide fractional bandwidth service between communicating N_Ports. The service multiplexes frames at frame boundaries to or from one or more N_Ports with acknowledgment provided.

Class l/SOFcl
Class 1 frame with a SOFcl delimiter.

Classes of service
Different types of services provided by the Fabric and used by the communicating N_Ports.

Code balance
The numerical sum of the 1 bits in any 10 bits in the transmitted bit stream divided by 10 (e.g., 1110100011 has a code balance of 6/10 = 60%).

Code bit
The smallest time period used by FC-0 for transmission on the media.

Code violation
An error condition that occurs when a received transmission character cannot be decoded to a valid data byte or special code using the validity checking rules specified by the transmission code.

Comma
The seven bit sequence 0011111 or 1100000 in an encoded stream.

Comma character
A special character containing a comma.

Command
A request describing a unit of work to be performed by a device server.

Command byte count
Upper limit on the extent of the data to be transferred by the SCSI command.

Command descriptor block (CDB)
A structure up to 16 bytes in length used to communicate a command from an application client to a device server.

Concatenation
A logical operation that “joins together” strings of data. This operation is represented with the symbol “||”. Two or more fields are concatenated to provide a reference of uniqueness (e.g., S_ID||X_ID).

Connection
See Dedicated Connection.

Connection initiator
The source N_Port which initiates a Class 1 connection with a destination N_Port through a connection request and also receives a valid response from the destination N_Port to complete the connection establishment.

Connection recipient
The destination N_Port which receives a Class 1 connect-request from the connection Initiator and accepts establishment of the connection by transmitting a valid response.
Connection-oriented frames
Frames sent in either a Class 1 dedicated connection or a Class 4 circuit.

Connectionless buffers
Receive buffers participating in connectionless service and capable of receiving connectionless frames.

Connectionless frames
Frames participating in connectionless service (i.e., Class 1 frames with SOFc1, Class 2, and Class 3 frames referred to individually or collectively).

Connectionless service
Communication between two N_Ports performed without a dedicated connection.

Continuously increasing relative offset
The relationship specified between relative offset values contained in frame (n) and frame (n+1) of an information category within a single Sequence.

COR
Camp-On Request.

Credit
The maximum number of receive buffers allocated to a transmitting N_Port or F_Port. It represents the maximum number of outstanding frames which can be transmitted by that N_Port or F_Port without causing a buffer overrun condition at the receiver.

Credit_CNT
Credit Count.

CTI
Circuit Initiator.

CTR
Circuit Recipient.

Current running disparity
The running disparity present at a transmitter when Encoding of a valid data byte or special code is initiated, or at a receiver when decoding of a transmission character is initiated.

D_ID
Destination_Identifier.

Data character
Any transmission character associated by the transmission code with a valid data byte.

Data frame
A frame containing information meant for FC-4/ULP or the link application.

Data out delivery service
A confirmed service used by the device server to request the transfer of data from the application client.

dB
Decibel.

dBm
Decibel (relative to 1 mw power).
Decoding
Validity checking of received transmission characters and generation of valid data bytes and special codes from those characters.

Dedicated connection
A communicating circuit guaranteed and retained by the Fabric for two given N_Ports.

Dedicated duplex
A synonym for Class 1 dedicated connection.

Dedicated simplex
A unidirectional Class 1 connection with ACKs transmitted in Class 2.

Delimiter
An ordered set used to indicate a frame boundary.

Destination N_Port
The N_Port to which a frame is targeted.

Destination_Identifier (D_ID)
The address identifier used to indicate the targeted destination of the transmitted frame.

Device server
An object within the logical unit which executes SCSI tasks and enforces the rules for task management.

DF_CTL
Data Field Control.

Discard policy
An error handling policy where an N_Port is able to discard data frames received following detection of a missing frame in a sequence.

Disparity
The difference between the number of ones and zeros in a transmission character.

Dispersion
A term used to denote pulse broadening and distortion. The two general categories of dispersion are modal dispersion, due to the difference in the propagation velocity of the propagation modes in a multimode fibre, and chromatic dispersion, due to the difference in propagation of the various spectral components of the optical source.

DJ
Deterministic jitter.

DUT
Device under test.

E_D_TOV
Error_Detect_Timeout value.

ECL
Emitter Coupled Logic.

EE_buffer
The buffer associated with end-to-end flow control.

EE_Credit
End-to-End Credit.
EE_Credit_CNT
End-to-End Credit Count.

EIA
Electronic Industries Association.

**Electrical fall time**
The time interval for the falling edge of an electrical pulse to transition from its 90% amplitude level to its 10% amplitude level.

**Electrical rise time**
The time interval for the rising edge of an electrical pulse to transition from its 10% amplitude level to its 90% amplitude level.

EMC
Electromagnetic compatibility.

**Encoding**
Generation of transmission characters from valid data bytes and special codes.

EOF
End of frame.

ESB
Exchange Status Block.

ESTC
Estimate Credit.

ESTS
Establish Streaming.

Exchange
The basic mechanism which transfers information consisting of one or more related non-concurrent sequences which may flow in the same or opposite directions. An exchange may span multiple Class 1 dedicated connections. The exchange is identified by an Originator Exchange_Identifier (OX_ID) and a Responder Exchange_Identifier (RX_ID).

Exchange Status Block
A logical construct which contains the state of an exchange. An originator N_Port has an Originator Exchange Status Block and the responder N_Port has a Responder Exchange Status Block for each concurrently active Exchange.

Exchange_Identifier (X_ID)
A generic reference to OX_ID and RX_ID (see Exchange).

**Exclusive connection**
A Class 1 dedicated connection without Intermix (see dedicated connection).

Execute command service
A peer-to-peer, confirmed service requested by the application client to perform a SCSI command.

**Extinction ratio**
The ratio (in dB) of the average optical energy in a logic one level to the average optical energy in a logic zero level measured under modulated conditions at the specified baud rate.

Eye opening
The time interval across the eye, measured at the 50% normalized eye amplitude which is error free to the specified BER.
**F_BSY**
Fabric_Port_Busy.

**F_BSY(DF)**
F_BSY response to a data frame.

**F_BSY(LC)**
F_BSY response to any link control except P-BSY.

**F_CTL**
Frame_Control.

**F_Port**
The Link_Control_Facility within the fabric which attaches to an N_Port through a link. An F_Port is addressable by the N_Port attached to it, with a common well known address identifier (hex “FFFFFE”) (see local F_Port, and remote F_Port).

**F_Port**
Fabric_Port.

**F_Port Name**
A Name_Identifier associated with an F_Port.

**F_RJT**
Fabric_Port_Reject.

**Fabric**
The entity which interconnects various N_Ports attached to it and is capable of routing frames by using only the D_ID information in a FC-2 frame header.

**Fabric_Name**
A Name_Identifier associated with a fabric.

**FACT**
Fabric Activate Alias.

**FC**
Fibre Channel

**FC-4 Region**
A set of N_Ports connected either point-to-point or to a common fabric, such that any N_Port in the set can successfully complete the N_Port Login procedure with all other N_Ports in the set and successfully maintain an exchange for a particular FC-4.

**FC-PH**

**FC-4**
Fibre Channel Layer 4 mapping layer.

**FCP**
Fibre Channel Protocol.

**FCP I/O operation**
An unlinked SCSI command, a series of linked SCSI commands, or a task management function.

**FCP_Port**
An N_Port or NL_Port that supports the SCSI Fibre Channel Protocol.
FCPH
The architecture specified by the Fibre Channel standard.

FCS
Frame Check Sequence.

FCSI
Fibre Channel Systems Initiative.

FDACT
Fabric Deactivate Alias.

Fiber optic test procedure (FOTP)
Standards developed and published by the Electronic Industries Association (EIA) under the EIA-RS-455 series of standards.

Fibre
A general term used to cover all transmission media specified in FC-PH.

Fibre Channel Name
A Name_Identifier which is Fibre Channel unique.

Fibre optic cable
A jacketed optical fibre or fibres.

FL_Port
An F_Port that contains Arbitrated Loop functions associated with Arbitrated Loop topology.

FOTP
Fiber optic test procedure.

FQXID
Fully qualified exchange identifier.

Fractional bandwidth
A portion of the total bandwidth available on a path.

Frame
An indivisible unit of information used by FC-2.

Frame Content
The information contained in a frame between its Start-of-Frame and End-of-Frame delimiters, excluding the delimiters.

FRU
Field Replaceable Unit.

FT-1
Frame type 1.

FT-0
Frame type 0.

Fully qualified exchange identifier
A token used to uniquely identify a FCP I/O Operation.

FWHM
Full Width Half Max.
GAID
Get Alias_ID.

Hard address
The AL_PA which an NL_Port attempts to acquire in the LIHA Loop Initialization Sequence.

Hex
Hexadecimal notation.

HG
Hunt Group.

Hunt Group
A set of N_Ports with a common alias address identifier managed by a single node or common controlling entity. However, FC-PH does not presently specify how a Hunt Group can be configured.

Hz
Hertz = 1 cycle per second.

ID
Identifier.

Idle
See "Idle Word."

Idle Word (Idle)
An ordered set of four transmission characters which are normally transmitted between frames. The Idle Word is also referred to as an Idle.

IEEE
Institute of Electrical and Electronics Engineers.

Ignored
A field that is not interpreted by the receiver.

Infinite buffer
A terminology to indicate that at FC-2 level, the amount of buffer available at the Sequence Recipient is unlimited. The ULP chooses the amount of buffer per Sequence based on its MTU (maximum transfer unit).

Information Category
A frame header field indicating the category to which the frame payload belongs (e.g., Solicited Data, Unsolicited Data, Solicited Control, and Unsolicited Control).

Information transfer
Transfer of frames whose payload has meaning to the cooperating FC-4s.

Information Unit
An organized collection of data specified by FC-4 to be transferred as a single sequence by FC-2.

Initial Relative Offset
A relative offset value specified at the sending end by an upper level for a given block or subblock and used by the sending FC-2 in the first frame of that block or subblock (see subblock, block, and Relative Offset). Initial Relative Offset value may be zero or non-zero.
Initialization
For FC-1 level the period beginning with power on and continuing until the transmitter and receiver of that level become operational.

Initiator
An SCSI device containing application clients that originate device service requests and task management functions to be processed by a target SCSI device.

Initiator identifier
Token by which a target identifies the initiator device.

Interface connector
An optical or electrical connector which connects the media to the Fibre Channel transmitter or receiver. The connector consists of a receptacle and a plug.

Intermix
A service which interleaves Class 2 and Class 3 frames on an established Class 1 connection.

Intersymbol interference
The effect on a sequence of symbols in which the symbols are distorted by transmission through a limited bandwidth medium to the extent that adjacent symbols begin to interfere with each other.

IP
Internet Protocol.

IPA
Initial process associator.

ITV-TS
The International Union–Telecommunication Standardization (formerly CCITT).

IU
Information Unit.

Jitter
Deviations from the ideal timing of an event which occur at high frequencies. Low frequency deviations are tracked by the clock recovery and do not directly affect the timing allocations within a bit cell. Jitter is not tracked by the clock recovery and directly affects the timing allocations in a bit cell. For FC-PH the lower cutoff frequency for jitter is defined as the bit rate divided by 2,500. Jitter is customarily subdivided into deterministic and random components.

Jitter, deterministic (DJ)
Timing distortions caused by normal circuit effects in the transmission system. Deterministic jitter is often subdivided into duty cycle distortion (DCD) caused by propagation differences between the two transitions of a signal and data dependent jitter (DDJ) caused by the interaction of the limited bandwidth of the transmission system components and the symbol sequence.

Jitter, random (RJ)
Jitter due to thermal noise which may be modeled as a Gaussian process. The peak-to-peak value of RJ is of a probabilistic nature and thus any specified value yields an associated BER.

JNA
Join Alias Group.

L_Port
An N_Port or F_Port that contains Arbitrated Loop functions associated with Arbitrated Loop topology.

LA RJT
Link Application Reject.
LAN
Local Area Network.

laser chirp
A phenomenon in lasers where the wavelength of the emitted light changes during modulation.

LCF
Link Control Facility.

LCR
Link Credit Reset.

LED
light emitting diode.

level
1. A document artifice used to group related architectural functions. No specific correspondence is intended between levels and actual implementations.
2. a specific value of voltage (e.g., voltage level).

link
1. Two unidirectional fibres transmitting in opposite directions and their associated transmitters and receivers.
2. The full-duplex FC-0 level association between FC-1 entities in directly attached Ports (see Port).

Link_Control_Facility
A link hardware facility which attaches to an end of a link and manages transmission and reception of data. It is contained within each N_Port and F_Port.

LLC
Logical Link Control.

Local F_Port
The F_Port to which an N_Port is directly attached by a link (see remote F_Port).

LOGI
Log in.

Logical unit
A target resident entity that implements a device model and executes SCSI commands sent by an application client.

Logical unit identifier
Identifier used by an initiator to reference the logical unit.

Login_BB_Credit
On FC-AL, equal to the number of receive buffers that a receiving NL_port must have available when a loop circuit is established. Login_BB_Credit is discovered in the PDISC or PLOGI protocol.

LOGO
Log out.

LOL
Loss of light.

Loop Tenancy
The period of time beginning when a port wins arbitration and ending when it receives a CLS in response to its own CLS, or forwards a CLS transmitted to it.
Loop_ID
7-bit values numbered contiguously from 0 to 126 decimal and representing the 127 legal hard addresses on a loop (not all of the 256 possible AL_PAs are used in FC-AL for reasons related to running disparity). Loop_IDs correspond to the 7-bit SEL word in SFF-8045 used for specifying hard addresses. Decimal 127 (7F hex) is not a valid Loop_ID, but is used to signify that no hard address is being assigned to an NL_Port.

Loopback
A mode of FC-1 operation in which the information passed to the FC-1 transmitter for transmission is shunted directly to the FC-1 receiver, overriding any signal detected by the receiver on its attached fibre.

LR
Link Reset primitive sequence.

LRR
Link Reset Response primitive sequence.

LS_ACC
Link Service Accept.

LW
Long wavelength.

m
Meter.

MAC
Media Access Control.

Mandatory
A function which is required to be supported by a compliant implementation of FC-PH.

MAS
Master of link.

Mb
Mega bit.

MB
Mega byte.

MBd
Mega baud.

Meaningful
A control field or bit shall be applicable and shall be interpreted by the receiver, wherever it is specified as meaningful. Wherever it is specified as “not meaningful,” it shall be ignored (see valid).

MM
Multimode.

Mode-partition noise
Noise in a laser based optical communication system caused by the changing distribution of laser energy partitioning itself among the laser modes (or lines) on successive pulses in the data stream. The effect is a different center wavelength for the successive pulses resulting in arrival time jitter attributable to chromatic dispersion in the fibre.

ms
Millisecond.
ms
Microsecond.

N_Port
A hardware entity which includes a Link_Control_Facility. It may act as an originator, a responder, or both.

N_Port
Node_Port.

N_Port Identifier
A fabric-unique address identifier by which an N_Port is uniquely known. The identifier may be assigned by the fabric during the initialization procedure. The identifier may also be assigned by other procedures not defined in FC-PH. The identifier is used in the S_ID and D_ID fields of a frame.

N_Port Name
A Name_Identifier associated with an N_Port.

NA
Not applicable.

NAA
Network Address Authority.

NACT
N_Port Activate Alias.

Name_Identifier
A 64-bit identifier, with a 60-bit value preceded with a 4-bit Network Address Authority Identifier, used to identify entities in Fibre Channel such as N_Port, Node, F_Port, or Fabric.

NDACT
N_Port Deactivate Alias.

Network_Address_Authority (NAA)
An organization which administers network addresses.

Network_Address_Authority (NAA) identifier
A four-bit identifier defined in FC-PH to indicate a Network_Address_Authority (NAA).

NL_Port
An N_Port that contains arbitrated loop functions associated with the Fibre Channel Arbitrated Loop topology.

Node
A collection of one or more N_Ports or NL_Ports controlled by a level above FC-2.

Node_Name
A Name_Identifier associated with a node.

Non-repeating ordered set
An ordered set which, when issued by FC-2 to FC-1 for transmission, is to be transmitted once.

NOP
No operation.

NOS
Not Operational primitive sequence.
**Not Operational**
A receiver or transmitter that is not capable of receiving or transmitting an encoded bit stream respectively, based on the rules defined by FC-PH for error control. For example, FC-1 is Not Operational during Initialization.

**ns**
Nanosecond.

**NTP**
Network Time Protocol.

**OESB**
Originator Exchange Status Block.

**OFC**
Open fibre control.

**Offset**
Relative Offset.

**OFSTP**
Optical fiber system test practice.

**OLS**
Online primitive sequence.

**Open**
The period of time starting when a sequence (an exchange) is initiated until that sequence (the exchange) is normally or abnormally terminated.

**Open fibre control (OFC)**
A safety interlock system that controls the optical power level on an open optical fibre cable.

**Operation**
A construct which may be used by a level above FC-2 and is associated with one or more exchanges.

**Operation_Associator**
A value used in the Association_Header to identify a specific operation within a Node and correlate communicating processes related to that operation. Operation_Associator is the mechanism by which an operation within a given Node is referred to by another communicating Node. Operation_Associator is a generic reference to Originator Operation_Associator and Responder Operation_Associator (see Process_Associator).

**Operational**
The state of a receiver or transmitter that is capable of receiving or transmitting an encoded bit stream, respectively, based on the rules defined by FC-PH for error control. Those receivers capable of accepting signals from transmitters requiring laser safety procedures are not considered operational after power on until a signal of a duration longer than that associated with laser safety procedures is present at the fibre attached to the receiver.

**OPN Initiator**
The port on an Arbitrated Loop that sent the OPN primitive.

**Optical fibre**
Any filament or fibre, made of dielectric material, that guides light.

**Optional**
Characteristics that are not required by FC-PH. However, if any optional characteristic is implemented, it shall be implemented as defined in FC-PH.
Ordered set
A transmission word composed of a special character in its first (leftmost) position and data characters in its remaining positions. An ordered set is represented by the combination of special codes and data bytes which, when encoded, result in the generation of the transmission characters specified for the ordered set.

Originator
The logical function associated with an N_Port responsible for originating an exchange.

Originator Exchange Identifier (OX_ID)
An identifier assigned by an originator to identify an exchange and meaningful only to the originator (see Responder Exchange Identifier).

ORL
Optical return loss.

OVC_ID
Originator VC_ID.

OX_ID
Originator_Exchange_Identifier.

P_BSY
N_Port Busy.

Payload
Contents of the data field of a frame, excluding optional headers and fill bytes, if present.

PDISC
Discover N_Port Service parameters.

Plug
The cable half of the interface connector which terminates an optical or electrical signal transmission cable.

Port
A generic reference to an N_Port or F_Port.

Port_Name
A Name_Identifier associated with a port.

Power on state
In this state, any circuits or optical devices respond to controls resulting from higher levels.

ppm
Parts per million.

Preferred Address
On FC-AL, the AL_PA which an NL_Port attempts to acquire first during loop initialization. Following power-on reset, the preferred address of a private NL_Port is its hard address (if any). Following receipt of a LIP other than LIP(AL_PD,AL_PS), the preferred address of a private NL_Port is its previously acquired address. Fabric-assigned or soft addresses are not considered to be preferred.

Previously Acquired Address
This address only has meaning during loop initialization. During initialization, it is the AL_PA which was in use prior to receipt of LIP. After the time a loop initialization completes and the next one begins, an NL_Port has no previously acquired address.

Primitive Sequence
An ordered set transmitted repeatedly and continuously until a specified response is received.
**Primitive Signal**
An ordered set designated to have a special meaning such as an Idle or Receiver_Ready (R_RDY).

**Private loop device**
A device with only private NL_Ports.

**Private NL_Port**
An NL_Port which is observing the rules of private loop behavior.

**PRLI**
Process Login.

**PRLO**
Process Logout.

**Process policy**
An error handling policy where an N_Port is able to continue processing Data frames received following detection of one or more missing frames in a sequence.

**Process_Associator**
A value used in the Association_Header to identify a process or a group of processes within a Node. Process_Associator is the mechanism by which a process is addressed by another communicating process. Process_Associator is a generic reference to Originator Process_Associator and Responder Process_Associator (see Operation_Associator).

**Profile**
An interoperability specification that provides implementation guidelines for systems manufacturers, system integrators, component manufacturers, and users seeking to design and select interoperable Fibre Channel peripherals, hosts, and components. A Profile specifies particular settings for various Fibre Channel physical, link-level, and upper-level protocol options to enhance interoperability.

**Public loop device**
A device with at least one public NL_Port.

**Public NL_Port**
An NL_Port which can observe the rules of either public or private loop behavior. A public NL_Port may have open Exchanges with both private and public NL_Ports concurrently.

**QoSF**
Quality of Service Facilitator.

**QoSR**
Quality of Service Request.

**R_CTL**
Routing Control.

**R_A_TOV**
Resource_Allocation_Timeout value.

**R_RDY**
Receiver_Ready.

**R_T_TOV**
Receiver_Transmitter_Timeout Value.
Random Relative Offset
The relationship specified between Relative Offset values contained in frame (n) and frame (n+1) of an Information Category within a single Sequence. For a given Information Category i within a single Sequence, Random Relative Offset (RO[i]) value for a frame (n+1) is unrelated to that of the previous frame (n). (see Initial Relative Offset and Continuously Increasing Relative Offset).

RCS
Read Connection Status.

Receiver
1. The portion of a Link_Control_Facility dedicated to receiving an encoded bit stream from a fibre, converting this bit stream into Transmission Characters, and Decoding these characters using the rules specified by FC-PH.
2. An electronic circuit (Rx) that converts a signal from the media (optical or electrical) to an electrical retimed (or nonretimed) serial logic signal.

Receiver overload
The condition of exceeding the maximum acceptable value of the received average optical power at point g_R to achieve a BER <10^{-12}.

Note. (See FC-PI-2, Section 5.9, Interoperability points, Figure 10, where g_R = Bulkhead Receiver Connector).

Receiver sensitivity
The minimum acceptable value of average received signal at point g_R to achieve a BER <10^{-12}. It takes into account power penalties caused by use of a transmitter with a worst-case output. In the case of an optical path, it does not include power penalties associated with dispersion, jitter, effects related to the modal structure of the source or reflections from the optical path. These effects are specified separately in the allocation of maximum optical path penalty.

Note. (See FC-PI-2, Section 5.9, Interoperability points, Figure 10, where g_R = Bulkhead Receiver Connector).

Receptacle
The fixed or stationary female half of the interface connector which is part of the transmitter or receiver.

Reflections
Power returned to point S of figure x by discontinuities in the physical link.

Relative Offset (Offset)
The displacement, expressed in bytes, of the first byte of a Payload related to an upper level defined-origin for a given Information Category (see Continuously Increasing and Random Relative Offset).

Relative Offset space
A virtual address space defined by the sending upper level for a single information category. The address space starts from zero, representing the upper level defined-origin, and extends to its highest value.

Remote F_Port
The F_Port to which the other communicating N_Port is directly attached (see local F_Port).

Repeating ordered set
An ordered set which, when issued by FC-2 to FC-1 for transmission, is to be repetitively transmitted until a subsequent transmission request is issued by FC-2.

REQCS
Request Clock Synchronization.

Request byte count
Number of bytes to be moved by a data delivery service request.
RES
Read Exchange Status Block.

RESB
Responder Exchange Status Block.

reserved
A field which is filled with binary zeros by the source N_Port and is ignored by the destination N_Port. **Note:** Future enhancements to FC-PH may define usages for reserved fields. The reserved fields should not be checked or interpreted. Any violation of this guideline may result in loss of upward compatibility with future implementations which comply with future enhancements to FC-PH.

Responder
The logical function in an N_Port responsible for supporting the Exchange initiated by the Originator in another N_Port.

Responder Exchange_Identifier (RX_ID)
An identifier assigned by a Responder to identify an Exchange and meaningful only to the Responder.

RFI
Radio Frequency Interference.

RIIN
reflection induced intensity noise.

RJ
Random jitter.

RJT
Reject.

RIN
Relative intensity noise.

RMC
Remove connection.

RMS
Root mean square.

RNC
Report node capability.

RO
Relative offset.

RSS
Read sequence status block.

RTV
Read timeout value.

Run length
Number of consecutive identical bits in the transmitted signal, e.g., the pattern 0011111010 has a run length of five (5).

Running disparity
A binary parameter indicating the cumulative disparity (positive or negative) of all previously issued transmission characters.
**RVC_ID**
Responder VC_ID.

**RVCS**
Read virtual circuit status.

**Rx**
Receiver.

**RX_ID**
Responder Exchange Identifier.

**s or sec**
Second(s).

**S/N**
signal-to-noise ratio.

**S_ID**
Source Identifier.

**S_Length**
Security Length.

**S_Type**
Security Type.

**SBCCS**
Single byte command code sets.

**SCN**
State change notification.

**SCR**
Stacked connect request.

**SCSI**
Small Computer System Interface.

**SCSI device**
A device that originates or services SCSI commands.

**SEQ-CNT**
Sequence count.

**SEQ_ID**
Sequence ID.

**Sequence**
A set of one or more data frames with a common Sequence_ID (SEQ_ID), transmitted unidirectionally from one N_Port to another N_Port with a corresponding response, if applicable, transmitted in response to each data frame.

**Sequence initiator**
The N_Port which initiates a sequence and transmits data frames to the destination N_Port.
Sequence recipient
The N_Port which receives data frames from the sequence initiator and, if applicable, transmits responses (Link_Control frames) to the sequence initiator.

Sequence status block
A logical construct which tracks the state of a sequence. Both the sequence initiator and the sequence recipient have a sequence status block for each concurrently active sequence.

Sequence_ID (SEQ_ID)
An identifier used to identify a sequence.

SISB
Sequence initiator status block.

SM
Single mode.

SOF
Start of frame.

Solicited control
One of the information categories indicated in the frame header.

Solicited data
One of the information categories indicated in the frame header.

Source N_Port
The N_Port from which a frame is transmitted.

Source_Identifier (S_ID)
The address identifier used to indicate the source port of the transmitted frame.

Special character
Any transmission character considered valid by the transmission code but not equated to a valid data byte. Special characters are provided by the transmission code for use in denoting special functions.

Special code
A code which, when encoded using the rules specified by the transmission code, results in a special character. Special codes are typically associated with control signals related to protocol management (e.g., K28.5).

Spectral width
1. FWHM (Full Width Half Maximum) The absolute difference between the wavelengths at which the spectral radiant intensity is 50 percent of the maximum power. This form is typically used for LED optical sources.
2. RMS The weighted root mean square width of the optical spectrum. See FOTP-127. This form is typically used for laser optical sources.

SRSB
Sequence recipient status block.

SSB
Sequence status block.

Status
A single byte returned by the device server to the application client in its response to indicate the completion and completion state of a command.
**STP**
Shielded twisted pair.

**Streamed sequence**
A new Class 1 or Class 2 sequence initiated before receiving the final acknowledgement for the previous sequence in the same exchange. Any new Class 3 sequence initiated before the expiration of R_A_TOV for all data frames in the previous sequence.

**Subblock**
A upper level construct which contains partial application data for a single information category (see block). A collection of subblocks for a given information category may be specified for transfer within a single sequence.

**SW**
Short wavelength.

**Synchronization**
Receiver identification of a transmission word boundary.

**Tag**
The initiator-specified component of the task identifier.

**Target**
A SCSI device that receives SCSI commands and directs such commands to one or more logical units for execution.

**Target identifier**
Address of up to 64 bits by which a target is identified.

**Task**
An object within the logical unit representing the work associated with a command or group of linked commands.

**Task attribute**
The queuing specification for a task (Simple, Ordered, Head of Queue, ACA).

**Task identifier**
The information uniquely identifying a task.

**Task management function**
A peer-to-peer confirmed service provided by a task manager that can be invoked by an application client to affect the execution of one or more tasks.

**TP**
Twisted pair.

**TPLS**
Test process login status.

**Transceiver**
A transmitter and receiver combined in one package.

**Transmission character**
Any encoded character (valid or invalid) transmitted across a physical interface specified by FC-0. Valid transmission characters are specified by the transmission code and include data and special characters.

**Transmission code**
A means of encoding data to enhance its transmission characteristics. The transmission code specified by FC-PH is byte-oriented, with:
1. Valid data bytes; and
2. Special codes encoded into 10-bit transmission characters.

**Transmission word**
A string of four contiguous transmission characters occurring on boundaries that are zero modulo 4 from a previously received or transmitted special character.

**Transmitter**
1. The portion of a Link_Control_Facility dedicated to converting valid data bytes and special codes into transmission characters using the rules specified by the transmission code, converting these transmission characters into a bit stream, and transmitting this bit stream onto the transmission medium (optical or electrical).
2. An electronic circuit (Tx) that converts an electrical logic signal to a signal suitable for the communications media (optical or electrical).

**Tx**
Transmitter.

**TYPE**
Data structure type.

**UI**
Unit interval == 1 bit period.

**ULP**
Upper layer protocol.

**ULP**
Upper Level Protocol.

**ULP process**
A function executing within an FC node which conforms to Upper Layer Protocol (ULP) defined protocols when interacting with ULP processes residing in other FC nodes.

**Uncategorized information category**
One of the information categories indicated in the frame header.

**Unrecognized ordered set**
A transmission word containing a K28.5 in its first (leftmost) position but not defined to have meaning by FC-PH.

**Unsolicited control**
One of the information categories indicated in the frame header.

**Unsolicited data**
One of the information categories indicated in the frame header.

**Upper level**
A level above FC-2.

**Upper Level Protocol (ULP)**
The protocol user of FC-4.

**Valid**
A validity control bit indicates if a field is valid, in which case, the value in the field shall be treated as valid. If a validity control bit indicates that a field is invalid, the value in the field shall be treated as invalid (see meaningful).

**Valid data byte**
A string of eight contiguous bits within FC-1 which represents a value with 0 to 255, inclusive.
Valid frame
A frame received with a valid Start_of_Frame (SOF), a valid End_of_Frame (EOF), valid data characters, and proper cyclic redundancy check (CRC) of the frame header and data field.

VC
Virtual circuit.

VC_Credit
Virtual circuit credit.

VC_ID
Virtual circuit identifier.

VC_RDY
Virtual circuit ready.

Vendor unique
Functions, code values, and bits not defined by FC-PH and set aside for private usage between parties using FC-PH. Caution: different implementations of FC-PH may assign different meanings to these functions, code values, and bits.

Virtual circuit (VC)
A unidirectional path between two communicating N_Ports that permits Class 4 service to be used. Two virtual circuits are required to form a Class 4 circuit.

Virtual Circuit Credit (VC_Credit)
The number of receiver buffers allocated to a virtual circuit by an F_Port. It represents the maximum number of frames that an N_Port may transmit without causing a buffer overrun condition at the F_Port receiver.

Virtual circuit credit limit
The maximum number of VC_Credits available for a virtual circuit. It represents the maximum number of VC_Credits held by an N_Port on a given virtual circuit.

Virtual Circuit Identifier (VC_ID)
An identifier associated with either the originator (OVC_ID) or responder (RVC_ID) for a virtual circuit.

Virtual path
A fixed route through a fabric in support of a virtual circuit.

WAN
Wide area network.

Well-known addresses
A set of address identifiers defined in FC-PH to access global server functions such as a name server.

Word
A string of four contiguous bytes occurring on boundaries that are zero modulo 4 from a specified reference.

Worldwide_Name
A Name_Identifier which is worldwide unique, and represented by a 64-bit unsigned binary value.

WWN
Worldwide name.

X_ID
Exchange_Identifier.
## Index

### Numerics

<table>
<thead>
<tr>
<th>Numerical Reference</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>10-bit character</td>
<td>23</td>
</tr>
<tr>
<td>8045 ESI pinouts</td>
<td>115</td>
</tr>
<tr>
<td>8045 mode</td>
<td>115</td>
</tr>
<tr>
<td>8067 ESI command</td>
<td>116</td>
</tr>
<tr>
<td>8067 ESI interface pinouts</td>
<td>117</td>
</tr>
<tr>
<td>8067 information format</td>
<td>117</td>
</tr>
<tr>
<td>8067 mode</td>
<td>116</td>
</tr>
</tbody>
</table>

### A

- **Abort Sequence** 66, 67
- **Abort Task Set**
  - FCP CMND Payload 140, 142
- **Aborted Command**
  - disc drive sense keys 166
- **ABTS. See Abort Sequence**
- **ACA. See Auto Contingent Allegiance**
- **access fairness algorithm** 60, 61
- **access unfairness** 61
- **access window** 60
- **ACK. See Acknowledge**
- **Acknowledge_0 Capable**
  - Class Service Parameters 76
  - Initiator Control fields 77
  - Recipient Control fields 77
- **Acknowledge_N Capable**
  - Class Service Parameters 76
  - Initiator Control fields 77
  - Recipient Control fields 77
- **Action Code**
  - Device Address page 125
  - Device Identification page 127, 134
  - Device Standard Inquiry Data 124
  - Device Temperature page 128
  - Enclosure Initiated ESI Page Format 124
  - Enclosure Request 122
  - ESI Data Validation Accept 135
  - ESI data validation accept 135
  - Link Status page 131, 132
  - Loop Position Map page 126
  - Port Parameters page 129
  - Spin-Down Control Status page 134
- **Action Specific**
  - Enclosure Request 122, 123
- **Actual Retry Count**
  - Actual Retry Count bytes 164, 165
- **Actual Retry Count bytes**
  - Sense Key Specific 164
- **Additional Sense Code**
  - Extended Sense Data 162, 163
- **Additional Sense Code Qualifier**
  - Extended Sense Data 162, 163
- **Additional Sense Length**
  - Extended Sense Data 162, 163
- **ADISC. See Discover Address**
- **AL_PA** 63
- **AL_PA. See Arbitrated Loop Physical Address**
- **AL_PD** 60
- **ALLOCATION LENGTH**
  - CDB 12-byte 146
  - CDB 16-byte 147
  - CDB for long LBA 32-byte 148
- **Allocation Length**
  - CDB 149
- **Alternate Credit Model**
  - F_Port Common Service Parameters 83, 84
  - N_Port Common Service Parameters 74, 75
- **American National Standards Institute** 3
- **ANSI FC_PH. See ANSI Fibre Channel Physical Interface**
- **ANSI Fibre Channel Physical Interface Version**
  - F_Port Common Service Parameters 83
  - N_Port Common Service Parameters 74, 75, 83
- **ANSI standards** 5
- **ARB(F0)** 55, 60
- **Arbitrated Loop Physical Address** 45, 60, 61
- **Arbitrated Loop topology** 21
- **arbitration** 60
- **arbitration primitive** 59
- **ARBx** 55, 59
- **assigned AL_PA values** 63
- **Association Header**
  - RRQ Payload 102
- **asynchronous transmission** 4
- **Auto Contingent Allegiance Queue**
  - FCP CMND Payload 141

### B

- **BA_ACC. See Basic Accept**
- **BA_RJT. See Basic Reject**
- **background mode** 170
- **Basic Accept** 66, 68
- **Basic Accept Payload** 68
- **basic link services** 66
- **Basic Reject** 66, 69
- **basic services replies** 66
- **BB_Credit. See buffer to buffer credit**
- **Bit Pointer**
  - Field Pointer bytes 164
- **Bit Pointer Valid**
  - Field Pointer bytes 164
- **blocking environment** 62
- **blocking switch emulation** 62
- **BPV. See Bit Pointer Valid**
- **Broadcast**
  - F_Port Common Service Parameters 83, 84
- **buffer to buffer**
  - credit 42
  - transfer 24
- **Buffer to Buffer Credit**
**F_Port Common Service Parameters** 83, 84  
**N_Port Common Service Parameters** 74, 75

Buffered Class 1  
Class Service Parameters 84  
Service Option Class 3 fields 85

Burst Length  
FCP XFER RDY Payload 151, 152

**C**

C/D. See Command Data  
cable length 4

Camp-on  
Class Service Parameters 84  
Service Option Class 3 fields 85

Capability Entries  
RNC Payload 106

Categories per Sequence  
Class Service Parameters 76  
Recipient Control fields 77

CDB. See Command Descriptor Block  
channel level error recovery 65  
channels 4

Class 3 delimiters 27  
class of service 43

Class Service Parameters 76, 84

Class Valid  
Class Service Parameters 84  
Class Service Parameters 76  
Service Option Class 3 fields 85  
Service Option fields 77

Clear ACA  
FCP CMND Payload 140, 142

Clear Task Set  
FCP CMND Payload 140, 142

Clock skew management 61

CLS 56

Command Code  
RFT_ID Payload 111  
command code 109, 144

Command Data  
Field Pointer bytes 164

Command Descriptor Block 144  
FCP CMND Payload 140, 142

Command Specific Data  
Extended Sense Data 162, 163

Command/Data Mixed  
PRLI Accept Payload 89, 90  
PRLI Payload 87, 88

Common Features  
F_Port Common Service Parameters 84  
N_Port Common Service Parameters 75  
common transport header 109

Company Identifier Assigned by IEEE  
Node/Port Name format 73

Concurrent Sequences  
Class Service Parameters 76, 78

Continuous Increase SEQ_CNT

**Continuously Increasing Offset**  
F_Port Common Service Parameters 83, 84  
N_Port Common Service Parameters 74, 75

**Control Byte**  
CDB 149  
CDB six-byte 144  
CDB ten-byte 145, 146

control operations 137

CRC. See Cyclic Redundancy Check  
credit 42

current and deferred errors 166

Cyclic Redundancy Check 39

**D**

D_ID. See Destination Identifier  
Data  
Enclosure Initiated ESI Page Format 124  
data character 25  
data encoding 23  
data field 39

Data Field Control  
  basic link services header 66, 67  
  common transport header 109  
  extended link services header 71, 110  
  FCP CMND header 138, 139  
  FCP DATA frame header 153, 154  
  FCP RSP header 157  
  FCP XFER RDY header 150, 151  
  Frame header format 39

Data Length  
FCP CMND Payload 140

Data Overlay Allow  
PRLI Accept Payload 89, 90  
PRLI Payload 87, 88

Data Protect  
disc drive sense keys 166

Data Structure Type  
Frame header format 37

Data/Response Allowed  
PRLI Accept Payload 90

Data/Response Mix  
PRLI Accept Payload 89  
PRLI Payload 87, 88

decoding 23

Dedicated Simplex  
Class Service Parameters 84  
F_Port Common Service Parameters 83, 84  
Service Option Class 3 fields 85

default self-test 169

defered errors 166

destination device 60

Destination Identifier 54  
  basic link services header 66  
  common transport header 109  
  extended link services header 70, 71, 109
Fibre Channel Interface Manual, Rev. A  

FCP CMND header 138, 139  
FCP DATA frame header 153, 154  
FCP RSP header 156, 157  
FCP XFER RDY header 150, 151  
Frame header format 36  

Device ID Data  
Device Identification page 127  
Device Identification page 127  
device self-test mode 170  
DF_CTL. See Data Field Control  
Disc Drive Extended Sense Data Summary 162  
Discover Address 103  
Discover Address Accept Payload 105  
Discover Address Payload 104  
discovery process 113  
disparity 45  
DL. See Data Length  
Document Identifier  
Capability Entries 106  
Drive Capabilities  
Port Parameters page 129  
drive features 169  
Dynamic Half Duplex  
F_Port Common Service Parameters 83, 84  
N_Port Common Service Parameters 74, 75  

E  
E_D_TOV. See Error Detect Timeout Value  
EDV. See ESI Data Validation  
EE_Credit. See End-to-end Credit  
Enable SpinDn Ctrl  
Spin-Down Control Status page 133  
Enable Spin-Down  
Spin-Down Control Status page 133  
enclosure request 134  
enclosure requested information 122  
enlosure services interface 113  
command format 116  
Enclosure Services Interface Page  
Device Address page 125  
Device Identification page 127, 134  
Device Standard Inquiry Data 124  
Device Temperature page 128  
Enclosure Initiated ESI Page Format 124  
Link Status page 131, 132, 134, 135  
Loop Position Map page 126  
Port Parameters page 129  
Encoding and decoding 23  
End of Medium  
Extended Sense Data 162, 163  
End-of-frame (EOF) delimiter 26, 40  
End-of-frame Normal 28  
End-of-frame primitive 27  
End-of-frame Terminate 28  
End-to-end Credit 42  
End-to-end credit 42  
EOF. See End-of-frame  

EOM. See End of Medium  
Equal  
disc drive sense keys 166  
equal access 60  
Error Code  
Extended Sense Data 162, 163  
Error Detect Timeout Value 60  
N_Port Common Service Parameters 74, 75  
timer 56  
Error Detect Timeout Value Resolution  
F_Port Common Service Parameters 83, 84  
N_Port Common Service Parameters 74, 75  
Error Policy  
Class Service Parameters 76  
Recipient Control fields 77  
error recovery 65  
ESI command 134  
ESI data validation 134  
ESI data validation accept 135  
ESI Page  
ESI data validation accept 135  
ESI transfer phase 134  
ESI. See enclosure services interface  
Establish Image Pair  
PRLI Accept Payload 89, 90  
PRLI Payload 87, 88  
exchange 41  
Exchange Identifier Interlock  
Class Service Parameters 76  
Recipient Control fields 77  
Exchange Identifier Reassignment  
Class Service Parameters 76  
Initiator Control fields 77  
Explanation Code  
RFT_ID Payload 111, 112  
tended link services 70, 108  
tended link services header 70  
tended self-test 169  
Extended Sense Data Format 162  

F  
F_CTL. See Frame Control  
F_Port. See Fabric Port  
Fabric 20  
Fabric Login 82  
Fabric Login Accept Payload 85  
Fabric Login Payload 82  
Fabric Port  
F_Port Common Service Parameters 83, 84  
N_Port Common Service Parameters 74, 75  
Fabric Port Common Service Parameters  
FLOGI ACC Payload 85  
fabric topologies 61  
fair NL_Port 60, 61  
FC common transport 109  
FC Implementation Guide 7
FC Service Sub Type
   RFT_ID Payload 111
FC Services Sub Type
   RFT_ID Payload 111
FC Services Type Code
   RFT_ID Payload 111
FC-0 6
FC-1 6
FC-1.5 6
FC-2 6
FC-3 6
FC-4 7
FC-AL. See Fibre Channel Arbitrated Loop
FC-CT Revision
   RFT_ID Payload 111
FCP CMND. See Fibre Channel Protocol Command
FCP DATA. See Fibre Channel Protocol Data
FCP RSP. See Fibre Channel Protocol Response
FCP XFER RDY. See Fibre Channel Protocol
   Transfer Ready
Fibre Channel Arbitrated Loop
   Direct Attach SCSI Technical Report 5
   primitive signals 28
Fibre Channel Fabric Loop Attach Technical Report 5
Fibre Channel levels 6
Fibre Channel link services 65
Fibre Channel Physical and Signaling 5
Fibre Channel Protocol 137
   Command Header 138
   Command Payload 140
   Data frame header 153
   Response header 156
   Response Payload 158
   Transfer Ready header 150
   Transfer Ready Payload 151
Fibre Channel Protocol Command 137, 138
Fibre Channel Protocol Data 149, 153
Fibre Channel Protocol Response 137, 156
Fibre Channel Protocol Transfer Ready 150
Field Pointer
   Field Pointer bytes 164
Field Pointer bytes
   Sense Key Specific 164
Filemark
   Extended Sense Data 162, 163
Firmware Error
   disc drive sense keys 166
Fixed length CDBs
   CDB for 12-byte commands 146
   CDB for 16-byte commands 147
Flag
   CDB Control Byte 149
Flags
   Capability Entries 106
FLOGI. See Fabric Login
   foreground mode 170
Format Indication bytes
   Sense Key Specific 165
Frame Control
   basic link services header 66, 67
   bit description 154
   common transport header 109
   extended link services header 70, 71, 110
   FCP CMND header 138, 139
   FCP DATA frame header 153, 154
   FCP RSP header 156, 157
   FCP XFER RDY header 150, 151
   Frame header format 38
frame delimiters 26
frame header 34
frame sequence 41
frame structure 33
frames 33
G
Global Process Logout
   TPRLO Accept Payload 97
   TPRLO Payload 95, 96, 98
Group Code 144
H
Hard Address of Originator
   ADISC Payload 104
Hard Address of Responder
   ADISC Accept Payload 105
Hardware Error 164
   disc drive sense keys 165
Head of Queue
   FCP CMND Payload 141
Hunt Groups
   F_Port Common Service Parameters 83, 84
I
Idle 26, 60
ILI. See Incorrect Length Indicator
Illegal Request 164
   disc drive sense keys 166
Implementation Guide 7
Incorrect Length Indicator
   Extended Sense Data 162, 163
Information Bytes
   Extended Sense Data 162, 163
initial AL_PA 54
Initial Process Associator
   Class Service Parameters 76
   Initiator Control fields 77
Initiator Control
   Class Service Parameters 76, 77
   Initiator Control fields
   Class Service Parameters 77
Initiator Function
   PRLI Accept Payload 89, 90
Fibre Channel Interface Manual, Rev. A

PRLI Payload 87, 88
Inquiry Data
Device Standard Inquiry Data 124, 125

Intermix Mode
Class 3 Service Parameters 84
Class Service Parameters 76
Service Option Class 3 fields 85
Service Option fields 77

Invalid CRC Count. See Invalid Cyclic Redundancy Check Count

Invalid Cyclic Redundancy Check Count
Link Status page 131, 132, 133
RLS Accept Payload 100, 101

Invalid Transmission Word
Link Status page 131, 132, 133

L_Port 61
Length of Response Information
FCP RSP Payload 159
Length of Sense Information
FCP RSP Payload 160

LESB. See Link Error Status Block
levels
Fibre Channel 6
LIFA 55, 56
LIHA 55, 56
LILP 56, 57
Link
CDB Control Byte 149, 150
link 20, 33
Link Error Status Block 99, 100

Link Failure Count
Link Status page 131, 132
RLS Accept Payload 100

link service frames 65
Link Service Reject 108
Link Service Reject Payload 108

Link Services Command Code 70
ADISC Payload 104
FLOGI ACC Payload 85
FLOGI Payload 82
LOGO Accept 81
LOGO Payload 81, 82, 85
LS_RST Payload 108
PLOGI ACC Payload 79
PLOGI Payload 72
PRLI Accept Payload 89
PRLI Payload 87
PRLO Accept Payload 93
PRLO Payload 91
RLS Accept Payload 100
RLS Payload 99
RNC Payload 106
RRQ Accept Payload 103
RRQ Payload 102

TPRLO Accept Payload 98
TPRLO Payload 95

Link Status page 131
Link Status Page Change
Port Parameters page 129

Link_Reset 30
Link_Reset_Response 30

LIP 50
LIP F7 Initiated Count
Link Status page 131, 132, 133
LIP F7 Received Count
Link Status page 131, 132
LIP F8 Initiated Count
Link Status page 131, 132
LIP F8 Received Count
Link Status page 131, 132

LIP Loop A/B
Initiate LIP Action Specific Bits 123, 133

LIPA 55, 56
LIRP 56, 57
LISA 55, 56
LISM 48, 54
Logical Block Address 145, 148
CDB 148
CDB six-byte 144
CDB ten-byte 145

Logical Unit Number
FCP CMND Payload 140
LOGO. See Port Logout
Logout Payload 81
loop initialization 50, 58
loop failure, no valid AL_PA 31
loop failure, valid AL_PA 31
no valid AL_PA 31
reset 31
reset all 31
sequences 49, 55
state machine 58
steps 63
valid AL_PA 31

Loop Initialization Primitive (LIP) sequences 50
Loop Map Port x
Loop Position Map page 126, 127
loop port 61
bypass 32
bypass all 32
enable 32
enable all 32

Loop Position Map page 126
loop protocol 63
loop reinitialization 59

Loss of Signal Count
Link Status page 131, 132
RLS Accept Payload 100, 102

Loss of Synchronization Count
Link Status page 131, 132
RLS Accept Payload 100, 101
Low Revision
Capability Entries 106
LS Command Code. See Link Services Command Code
LS_RJT. See Link Service Reject
LUN. See Logical Unit Number

M
Medium Error 164
disc drive sense keys 165
meshed environment 62
Miscompare
disc drive sense keys 166
Mode Sense command
CDB 148
monitoring state 59
Multicast
F_Port Common Service Parameters 83, 84

N
N_Port. See Node Port
NACA. See Normal Auto Contingent Allegiance
Network Address ID
Node/Port Name format 73
neutral disparity 45
NL_Ports 61
No Sense
disc drive sense keys 165
Node Name
Device Address page 125, 126
FLOGI ACC Payload 85
FLOGI Payload 82
PLOGI ACC Payload 79
PLOGI Payload 72, 82
Node Name of Originator
ADISC Payload 104
Node Name of Responder
ADISC Accept Payload 105
Node Port Common Service Parameters
FLOGI Payload 82
PLOGI ACC Payload 79
PLOGI Payload 72, 82
Node Port End to End Credit
Class Service Parameters 76, 78
Node Port ID of Originator
ADISC Payload 104
Node Port ID of Responder
ADISC Accept Payload 105
Node Port Identifier
LOGO Payload 81
Node/Port Name format 73
Node/Port Name format 73
nodes 33
non-meshed environment 62
Normal Auto Contingent Allegiance
CDB Control Byte 149
Not Ready 164
disc drive sense keys 165
Not_Operational 30

O
Offline 30
Offset
Frame header format 39
Offset Port x
Loop Position Map page 126
Open Sequences per Exchange
Class Service Parameters 76, 78
operating parameters 65
OPERATION CODE
CDB 12-byte 146
CDB 16-byte 147
CDB 32-byte Long LBA 148
Operation Code 144
CDB six-byte 144, 145, 146, 149
CDB ten-byte 145
format for CDB 144
OPNyx 60
Options
RFT_ID Payload 111
Ordered Queue
FCP CMND Payload 141
Ordered sets 25
Originator Exchange Identifier
basic link services header 66, 67
common transport header 109
extended link services header 70, 71, 110
FCP CMND header 138, 139
FCP DATA frame header 153, 154
FCP RSP header 156, 157
FCP XFER RDY header 150, 151
Frame header format 39
RRQ Payload 102
Originator Exchange Identifier, Aborted
BA_ACC Payload 68
Originator Process Associator
PRLI Accept Payload 89, 90
PRLI Payload 87, 88
PRLO Accept Payload 93
PRLO Payload 91
Originator Source Identifier
RRQ Payload 102
OX_ID. See Originator Exchange Identifier

P
Page Code
Enclosure Request 122
ESI command format 116
Page Length
Device Address page 125, 126
Device Identification page 127, 133, 134
Device Standard Inquiry Data 124
Device Temperature page 128, 129
Process Logout 91
Process Logout Accept Payload 93
Process Logout Payload 91
Progress Indication
  Format Indication bytes 165
protocol 6

R
R_CTL. See Routing Control
Random Relative Offset
  F_Port Common Service Parameters 83, 84
  N_Port Common Service Parameters 74, 75
RD XFR RDY. See Read Transfer Ready
Read Capacity command 148
Read Data
  FCP CMND Payload 142
Read Link Error Status Block 99
Read Link Error Status Block Accept 100
Read Link Error Status Block Accept Payload 100
Read Link Error Status Block Payload 99
Read Transfer Ready Disable
  PRLI Accept Payload 89, 90
  PRLI Payload 87, 88
Reason Code
  BA_RJT Payload 69
  LS_RJT Payload 108
  RFT_ID Payload 111, 112
Reason Explanation
  BA_RJT Payload 69
  LS_RJT Payload 108
Receive Data Field Size
  Class Service Parameters 76, 77
  F_Port Common Service Parameters 83
  N_Port Common Service Parameters 74, 75, 84
Receiver ready 26
Recipient Control
  Class Service Parameters 76, 77
Recipient Control fields
  Class Service Parameters 77
Recovered Error 164
disc drive sense keys 165
Register FC-4 Types Name Service 111
Register FC-4 Types Name Service Payload 111
reinitialization 59
Reinstate Recovery Qualifier 102
Reinstate Recovery Qualifier Accept Payload 103
Reinstate Recovery Qualifier Payload 102
RelAdr. See Relative Address
Relative Address
  CDB 149
cDB ten-byte 145, 146
Relative Offset
  FCP DATA frame header 153, 155
  FCP XFER RDY Payload 151, 153
Relative Offset by Info Category
  N_Port Common Service Parameters 74, 75
Report Node Capabilities 106
Report Node Capabilities Flags
  RNC Payload 106
Report Node Capabilities Payload 106
Residual Count
  FCP RSP Payload 160
Residual Over Run
  FCP RSP Payload 158, 159
Residual Under Run
  FCP RSP Payload 158, 159
Responder Exchange Identifier
  Frame header format 39
Responder Identifier
  basic link services header 66, 67
  common transport header 109
  extended link services header 70, 71, 110
  FCP CMND header 138, 139
  FCP DATA frame header 153, 155
  FCP RSP header 157
  FCP XFER RDY header 151
  RRQ Payload 102
Responder Identifier, Aborted
  BA_ACC Payload 68
Responder Process Associator
  PRLI Accept Payload 89, 90
  PRLI Payload 87, 88
PRLO Accept Payload 93
PRLO Payload 91
Response Code
  PRLI Accept Payload 89, 90
  PRLO Accept Payload 93, 94
  TPRLO Accept Payload 98
Response Information
  FCP RSP Payload 158, 160
Response Information format
  FCP RSP Payload 161
Response Length Valid
  FCP RSP Payload 158, 159
RFT_ID. See Register FC-4 Types Name Service
RLS. See Read Link Error Status Block
RNC. See Report Node Capabilities
RO. See Relative Offset
rounding 167
Routing Control
  basic link services header 66
  common transport header 109
  extended link services header 70, 109
  FCP CMND header 138, 139
  FCP DATA frame header 153, 154
  FCP RSP header 156
  FCP XFER RDY header 150
  Frame header format 35
RRQ. See Reinstate Recovery Qualifier
RSP Length Valid. See Response Length Valid
running disparity 23, 45
RX_ID. See Responder Identifier
**S**

S_ID. See Source Identifier  
SCSI Fibre Channel Protocol 5  
SCSI operations 137  
SCSI Parallel Interface-3 (SPI-3) 5  
SCSI Sense Information  
FCP RSP Payload 158, 161  
SCSI Status  
FCP RSP Payload 158, 162  
SCSI-FCP. See Fibre Channel Protocol  
Segment Number  
Extended Sense Data 162, 163  
Self-Monitoring Analysis and Reporting Technology 169  
sen-test  
default 169  
extended 169  
short 169  
Send  
ESI command format 116  
Send Diagnostic Parameter Length  
ESI command format 116  
Sense Key  
Extended Sense Data 162, 163  
Sense Key Specific 164  
Extended Sense Data 162, 163  
Sense Key Specific Valid 164  
Extended Sense Data 162, 163  
Field Pointer bytes 164, 165  
Format Indication bytes 165  
Sense Length Valid  
FCP RSP Payload 158, 162  
SEQ_ID. See Sequence Identifier  
Sequence Count  
basic link services header 66, 67  
common transport header 109  
extended link services header 70, 71, 110  
FCP CMND header 139  
FCP DATA frame header 153, 154  
FCP RSP header 157  
FCP XFER RDY header 150, 151  
Frame header format 36  
Sequence Count, Highest  
BA_ACC Payload 68  
Sequence Count, Lowest  
BA_ACC Payload 68  
Sequence Delivery  
Class 3 Service Parameters 84  
Class Service Parameters 76  
Service Option Class 3 fields 85  
Service Option fields 77  
Sequence Identifier  
basic link services header 66, 67  
common transport header 109  
extended link services header 70, 71, 110  
FCP CMND header 138, 139  
FCP DATA frame header 153, 154  
FCP RSP header 156, 157  
FCP XFER RDY header 150, 151  
Frame header format 38  
Sequence Identifier, Last  
BA_ACC Payload 68  
Sequence Identifier, Valid  
BA_ACC Payload 68  
serial transmission 23  
serialization process 24  
Service Option fields  
Class Service Parameters 85  
Service Options  
Class Service Parameters 76, 78  
Class3 Service Parameters 84  
Service Parameters  
PRLI Accept Payload 89  
PRLI Payload 87  
Service Parameters, Class 1, 2, 3  
FLOGI ACC Payload 85, 86  
PLOGI Payload 82  
PLOGI ACC Payload 80  
PLOGI Payload 72, 73  
short self-test 169  
Simple Queue  
FCP CMND Payload 141  
skew management 61  
SKSV. See Sense Key Specific Valid  
SOF. See Start-of-frame  
Source Identifier 54  
basic link services header 66  
extended link services header 71, 110  
FCP CMND header 138, 139  
FCP DATA frame header 153, 154  
FCP RSP header 156, 157  
FCP XFER RDY header 150, 151  
Frame header format 36  
Source Identifier of Requesting N_Port  
RFT_ID Payload 111, 112  
special characters 25  
Spin-Down Control Status 134  
Stacked Connect Request  
Class 3 Service Parameters 84  
Class Service Parameters 76  
Service Option Class 3 fields 85  
Service Option fields 77  
standards 5  
Start-of-frame (SOF) delimiter 26, 34  
Start-of-frame Initialize Loop 28  
Start-of-frame Initiate Class 3 28  
Start-of-frame Normal Class 3 28  
support services 173  

**T**

Target Function  
PRLI Accept Payload 89, 90
PRLI Payload 87, 88
Target Reset
  FCP CMND Payload 142
Task Attribute
  FCP CMND Payload 140, 142
Task Management functions 137
Task Set control 137
technical support services 173
Temperature
  Device Temperature page 128
Term Task. See Terminate Task
Terminate Task
  FCP CMND Payload 140, 142
Third Party Orig N_Port ID Validity
  TPRLO Accept Payload 97
  TPRLO Payload 95
Third Party Orig Proc Assc Valid
  TPRLO Accept Payload 97
  TPRLO Payload 95
Third Party Originator N_Port ID
  TPRLO Accept Payload 97, 98
  TPRLO Payload 95, 96
Third Party Originator N_Port ID Validity
  TPRLO Accept Payload 99
  TPRLO Payload 96, 97, 98
Third Party Originator Process Associator
  TPRLO Accept Payload 97
  TPRLO Payload 95
Third Party Process Logout 95
Third Party Process Logout Accept Payload 97
Third Party Process Logout Payload 95
Third Party Resp Proc Assc Valid
  TPRLO Accept Payload 97
  TPRLO Payload 95
Third Party Responder Process Associator
  TPRLO Accept Payload 97
  TPRLO Payload 95
topologies 19
topology standards 7
Total Concurrent Sequences
  N_Port Common Service Parameters 74, 75
TPRLO. See Third Party Process Logout
Transfer Length
  CDB 149
  CDB six-byte 144, 145
  CDB ten-byte 145, 146
transmission word 25, 45, 59
Type 39
  basic link services header 66, 67
  common transport header 109
  extended link services header 70, 72, 110
  FCP CMND header 138, 139
  FCP DATA frame header 153, 154
  FCP RSP header 157
  FCP XFER RDY header 150, 151
Type Code
  PRLI Accept Payload 89
  PRLI Payload 87
PRLO Payload 91, 95
TPRLO Payload 95
Type Code Extension
  PRLI Accept Payload 89, 90
  PRLI Payload 87, 88
  PRLO Payload 91, 92, 95
  TPRLO Payload 96
Typical CDB
  six-byte 116, 144
  ten-byte 145

U
unfair NL_Port 61
Unique Drive Identifier
  Node/Port Name format 73
Unit Attention
disc drive sense keys 166
Untagged Queue
  FCP CMND Payload 141

V
valid addresses 45
valid arbitrated loop physical address 61
Valid Vendor Version
  F_Port Common Service Parameters 83, 84
  N_Port Common Service Parameters 74, 75
Validity Bit
  Extended Sense Data 162
Variable length CDB formats
  Typical variable length CDB for long LBA 32-byte commands 148
Vendor Identifier
  RNC Payload 106
Vendor Unique
  BA_RJT Payload 69
  LS_RJT Payload 108
  RFT_ID Payload 111, 112
Vendor Version
  FLOGI ACC Payload 85, 86
  FLOGI Payload 82
  PLOGI ACC Payload 80
  PLOGI Payload 72
Volume Overflow
disc drive sense keys 166
VU Information Length
  RNC Payload 106
VU Information Lengths
  RNC Payload 106

W
window
  access 60
WR XFR RDY. See Write Transfer Ready
Write Data
  FCP CMND Payload 142
Write Transfer Ready Disable
X

X_ID. See Exchange Identifier